

# Remarry Syscon

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Replacing the Syscon requires you to remarry it to [CELL](#).

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## Introduction

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The [System Controller](#) has different *EEPROM* regions of which two are the NVS (non-volatile storage) and the SNVS (secure NVS).

If CELL wants to access the SNVS it has to be married to the Syscon. The SNVS holds for example the secure [product mode](#) information, the [lv0/lv1](#) hash and the [SRK/SRH](#).

This guide will only cover the perconsole section which holds the pairing information and the SNVS section, not the NVS nor the internal (only by Syscon plain accessible) regions.

For easier reading the complete area will be called **SPCR** - Syscon Per Console Region.

## Basic Syscon Prerequisites

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In order to remarry the Syscon you have to have raw access to the corresponding EEPROM regions.

The SPCR on Mullion (CXR) models is saved at 0x0000-0x2800 while it's at 0x3000-0x5800 on Sherwood (SW) models.

For the NVS regions (and internal regions) please refer to the [EEPROM](#) page, this guide will only provide the offsets used by [UM](#).

## Procedure

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### Case #1: A full dump of the original Syscon SPCR is available

If this is the case and you're not switching from Mullion to Sherwood or the other way around you can just copy the SPCR.

### Case #2: The original Syscon SPCR is not available

**The PS3 needs to run a factory (JIG) firmware in order to remarry the Syscon!**

First blank the complete SPCR with hex *FF*. Then for the first 0x30 bytes write this:

```
5E B4 F7 C9 50 62 F1 B2 EC F7 EE 1A 3C E3 D8 D0
C5 C2 73 4B A4 13 3D 2C 9E EE 88 ED 0C A8 15 C7
8F 59 DC E4 35 A8 11 BD 8B EC 4E 95 09 F1 E7 38
```

If you start the PS3 you should see this on PS3s with a Mullion Syscon (as part of the bootlog obtainable from the SB UART):

and this on Sherwood models:

To complete the remarrying procedure please install any firmware using the manufacturing updater (launched by *lv2diag*) and set offset 0x48C07 of the NVS back to *FF*.

## Explanation

encrypted with Key 0x020.

The last 16 bytes are the CMAC of the previous 16 bytes using Key 0x020.

The Hypervisor uses the Syscon status to determine if it needs to be remarried. 1 means that it needs to be remarried, 2 means it's already married to CELL.

The key saved at offset 0x10 is used to decrypt the second layer of the EID1 stored on the flash.

If this key is wrong, Syscon will return the error 800040C. The only time this has been observed is on prototype consoles with a Sherwood Syscon. To fix that problem you need to create your own EID1 using the eid\_root\_key and patch it into the Syscon firmware.

v · e ( <a href="http://www.psdevwiki.com/ps3/edit/Template:Reverse_engineering">http://www.psdevwiki.com/ps3/edit/Template:Reverse_engineering</a> )	
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