SC EEPROM

Most of the information we have about the Syscon EEPROM comes from graf_chokolo reverse engineering of the HV. See Hypervisor Reverse Engineering

Syscon EEPROM is where system flags, tokens and hashes are stored.

Right now, most of the communication we have with the Syscon EEPROM is through Linux using graf_chokolo ps3dm-utils and/or using his payloads.

See also Discussion page and ZeroTolerance

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Information

- On <u>Mullion</u> syscons the EEPROM "pins" are exposed externally so we can capture the EEPROM traffic by attaching devices like Logic Analyzers, Protocol Analyzers, etc...
 - On Syscon CXR713 Series the EEPROM consists of 0x4000 blocks, every block contains 2 bytes of data, so the total EEPROM size is 0x8000 bytes (32KB).

- On Syscon CXR714 Series the EEPROM consists of 0x2800 blocks, every block contains 2 bytes of data, so the total EEPROM size is 0x5000 bytes (20KB).
- On Sherwood syscons the EEPROM is virtualized inside FLASH menory, so there is not physical access to the EEPROM.
 - On Syscon SW Series, Syscon SW2 Series and Syscon SW3 Series the virtual EEPROM consists of 0x4000 blocks, every block contains 2 bytes of data, so the total EEPROM size is 0x8000 bytes (32KB).

Dont confuse the SPI block access (using blocks of 2 bytes leght) with the "Block ID" used by the SERV_NVS Syscon Service

SPI Commands

Syscon EEPROM uses a standard SPI protocol with proprietary commands as following:

Description	Command	Note	
Unlock Command	ommand 0xA3 0x00 0x00 This command must be send first before write command.		
Write Command	0xA4 0xXX 0xXX	XX XX is a block to be written (in the range 0x0000 up to 0x3FFF for Syscon CXR713 Series, or 0x0000 up to 0x27FF for Syscon CXR714 Series) The maximum data to be written in one command cycle is 32 byte length (16 blocks).	
Read Command	0xA8 0xXX 0xXX	XX XX is a block to be read (in the range 0x0000 up to 0x3FFF for Syscon CXR713 Series, or range 0x0000 up to 0x27FF for Syscon CXR714 Series) There is no maximum limit for read command so we can send it once with block 0x00 0x00 then read the full SC EEPROM at once without sending read command again.	
Check Status Command	0xA9 0x00 0x00 0x00	The response of this command is 0xFFFFFFF if there is no error, or any other value if there is error happened or SC EEPROM still busy doing something.	

Dumps

- https://mega.co.nz/#!Bt8klAhQ!-t5YVetoL9gz6iZucpqQB9Vl9chCkbhFiMfqjbmotoc
 MD5:B0E0551116B718A4921757B2B074693F (https://www.google.com/search?q=B0E0551116B718A4921757B2B074693F)
- https://mega.co.nz/#!B51wWJYAlzg8O-vCvRBOgK5mpzTQ1H2hgBZmykglmbksB5w1Mlfg MD5:3E0E73DACF7E10F2369624EA439C661B (https://www.google.com/search?q=3E 0E73DACF7E10F2369624EA439C661B) (partial: MD5:7E2BAD4DFDEE485494C8749B1C3E5676 (https://www.google.com/search?q=7E2BAD4DFDEE485494C8749B1C3E5676) / MD5:05D9ED4B545C709C9C4564F047028DE8 (https://www.google.com/search?q=05D9ED4B545C709C9C4564F047028DE8))
- https://mega.co.nz/#!t50DCliL!RYLvjm35nmH3JVfhsGIXFWVHxmCr07ERsFBWhAOxD_Q MD5:95DDFB21D65E38F20CD66517B67EAE7F (https://www.google.com/search?q=95 DDFB21D65E38F20CD66517B67EAE7F)
- https://mega.co.nz/#!x4V0XBgJ!inxGYA5s8lfAF5Pe-naKCzTa1r5pY8Pn18js3D7QlnI
- https://mega.nz/#!iV0nGY4!!94ByAd-sourgK8_I_4s-6BX_V7iVOrysQd55bI0N6ws MD5:1DB1CAA8E3D54256A59D08B6AF2B9BC5 (https://www.google.com/search?q=1DB1CAA8E3D54256A59D08B6AF2B9BC5) (Dumped by Syscon EEPROM Flasher done by me "Abkarino" using Arduino Mega).
- https://mega.nz/#!AwF1jlaB!5qei9JOCzisgUHARcjARCw0zvQENkkvtAdd_O0dRUfl DECR Syscon EEPROM dump from lv2 um_manager, needs documentation.

Note: different consoles have same initial 16 bytes -> maybe key/iv?

Important offsets

SC EEPROM Offset Table - Flags and Tokens

Here is the table of SC EEPROM offsets that can be accessed through Update Manager (3.15):

Offset	Size	Description	
0x02F00	8	Manufacturing Update Release Version String	
0x02F08	0x18	Manufacturing Update Build Version + Build Date String	
0x02F20	8	Manufacturing Update Build Target ID (Can be 0x83(CEX-ww), 0x82(DEX-ww), 0x81(DevelopmentTool) or 0xDEAD. Written during the manufacturing fw update process according to target string inside /dev_flash/vsh/etc/version.txt)	
0x02F28	0xD0	Padding/undocumented (the sample below is from motherboard REX-001(eMMC), syscon SW3-304) 00 00 00 00 00 00 00 00 00 00 00 00 00	
0x02FF8	1	Factory Bit (0 = ?, 1 = reset, 2 = ?, 3 = (on retails))	
0x02FF9	0x7	Padding/undocumented 00 00 01 01 01 00	

	Offset	Size	Description
0x4	48000	0x13	(Iv0 NVS region 0 start)
<u>0</u>	0x48000	0x13	(Iv0 NVS region 0)
0x4	48012	-	(Iv0 NVS region 0 end)
0x4	48013	0x2A	QA Token ECDSA Signature (=> 3.60 firmwares)
0x4	48800	0x0F	(Iv0 NVS region 1 start)
	0x48800	1	?
ľ	0x48801	1	- hv log settings/infos? -
ľ	0x48802	2	? (Iv0/Iv1 CodeVerifier::spu_interrupt_handler_class2 related)
1	0x48804	4	bootrom failure code
ľ	0x48808	4	bootrom failure timestamp
ľ	0x4880C	4	?
0x4	4880B	-	(Iv0 NVS region 1 end)
0x4	48C00	0x20	(Iv0 NVS region 2 start)
			boot flag (load_image_in_rom flag (os_boot_order_flag)
	0x48C00	1	0 = network 1st 1 = flash 1st
ľ	0x48C01	1	sys.dbgcard.hostpc (force standalone mode related)
ŀ			Network Device Mode (sys.dbgcard.dgbe / debug interface (select_net_device)
	0x48C02	1	-1: Ethernet 2 0: IFB 1: CP 2: SB UART 3: CP ch4 5: Disabled (default)
	0x48C03	1	sys.dbgcard.dgbe.index (select_dgbe_device)
	0x48C04	1	used to reset dgbe_config (only <= 0.85)
Ì	0x48C05	1	force update flag (update_flag for consoles with flash_format 0)
ľ	0x48C06	1	FSELF Control Flag / toggles release mode (fself_ctrl used by Iv0 for failsafe mode and by Iv2 to bypass protection checks)
ľ	0x48C07	1	Non-secure Product Mode (only <= 0.85) / force Syscon remarry (only JIG firmwares)
ľ	0x48C08	1	lv0 passes this to lv1ldr (not used on >= 0.82, maybe only CEB)
ľ	0x48C09	1	boot_fir_config (Iv0ldr, bit 1/2, delays setting of BE fault-iso-regs/SB params to Iv0)
ľ	0x48C0A	1	QA Flag exist flag
2	0x48C0B	1	mode_auth_flag / gx enable
ľ	0x48C0C	1	Memory Diag Flag (bootrom diagnostic mode and parameter (bootrom_diag))
Ì	0x48C0D	1	Memory Diag Status (Iv0Idr related)
Ì	0x48C0E	1	XDR_Link_Init failure flag
Ì	0x48C0F	2	cell os flags (loader parameter)
	0x48C11	1	bootrom trace level 0x00: fatal errors 0x01: errors 0x02: information messages 0x03: debug messages 0xFF: ? (default)
ŀ	0x48C12	1	?
ŀ	0x48C13	1	flash ext flag (Device Type (flash_ext_format))
ŀ	0x48C14	4	cellos_spu_configure
ŀ	0x48C18	4	Safe Mode System Language. Using the language codes. See also XRegistry.sys/setting/system/language
	0x48C1C	4	Safe Mode VSH Target (maybe QA,Debug,Retail,Kiosk?). See Promo_flags.txt and GetReleaseTarget vsh export 0x00000000 = ? (default) 0x000000001 = ? 0x000000005 = dtcpipdevdex (can't update to any firmware, except dtcpipdevdex firmware) 0xFFFFFFFF = ?
0x4	48C1F	-	(Iv0 NVS region 2 end)
	48C22	0x03	(Iv0 NVS region 3 start)
	0x48C22	1	be nclk (be_nclck_flag1)
3	0x48C23	1	be ref clk (be_nclck_flag2)
-	0x48C24	1	Bank #0 OS-Flag (ros0 if 0xFF else ros1, for NOR consoles only) (os_bank_indicator)
Ov/	48C24	_	(Ivo NVS region 3 end)
	48C25	1	Bank #0 rvkprg-Flag
	48C26	1	Bank #0 rvkpkg-Flag
	48C27	1	Bank #1 OS-Flag
			-
0x48C28		1	Bank #1 rvkprg-Flag

/02	2/2023, 0	3.33	SC EEPROM - P53 Developer Wiki					
0x4	48C29	1	Bank #1 rvkpkg-Flag					
0x48C30		0x0D	(Iv0 NVS region 4 start)					
	0x48C30	1	SPU num - Usally 0x06(default), can be set to 0x07 to enable the 8 SPE (restrict_spu) or can be set to 0xFF(unlimit)					
4	0x48C31	4	sata param					
	0x48C35	8	initial TB value (spr_tbuw_value (cellos_spu_configure))					
0x48C3C -		-	(Iv0 NVS region 4 end)					
	48C40	0x10	(Iv0 NVS region 5 start)					
0x48C42		1	HDD Copy Mode					
	0x48C43	4	**					
			Hdd Ident Information					
	0x48C47	1	Analog Sunset Flag, will disable AACS video output without HDMI cable soon					
	0x48C50	0x10	Debug Support Flag					
	0x48C60	1	Update Status					
	0x48C61	1	Recover Mode Flag					
	0x48C62	8	boot param. Accessed by syscalls 404?. See also this					
5.	0x48C6A	2	factory process completion (bitflags?). Accessed by syscalls 405, 406, 407?. See also this Usually FFFF, but also: 00E2 - CokC12, SEM-001, CXR713120-203GB 00EA - CokD10, DIA-001, CXR714120-301GB 00E6 - CokE10, DIA-002, CXR714120-302GB 00EA - CokF10, VER-001, SW-301 00AA - CokG11, DYN-001, SW2-301 00BE - CokH11, SUR-001, SW2-301 00BE - CokH11, SUR-001, SW2-302 00B2 - CokJ13, JTP-001, SW2-303 & CokK10, KTE-001, SW3-301 00B0 - CokM20, MSX-001, SW3-302 & CokM30, MPX-001, SW3-302 & CokN10, NPX-001, SW3-302 & CokP10, PQX-001, SW3-304 & CokR40, REX-001, SW3-3000 00F0 - CokD10, DIA-001, CXR714120-304GB Refurb 40nm RSX 01FE - Cok14, COK-001, CXR714120-304GB Refurb 40nm RSX					
0x4	48C4F	-	(Iv0 NVS region 5 end)					
0x4	48C80	0x10	(Iv0 NVS region 6 start)					
	0x48C80	8	(rsx.rdcy.0)					
<u>6</u>	0x48C88	8	(rsx.rdcy.1)					
Ωx4	48C8F	_	(Iv0 NVS region 6 end)					
	48C90	0x30	(Iv0 NVS region 7 start)					
0.4-								
	0x48C90	8	(rsx.rdcy.2)					
	0x48C98	8	(rsx.rdcy.3)					
7.	0x48CA0	8	(rsx.rdcy.4)					
	0x48CA8	8	(rsx.rdcy.5)					
	0x48CB0	8	(rsx.rdcy.6) / game_board_storage_read					
	0x48CB8	8	(rsx.rdcy.7) / game_board_storage_read					
0x4	48CBF	-	(Iv0 NVS region 7 end)					
0x4	48CCE	1	0xFF / 0xFE / 0x00 (?)					
0x4	48CCF	1	pme_user debug printf flag (& 0x03 verbose level)					
0x4	48CF0	0x10	(NVS region start)					
	0x48CF0	1	ss.common.printf.enabled					
	0x48CF1	1	ss.common.debug.level+ss.update.debug.level					
	0x48CF2	1	ss.updatefe.debug.level+ss.ss_init.debug.level					
	0x48CF3	1	ss.ss_proxy.debug.level+ss.spm.debug.level					
	0x48CF4	1	ss.spm.debug.policy+ss.ac_cntl.debug.level					
	0x48CF5	1	ss.ploader.debug.level+ss.gloader.debug.level					
ŀ	0x48CF6	1	ss.commlib.debug.level+ss.sc_mngr.debug.level					
	0x48CF7	1	ss.sc_iso.debug.level+ss.ii_mngr.debug.level					
ŀ	0x48CF8	1	ss.vtrm.debug.level+ss.sec_rtc.debug.level					
	0x48CF9	1	ss.sb_mngr.debug.level+ss.sb_iso.debug.level					
	0x48CFA	1	ss.app_info.debug.level+ss.aim_iso.debug.level					
	0x48CFB	1	ss.fdm.debug.level+ss.fdm_iso.debug.level					
	0x48CFC	1						
			ss.fw.debug.level+ss.stricv.debug.level					
	0x48CFD	1	ss.usbauth.debug.level+ss.dispatch.debug.level					
	0x48CFE	1	ss.sc_test.debug.level+ss.sc_test.debug.spu					
	0x48CFF	1	ss.token.debug.level					
	48CFF	-	(NVS region end)					
0x4	48D00	0x0C	(Iv0 NVS region 8 start)					
8	0x48D00	4	ip_addr (dgbe_config)					
- 1	0x48D04	4	ip_netmask					

1					
	0x48D08	4	ip_gateway		
0x	0x48D0B		(Iv0 NVS region 8 end)		
0x	0x48D20 0x08 (lv		(Iv0 NVS region 9 start)		
9	0x48D20	8	spider.gbe0.macaddr.0 (0xFFFFFFFFFFFFFF if unused/nonpresent)		
0x	48D27	-	(Iv0 NVS region 9 end)		
0x	0x48D28 0x18		(Iv0 NVS region B start)		
	0x48D28 8		spider.gbe0.macaddr.1 (FFFFFFFFFFFFF if unused/nonpresent)		
В	0x48D30	8	spider.gbe0.macaddr.2 (FFFFFFFFFFFFF if unused/nonpresent)		
	0x48D38	8	spider.gbe0.macaddr.3 (FFFFFFFFFFFFF if unused/nonpresent)		
0x	48D3F	-	(Iv0 NVS region B end)		
0x	48D3E	0x50	(Iv0 NVS region A start)		
A	<u>A</u> 0x48D3E 0x50 QA Tok		QA Token - UM doesn't allow access to this offset but SC Manager can read/write it (qa_token)		
0x	48D8D	-	(Iv0 NVS region A end)		
0x48D8E 0x50 mode_auth_data (read/cleared by ss_sc_init_pu, checked by spu_mode_auth, us		0x50	mode_auth_data (read/cleared by ss_sc_init_pu, checked by spu_mode_auth, used to enter product mode on jig firmwares without a dongle)		

In a standard mostly untouched ps3 the common value for this flags is OXFF wich means not active, anything else means active (e.g. OXFE)

To change this to an active status you have to write 0x00 to turn on the flag

Debug support flag is tied to EID which is supposed to be hashed and saves in SC EEPROM

OA flag is tied to QA token that is also saved in this part of the SC EEPROM

QA Token ECDSA Signature is stored in 0x48013 offset (starting from 3.60 firmwares)

Undocumented region

This is 0x48800 on SC EEPROM, or at 0x7100 (mullions with 32KB EEPROM used), or at 0x4100 (mullions with 20KB EEPROM used), or at 0x1100 (sherwoods)

 $Accessed \ by \ \underline{Hypervisor} \ \underline{Service} \ \underline{ID} \ \underline{32} \ \underline{REQUEST_SYSTEM_EVENT_LOG} \ ?, and \ \underline{syscall} \ \underline{395} \ \underline{sys_sm_request_system_event_log} \ ?$

There is an unknown syscon response of 0x100 bytes when using NVS service with such params: BlockID=1, Offset=0, Size=0.

Sometimes the whole region is filled with FF's (empty, never used, or erased), it seems this procedure can be used to reset it

It can be considered an structure composed by a 0x10 header, and six available "slots" of 0x28 each, the second byte of the header seems to be some kind of counter related with the slots where the only values possibles are 0-5. The presence of data in the slots could vary usually all them are filled with data but in some rare cases the slots are empty (filled with FF's)

Sample (CokH11, SUR-001, SW2-302)

```
Offset(h) 00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
     00001100
                                    ÿ.ÿÿÿÿÿÿÿÿÿÿÿÿÿ
..ð5..á...S.....
.....UUUU
00001110
00001120
                                    .....ñoh..á.
00001140
                                    ..S.......
      00001150
00001160
                                    ....UUUU......
.ñoÇ..á...S.....
                                    ......ÿî'..á.
00001170
      00001180
                                    ....quôu.....
000011A0
                                    .!sR..á..S....
.....PuUQ
......ïÕ..á.
      000011B0
000011D0
```

Sample with 2 slots used

Sample with only 1 slot used (CokP10, PQX-001nor, SW3-304)

```
ÿÿÿÿÿÿÿÿÿÿÿÿÿÿÿÿ
ÿÿÿÿÿÿÿÿÿÿÿÿÿÿÿ
000011A0
```

The structure of an slot seems to be: 0x4 (timestamp) + 0x2 (unknown, always 0000) + 0x1 (unknown, always 0xE1 or 0xE2) + 0x1 (Data Size?, usually 0x18) + 0x4 (Data Type?) + 0x1C (data, included padding)

■ The timestamp follows the same format than the timestamps of the Syscon Error Codes, in some syscon models the lowest value posible for this timestamps seems to be 0x0B488680 (2005/12/31 00:00:00)

```
Offset(h) 00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00001110 0B 74 08 2F 00 00 E1 18 00 03 15 00 0C 03 00 00 .t./..á.......

00001120 A8 00 00 18 32 E2 00 00 00 80 00 00 FF FF FF FF ...2à...€..ÿÿÿÿ

00001130 00 00 00 00 00 00 00 00
Offset(h) 00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00001110 1F AC E5 B0 00 00 E1 18 00 03 02 00 0C 03 00 00 .Ρ..á...........
00001120 A8 00 00 15 8A 20 00 00 00 40 00 00 AA AA AA AA ...Š ...@..ªaaa
00001130 00 00 00 00 00 00 00 .......
Offset(h) 00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00001110    1D 59 29 DB 00 00 E2 18 01 51 40 25 40 01 03 00 .Y)Û..â..Q@%@...
00001120 00 00 01 08 E5 00 13 00 C7 00 00 00 00 00 00 ....å...Ç......
00001130 00 00 00 00 00 00 00 00 .....å
Offset(h) 00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
Offset(h) 00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00007110 0B 48 86 7D 00 00 E1 18 53 54 52 3A 50 41 54 41 ..ht}..á.STR:PATA 00007120 43 30 3A 43 61 62 6C 65 20 4E 6F 74 20 43 6F 6E C0:Cable Not Con 00007130 6E 65 63 74 00 00 00 00 nect....
```

- Notes
 - See the timestamp of the last sample with value 0B48867D, very close to 0B488680 (2005/12/31 00:00:00)

lvo SC EEPROM usage

```
[*] lv0 NVS regions:
                                                block size
0x48000 0x13
0x48800 0x0C
           # start_offset end_offset block 0 0x00 0x12 0x4800
           1 0×00
                                 0x0B
           2 0x00
                                 0x1F
                                                 0x48C00 0x20
           3 0x22
4 0x30
                                                 0x48C00 0x03
0x48C00 0x0D
           5 0x40
                                 0x4F
                                                0x48C00 0x10
           6 0x80
7 0x90
                                 0x8F
0xBF
                                                 0x48C00 0x10
0x48C00 0x30
           8 0x00
                                 0x0B
                                                0x48D00 0x00
                                0x27
0x8D
           9 0x20
                                                 0x48D00 0x08
                                                 0×48D00 0×50
           B 0x28
                                0x3F
                                                0x48D00 0x18
[*] Example region data (taken from region cache):
           FF FF FF FF FF FF FF 00 00 00 01 00 00 00 00
           3:
               FF FF 00
               06 18 18 17 18 FF FF FF FF FF FF FF
           5:
               FF FF OD 02 0A 02 FF FF FF FF FF FF FF FF FF
           9:
               FF FF FF FF FF FF FF
[*1 lv0 SC EEPROM usage:
                                         addr size structure
0x48804 0x04 [0x04 value]
0x48C00 0x01 [0x01 flag]
0x48C02 0x01 [0x01 index]
           UNKNOWN
           os_boot_order_flag
select_net_device
select_dgbe_device
fself_ctrl
                                         0x48C03 0x01 [0x01 index]
0x48C06 0x01 [0x01 flag]
           UNKNOWN (debug?)
qaf_enable
                                         0x48C08 0x01 [0x01 flag]
0x48C0A 0x01 [0x01 flag]
           talenable
cellos_flags
bootrom_trace_level
flash_ext_format
be_nclck_flag1
be_nclck_flag2
os_bank_indicator
                                         0x48C0F 0x02 [0x02 flags]
0x48C11 0x01 [0x01 level]
0x48C13 0x01 [0x01 flag]
                                         0x48C22 0x01 [0x01 flag]
                                         0x48C23 0x01 [0x01 flag]
0x48C24 0x01 [0x01 flag]
0x48C30 0x01 [0x01 flag]
           restrict spu
           sata_param 0x48C31 0x04 [0x04 flag] cellos_spu_configure 0x48C33 0x04 [0x04 config]
           spr_tbuw_value
rsx.rdcy.0
rsx.rdcy.1
                                         0x48C35 0x08 [0x08 value]
                                         0x48C80 0x08 [0x08 value]
                                         0x48C88 0x08 [0x08 value]
                                         0x48C90 0x08 [0x08 value]
            rsx.rdcv.2
           rsx.rdcy.3
rsx.rdcy.4
                                         0x48C98 0x08
0x48CA0 0x08
                                                             [0x08 value]
[0x08 value]
           rsx.rdcv.5
                                         0x48CA8 0x08 [0x08 value]
                                         0x48CB0 0x08 [0x08 value]
                                         0x48CB8 0x08 [0x08 value]
            rsx.rdcy
           dgbe_config
                                         0x48D00 0x0C [0x04 ip_addr, 0x04 ip_netmask, 0x04 ip_gateway]
```

UNKNOWN 0x48D20 0x08 [0x08 value] qa_token 0x48D3E 0x50 [0x50 token]

System Data from SC EEPROM

Here is the list of possible SC EEPROM offsets:

Index	SC EEPROM offset	Data size	Description
4	0x48D00	4	?
5	0x48D04	4	?
6	0x48D08	4	?
0	0x48D20	6	?
1	0x48D28	6	?
2	0x48D30	6	?
3	0x48D38	6	?

Dumpable SC EEPROM Offset - Block ID and Block Offset Mapping Table (NVS Service)

Right now we only have read access to some portions of the SC EEPROM to have access to this regions DM needs to be patched, see section dumping SC EEPROM.

SC EEPROM Offset	Block ID	Block Offset	Description	Physical Offset (CXR713)	Physical Offset (CXR714)	Virtual Offset (SW)
-	-	-	ERRLOG Errors are stored here	-	-	0x900
0x2F00 - 0x2FFF	0x10	0x2F00 - 0x2FFF	"Industry Area" aka OS Version Area	0x2F00	0x2F00	0xE00
0x3000 - 0x30FF	0x20	0x3000 - 0x30FF	"Customer Service Area"	0x3000	0x3000	0xF00
0x48000 - 0x480FF	0x00	0x48000 - 0x480FF	?	0x7000	0x4000	0x1000
0x48800 - 0x488FF	0x01	0x48800 - 0x488FF	HyperVisor Area	0x7100	0x4100	0x1100
0x48C00 - 0x48CFF	0x02	0x48C00 - 0x48CFF	Contains flags and tokens/ see above	0x7200	0x4200	0x1200
0x48D00 - 0x48DFF	0x03	0x48D00 - 0x48DFF	System Data Region	0x7300	0x4300	0x1300
N/A	0xFF	N/A	? sys_boot_gos flag is there	No SC EEPROM activity	?	?
All other offsets	Invalid	Invalid	?	-	-	-

Dumpable only with HW flasher SC EEPROM Offsets - Full Mapping Table (NAND only)

Sample from a CECHGxx with SEM-001 motherboard

Physical Offset	Description	Samples
0x0-0xF	magic0 (static bytes)	99D9662BB3D761546B9C3F9ED140EDB0
0x10-0x28F	eEID1 (probably encrypted)	
0x290-0x4FF	Unknown	
0x500-0x55F	magic1 (static bytes)	E01B01CF9C7FBC7D79D670086DAF497F 9BD3ASD5178DDE1D825344AE398113DD FF525D8BF4422CC766B13AA47FA2CC369 83A720CD45D18FB3D4112888187E3040 702B91D8E6ACEEC4B801315F357E1EE3 2DA1081408D72C41AFC1B61AE7C9882D
0x560-0x95F	Authenticated Data Region 0 (snvs region 0), not used	Used on COK-001, DIA-001 / CXR714120-304GB / 40nm RSX (official refurbished)
0x960-0xD5F	Authenticated Data Region 1 (snvs region 1), contains ss-service version, secure_product_mode flag, vtrm cipher/hasher keys, versions/hashes of installed update packages, etc	Used on COK-001, DIA-001 / CXR714120-304GB / 40nm RSX (official refurbished)
0xD60-0x115F	Authenticated Data Region 2 (snvs region 2), not used	Used on COK-001, DIA-001 / CXR714120-304GB / 40nm RSX (official refurbished)
0x1160-0x155F	Authenticated Data Region 3 (snvs region 3), not used	Used on COK-001, DIA-001 / CXR714120-304GB / 40nm RSX (official refurbished)
0x1560-0x195F	Authenticated Data Region 4 (snvs region 4), not used	Used on COK-001, DIA-001 / CXR714120-304GB / 40nm RSX (official refurbished)
0x1960-0x1D5F	Authenticated Data Region 5 (snvs region 5), not used	Used on COK-001, DIA-001 / CXR714120-304GB / 40nm RSX (official refurbished)
0x1D60-0x215F	Authenticated Data Region 6 (snvs region 6), not used	Used on COK-001, DIA-001 / CXR714120-304GB / 40nm RSX (official refurbished)
0x2160-0x255F	Authenticated Data Region 7 (snvs region 7), not used	Used on COK-001, DIA-001 / CXR714120-304GB / 40nm RSX (official refurbished)
0x2560-0x25FF	FF Region	
0x2600-0x26AF	FF Region	
0x26B0-0x26CF	Unknown, encrypted ?	
0x26D0-0x26EF	Unknown, encrypted ? (filled with FF's on TMU)	
0x26F0-0x26FF	FF Region	System Info
0x2700-0x270F	magic2 (static bytes) (does not exist in TMU dump) 857C4DE5BFAFD6A4A361CB5BFDD72D26	
0x2710-0x27FF	FF Region	-
0x2800-0x2BFF	Syscon Patch Content Top-Half	
0x2C00-0x2EFF	FF Region	
0x2F00-0x2FFF	Industry Area (nvs region 0x20)	
0x3000-0x30FF	Customer Service Area (nvs region 0x30)	
0x3100-0x31FF	Special Region #0	Platform Config (Platform_ID(hex) at relative offset 0xE
0x3200-0x32FF	Special Region #1	Hardware/XDR Config
0x3300-0x33FF	Special Region #2	Thermal Config
0x3400-0x34FF	Special Region #3	Thermal Config
0x3500-0x35FF	Special Region #4	On/Off Count, On-time
0x3600-0x36FF	Special Region #5	On/Off Count, On-time
0x3700-0x37FF	Special Region #6 Errorlog (retail PS3 models)or Serial Num (DECR only)	2M010001207K / 2D@ 40@
0x3800-0x38FF	FF Regionor Errorlog (DECR only)	
0x5000-0x6FFF	FF Region	
0x7000-0x70FFor 0x4000-0x40FF	Bluray Drive Area ?? (nvs region 0)	
0x7100-0x71FFor 0x4100-0x41FF	HyperVisor Area (nvs region 1)	System Software Config
0x7200-0x72FFor 0x4200-0x42FF	-0x72FF0r Tokon Arga (pyg. rogion 2)	
0x7300-0x73FFor 0x4300-0x43FF	System Data Area (nvs region 3)	
0x7400-0x7FFFor 0x4400-0x4FFF	Syscon Patch Content Bottom-Half	

Authenticated Data Regions Crypto Analysis

Tests

- AES128CBC with fixed key and incremented iv (by 1 each time) (https://i.imgur.com/A8g00bD.png)
- results (https://i.imgur.com/HZDWGSk.png)

- region 0 encrypted (https://i.imgur.com/2mtrtdm.png) vs decrypted (https://i.imgur.com/7bSdQni.png)
- region 7 encrypted (https://i.imgur.com/FGJKkuz.png) vs decrypted (https://i.imgur.com/7TSeHWK.png)

Conclusion

- different key for a different authenticated region.
- Sony uses either AES 128-cbc or AES 256-cbc (most likely 128-cbc)
- Sony does this weird cbc crypto in which they only decrypt portions of 0x10 bytes of the region, then increment or decrement (most likely increment) iv, and then decrypt again. I have decided to call it ctr-cbc.
- most likely the keys used are session perconsole keys.
- most likely the iv used starts with 00, then gets incremented by 1 for each 0x10 bytes

Acknowledgements

- Zer0Tolerance for the crypto findings
- flatz for his awesome Syscon tool

Dumping SC EEPROM

Linux

First you need graf_chokolo kernel ps3dm-utils and linux_hv_scripts.

Patch DM using linux_hv_scripts:

dmpatch.sh

Read the data from the region you want for example (see tables above):

ps3dm_scm /dev/ps3dmproxy 0x48000 0xFF

You can see some coolstuff containing dumps.

Hashes

Where exactly the hashes are stored is still a secret. It is said that those hashes are stored in SC EEPROM.

To retrieve the information about the packages you have installed you can also use ps3d_utils.

Linux

Installed Package info

ps3dm_um /dev/ps3dmproxy get_pkg_info TYPE

Examples

get_pkg_info 1 - Core OS package

0003004100000000

get_pkg_info 2 - Revoke List for program

0003004100000000

get_pkg_info 3 - Revoke list for package

0002003000000000

get_pkg_info 4

deadbeaffacebabe

get_pkg_info 5

deadbeaffacebabe

get_pkg_info 6 - Firmware Package

0003005000000000

You can find more information about this in Hypervisor Reverse Engineering.

Hashes

What algorithm is used and what exactly is hashed is still unknown. It seems that the content of files is hashed by the SHA-1.

ps3dm_scm /dev/ps3dmproxy get_region_data ID

These hashes are checked by lv1 to make sure that the data has not been altered through SC Manager: scm_get_region_data: get_result: ret[X]: ox%x

Examples

region data o - ROSo

00 03 00 41 00 00 00 00 00 c3 eb 01 96 24 d0 1c 26 14 f3 1c a4 a2 ff ce 81 77 3a 4c f8 42 86 04 ee 34 bb db be 1c a7 51 e5 59 f1 95 61 07 a5 eb

---lv0-----00 03 00 15 00 00 00 00 39 8F 56 3B D3 C3 19 27 42 F5 0B 2A 06 0D 31 64 18 F3 E3 8A 0A AB D0 BE F0 D7 47 7A A7 F4 A7 5B 2D 09 78 48 E9 46 40 62

region data 1 - ROS1

---1v0-------1v1---00 03 00 15 00 00 00 00 39 8F 56 3B D3 C3 19 27 42 F5 0B 2A 06 0D 31 64 18 F3 E3 8A 05 D4 15 79 F7 68 8A DF AD 9E CD 34 B4 C7 9F A8 C6 99 82 EE

region data 2 - RL FOR PROGRAM.img o

region_data 3 - RL_FOR_PROGRAM.img 1

region_data 4 - RL_FOR_PACKAGE.img o

region_data 5 - RL_FOR_PACKAGE.img 1

region data 6

de ad be af fa ce ba be be

DE AD BE AF FA CE BA BE 00 00 00 00 00 00 00 00 00 19 38 98 8F 93 C3 2F A9 C6 51 23 CF 12 CA 69 36 3E 59 7F 41 1F 56 D4 03 F4 C3 D2 6B 5D 51 F4 F4

region_data 7

de ad be af fa ce ba be be

region data 8 - BD Firmware Package

de ad be af fa ce ba be be

DE AD BE AF FA CE BA BE 00 00 00 00 00 00 00 00 00 05 FF B4 E 0B A7 FF 63 F4 F7 0A 22 D4 1B 3D F4 7D 24 32 71 B1 F9 84 B0 CD D7 42 7E FF 0C 77 C7 06

region data 10

de ad be af fa ce ba be de ad

region_data 11

de ad be af fa ce ba be de ad be af fa ce ba be

Local DE AD BE AF FA CE BA BE 00 00 00 00 00 00 00 00 A9 5A 92 EA 64 A6 64 C5 A2 06 93 38 B0 39 45 AD F3 AD 9D FF 90 17 88 26 B1 D3 6A D6 20 A5 73 2D

region_data 12

de ad be af fa ce ba be de ad be af fa ce ba be

Left of the ce ba be de ad be af fa ce ba be de ad be af fa ce ba be de ad be af fa ce ba be

Left of the ce ba be de ad be af fa ce ba be de ad be af fa ce ba be de ad be af fa ce ba be

Left of the ce ba be de ad be af fa ce ba be de ad be af

region data 13

de ad be af fa ce ba be de ad

region_data 14

de ad be af fa ce ba be de ad be af fa ce ba be

DE AD BE AF FA CE BA BE 00 00 00 00 00 00 00 D1 9B DB DA 69 32 00 5E 09 2F D4 8E 22 09 97 03 01 AB 1B D6 0E 19 41 3C 00 B6 2C 40 07 E4 FF 45

region_data 15

de ad be af fa ce ba be be af fa

region_data 16 - 47?

Dumped data

PTCH Body

COK-001

```
0000430 0600 0e00 2820 d023 dc18 2801 d0cf 2802
0000440 d0e1 2804 d12f 2100 69ea 2010 f000 f83a
0000450 2220 9200 2201 2101 6a2f 2010 ab02 f000
0000460 f839 6969 1c30 f000 f825 e7d4 2840 d005
0000470 2101 0289 4288 d116 2007 e7d2 0000480 6a69 1c30 f000 f816 6aa9 1c30
0000490 2120 6aea 1c30 f000 f815 6b29 1c30 f000
0000fc0
0000000 0001 0000 0000 0006 2cc4 0003 2d88 0003
0000010 6440 0003 08d0 0002 dff0 4669 1c22 dff1
0000020 dff2 e04f d00d dff3 f078 0200 f0c8 0200
0000030 f10c 0200 f140 0200 1fff e8bd
                                                               1ffe e92d
0000040 1b0b e3a0 0001 e150 0007 ba00 0000050 2005 e080 0001 e152 0003 ca00
                                                               1bld e3a0
1ffe e8bd
0000000 2005 e1a0 1014 e59f f001 e1b0 1ffe e8bd 0000000 2005 e1a0 0001 e3a0 1004 e59f f001 e1b0 0000000 2005 e1a0 0001 e3a0 1004 e59f f001 e1b0 0000000 2005 e000 1014 e3a0 1004 e59f 6001 e155 0000000 0007 ba00 1014 e3a0 0005 e84 0001 e155 0000000 0007 ba00 1014 e3a0 0005 e1a0 1010 e59f 0000000 f001 e1b0 1fff e8bd 0005 e1a0 1004 e59f 0000000 f001 e1b0 1fff e8bd 0001 e3a0 1004 e59f
0000100 1fff e8bd 1fbf e92d 0004 e28f 8002 e1a6
0000110 001e ea00 6007 e1a0 300c e3a0 0396 e003
0000120 0012 e288 fff4 eaff 1fbf e8bd 2054 e596
0000180 f002 elb0 1fbf e8bd 004c e59f f000 elb0
0000190 0000 e358 000a 0a00 3008 ela0 000a e353
00001a0 0001 ba00 300a e243 fffb eaff 0004 e353
00001b0 0001 ba00 3004 e243 fffb eaff 0000 e353 00001c0 0001 0a00 7000 e3a0 ff10 e12f 7001 e3a0
00001d0 ff10 e12f 0950 0002 0902 0002 08ee 0002
00001e0 f3f4 f1f2 f3f4 f1f2 f3f4 f1f2 f3f4 f1f2
00003:0 b5fe 4d3a lc06 6829 f000 f874 2801 d055
00003:0 6869 lc30 f000 f86e 4c35 2801 d101 6cal
00003:0 e0la 6889 lc30 f000 f865 2801 d103 6929
00003:10 lc30 f000 f85f 69a9 lc30 f000 f85b 2800
0000400 d0ed 2801 d101 6cel e006 68e9 lc30 f000
0000410 f851 2800 d008 6d21 lc30 f000 f84b 0600
0000420 0e00 bcfe bc08 4718 6bel lc30 f000 f842
0000430 0600 0e00 2820 d023 dc18 2801 d0cf 2802
0000440 d0e1 2804 d12f 2100 69ea 2010 f000
0000450 2220 9200 2201 2101 6a2f 2010 ab02
0000460 f839 6969 1c30 f000 f825 e7d4 2840 d005
0000470 2101 0289 4288 d116 2007 e7d2 2000 e7d0 0000480 6a69 1c30 f000 f816 6aa9 1c30 f000 f812 0000490 2120 6aea 1c30 f000 f815 6b29 1c30 f000
00004a0 f809 2006 e7bd 200b
00004b0 b720 0200 4708 0000
                                              e7bb 0000
0000 0000
                                                               f524 0200
0000 0000
00004f0 7ab5 0002 7917 0002 75db 0002 7b6d 0002
0000500 0f05 0003 0f05 0003 7cfb 0002 4053 0002 0000510 7009 0002 6497 0002 0000 0000 0000 0000
0000fc0
```

COK-002

```
0000000 0001 0001 0003 0002 2fd4 0003 eeee eeee
0000040 dddd dddd cccc cccc f3f4 f1f2 f3f4 f1f2
0000050 f3f4 f1f2 f3f4 f1f2 f3f4 f1f2 f3f4 f1f2
00003c0 b5fe 4d3a 1c06 6829 f000 f874 2801 d055
0000360 6869 1c30 f000 f86e 4c35 2801 d101 6ca1 00003e0 e01a 68a9 1c30 f000 f865 2801 d103 6929 00003f0 1c30 f000 f85f 69a9 1c30 f000 f85b 2800
0000400 d0ed 2801 d101 6cel e006 68e9 1c30 f000
0000410 f851 2800 d008 6d21 1c30 f000 f84b 0600
0000420 0e00 bcfe bc08 4718 6bel 1c30 f000 f842
0000430 0600 0e00 2820 d023 dc18 2801 d0cf 2802
0000440 d0e1 2804 d12f 2100 69ea 2010 f000 f83a
0000450 2220 9200 2201 2101 6a2f 2010 ab02 f000
0000460 f839 6969 1c30 f000 f825 e7d4 2840 d005
0000470 2101 0289 4288 d116 2007 e7d2 2000
0000480 6a69 1c30 f000 f816 6aa9 1c30 f000
                                                                e7d0
0000490 2120 6aea 1c30 f000 f815 6b29 1c30 f000
00004a0 f809 2006 e7bd 200b e7bb 0000 f524 0200
00004b0 b728 0200 4708 0000 0000 0000 0000 0000
00004d0 0000 0000 4738 0000
00004e0 0000 0000 7bf5 0002
                                         0000 0000
7c31 0002
                                                        0000
                                                        7c85 0002
00004f0 7c5d 0002 7abf 0002 7783 0002 7d15 0002
0000500 1205 0003 12b5 0003 7ea3 0002 4203 0002 0000510 71b9 0002 6647 0002 0000 0000 0000 0000
0000fb0 0000 0000 f401 0200 0080 0000 0000 1dfe
0000fc0
0000000 0001 0001 0003 0003 2fd4 0003 09f8 0002
0000010 dddd dddd cccc cccc 28ee d001 d00d dff1
```

```
0000020 dddd dddd cccc cccc ffff ffff f078 0200
0000030 dddd dddd cccc cccc lfff e8bd lfbf e92d
0000040 0004 e28f 8002 e1a0 001e ea00 6007 e1a0
0000050 300c e3a0 0396 e03 60ac e59f 6000 e596
0000060 6006 e083 3004 e086 3020 e243 0000070 0005 e153 0010 ba00 5000 e3a0
0000080 8000 e3a0 0002 e158 0008 0a00 0000 e28f
0000090 000c ea00 5000 e591 50ff e285 506e e285
00000a0 5007 e085 5000 e581 8001 e288 ffff eaff
00000b0 1fbf e8bd 2054 e59f f002 e1b0 1fbf e8bd
00000000 fffb eaff 0000 e353 0001 0a00 7000 e3a0 0000100 fffb e12f 7001 e3a0 ff10 e12f 0a78 0002 0a16 0002 f3f4 f1f2 f3f4 f1f2 0000120 f3f4 f1f2 f3f4 f1f2 f3f4 f1f2
00003c0 b5fe 4d3a 1c06 6829 f000 f874 2801 d055
00003d0 6869 1c30 f000 f86e 4c35 2801 d101 6ca1
00003e0 e01a 68a9 1c30 f000 f865 2801 d103 6929
00003f0 1c30 f000 f85f 69a9 1c30 f000 f85b 2800
0000400 d0ed 2801 d101 6cel e006 68e9 lc30 f000
0000410 f851 2800 d008 6d21 lc30 f000 f84b 0600
0000420 0e00 bcfe bc08 4718 6bel lc30 f000 f842
0000430 0600 0e00 2820 d023 dc18 2801 d0cf 2802
0000440 d0e1 2804 d12f 2100 69ea 2010 f000 f83a
0000450 2220 9200 2201 2101 6a2f 2010 ab02 f000
0000460 f839 6969 1c30 f000 f825 e7d4 2840 d005
0000470 2101 0289 4288 d116 2007 e7d2 2000 e7d6
0000480 6a69 1c30 f000 f816 6aa9 1c30 f000 f812
0000490 2120 6aea 1c30 f000 f815 6b29 1c30 f000
0000490 7615 6b29 1c30 f000
00004a0 f809 2006 e7bd 200b e7bb 0000 f524 0200
00004b0 b728 0200 4708 0000 0000 0000 0000 0000
00004e0 0000 0000 7bf5 0002 7c31 0002 7c85 0002
00004f0 7c5d 0002 7abf 0002 7783 0002 7d15 0002 0000500 1205 0003 12b5 0003 7ea3 0002 4203 0002 0000510 7bb9 0002 6647 0002 0000 0000 0000 0000 0000
0000fb0 0000 0000 f401 0200 0080 0000 0000 1dfe
```

SEM-001

```
0000000 0001 0002 0003 0002 341c 0003 11b8 0002
0000010 dddd dddd cccc cccc 28ee dd01 d00d dff1
0000020 dddd dddd cccc cccc ffff ffff f078 0200
0000030 dddd dddd cccc cccc ffff f8bd 1fbf e92d
0000030 dddd dddd cccc cccc 1fff e8bd 1fbf e92d
0000050 300c e3a0 0396 e003 60ac e59f 6000 e596
0000060 6006 e083 3004 e086 320e 243 301f e5d3
0000070 0005 e153 0010 ba00 5000 e3a0 5000 e581
0000080 8000 e3a0 0002 e158 0000 8000 0000 e28f
0000080 8000 e3a0 5000 e591 50ff e285 506e e285
0000030 5007 e085 5000 e581 8001 e288 fff4 eaff
0000080 5007 e085 5000 e581 8001 e288 fff4 eaff
0000000 004c e59f f000 e1b0 0000 e358 000a 0a00
0000000 004c e59f f000 e1b0 0000 e358 000a 0a00
0000000 fffb eaff 0000 e333 0001 ba00 3004 e243
0000000 fffb eaff 0000 e333 0001 ba00 3004 e243
0000000 fffb e1f0 e012 f7001 e3a0 ff100 e1f2 f132 0002
0000110 11ea 0002 11d6 0002 f3f4 f1f2 f3f4 f1f2
00000fc0
```

DIA-001

```
0000000 0001 0003 0003 0002 3740 0003 1074 0002
0000010 dddd dddd cccc cccc 28ee dd01 dd0d dff1
0000020 dddd dddd cccc cccc ffff ffff fff f80 2020
0000040 0004 e28f 8002 e1a0 001e e300 6007 e1a0
0000053 000c e3a0 0306 e033 6002 630 6002 e596
0000060 6006 e083 3004 e086 3020 e243 301f e5d3
0000070 0005 e153 0010 ba00 5000 e531
0000070 0005 e153 0010 ba00 5000 e531
0000070 0005 e500 e591 50ff e285 506e e285
0000000 000c ea00 5000 e591 50ff e285 506e e285
0000000 1fbf e8bd 2054 e59f f002 e1b0 1fbf e8bd
0000000 000c e305 0000 e533 0001 ba00 3000 e258 0000 6000 e300 6000 e538 0000 e530 0000 e530 e000 e53
```

DIA-002

```
0000000 0001 0004 0004 0002 3e90 0003 1204 0002
0000010 dddd dddd cccc cccc 28ee d001 d00d dff1
0000020 dddd dddd cccc cccc 1fff ffff f078 0200
0000030 dddd dddd cccc cccc 1fff f8bd 17bf e92d
0000030 dddd dddd cccc cccc 1fff 8bd 17bf e92d
0000040 0004 e28f 8002 e1a0 001e ea00 6007 e1a0
0000050 300c e3a0 0396 e003 60ac e59f 6000 e596
0000060 6006 e083 3004 e086 3020 e243 301f e5d3
0000070 0005 e153 0010 ba00 5000 e3a0 5000 e581
0000080 8000 e3a0 0002 e158 0008 0a00 0000 e28f
00000080 8000 e3a0 0002 e158 0008 0a00 0000 e28f
00000080 8000 e305 5000 e581 8001 e285 506e e285
0000000 0000 c000 c000 5000 e301 50ff e285 506e e285
0000000 1fbf e8bd 2654 e59f f000 e1b0 0000 e358 0000 a000
0000000 000c e059f f000 e1b0 0000 e358 0000 a000
0000000 000c e59f f000 e1b0 0000 e358 0000 a000
0000000 000c e59f f000 e1b0 0000 e358 0000 a000
0000000 fffb eaff 0000 e1b0 0000 e358 0000 a000
```

```
00000f0 fffb eaff 0000 e353 0001 0a00 7000 e3a0 0000100 ff10 e12f 7001 e3a0 ff10 e12f 1288 0002 0000110 1236 0002 1222 0002 f3f4 f1f2 f3f4 f1f2 0000120 f3f4 f1f2 f3f4
```

PROTO BOARD 1

PROTO BOARD 2

```
0000000 0001 0005 0001 0001 4000 0003 1228 0002
0000010 dddd dddd cccc cccc 28ee dd01 dd0d dff1
0000020 dddd dddd cccc cccc ffff ffff 678 0200
0000030 dddd dddd cccc cccc ffff ffff 68b 0200
0000050 300c e3a0 0396 e003 60ac e59f 6000 e596
0000050 300c e3a0 0396 e003 60ac e59f 6000 e596
0000050 300c e3a0 0396 e003 3004 e086 3020 e243 301f e5d3
0000070 0005 e153 0010 ba00 5000 e3a0 5000 e581
00000070 0005 e153 0010 ba00 5000 e3a0 5000 e581
00000000 8000 e3a0 0002 e158 0000 800 00000 e28f
0000000 8000 e3a0 0002 e158 0000 e591 50ff e285 506e e285
00000a0 5007 e085 5000 e591 50ff e285 506e e285
00000a0 5007 e085 5000 e591 8001 e288 fff4 eaff
0000000 fffb e3fd 0004 e353 0001 ba00 300a e243
0000000 fffb eaff 0004 e353 0001 ba00 300a e243
0000000 fffb eaff 0004 e353 0001 ba00 3004 e243
0000000 fffb eaff 0004 e353 0001 ba00 3004 e243
0000010 fffb e3fd 0004 e353 0001 ba00 3004 e243
0000010 fffb e3fd 0004 e353 0001 ba00 3004 e243
0000010 fffb e3fd 0004 e353 0001 ba00 3004 e243
0000010 fffb e3fd 0004 e353 0001 ba00 3004 e243
0000010 fffb e3fd 0004 e353 0001 ba00 3004 e243
0000010 fffb e3fd 0004 e353 0001 ba00 3004 e243
0000010 fffb e3fd fflo e12f 7001 e3a0 fflo e12f 12ac 0002
0000110 125a 0002 1246 0002 f3f4 f1f2 f3f4 f1f2
**
00000fc0
```

DYN-001

■ To be continued...

Authenticated Regions

Here is an example of data (partition 1) from syscon EEPROM which stores VTRM block key, SRK/SRH, region data, etc.

RETAIL TSOP:

```
0x0060:
                                       0x0080:
0×0090:
0x00B0:
     0x00D0:
0x00D0:
0x00E0:
0×00E0 ·
                                       ..... <- region data
0x0110:
0x0120:
0x0120:
0x0130:
0x0140:
                                                     region data
                                       .....dS.?^)G
                                                   <- region data 4
     ...XJ...P...... <- region data 4
0×0150:
0x0170:
                                       ..... <- region data
           FF FF FF FF FF FF
0x0180:
     FF FF FF
                            FF FF FF FF FF
..... <- region data 6
0x01B0:
0x01C0:
     DE AD BE AF FA CE BA BE DE AD BE AF FA CE BA BE DE AD BE AF FA CE BA BE DE AD BE AF FA CE BA BE
                                       .....<- region data
0x01D0: DE AD BE AF FA CE BA BE DE AD BE AF FA CE BA BE 0x01E0: DE AD BE AF FA CE BA BE DE AD BE AF FA CE BA BE
                                       ..... <- region data
```

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PROTO BGA (DECR):

```
000000000: f5 b6 36 d3 48 d5 56 20 - 87 b9 3a fd 3b 49 ab 71 00000070: 08 40 33 b5 40 07 84 b8 - 73 3f d1 91 04 3a 1b e8 00000000: 09 03 00 15 00 00 00 00 - 39 8f 56 3b d3 c3 19 27 000000000: 42 f5 0b 2a 66 0d 31 64 - 18 f3 e3 8a 0a ab d0 be 00000000: f0 d7 47 7a a7 f4 a7 5b - 2d 09 78 48 e9 46 40 62 00000000: 00 04 00 78 00 00 00 00 - 9f 00 c1 b7 ba 85 9b f0
 0000000c0: 54 2f b8 07 3a 2e b7 c4 - 48 d0 4b 6d c8 10 4b 90 000000d0: ec 1e b0 9d e9 a3 b4 04 - ef 9d 7d b0 83 24 69 73
                                                                                                          \begin{array}{cccc} \dots & \dots & \text{is} \\ \dots & \dots & \dots & \text{I} \dots & \text{I} \\ \text{Fe} & \dots & \text{E.n.} \dots & \\ \end{array}
...VW....
                                                                                                          ..... 3. k.It <- this was refurbished ...H..v .... <- this was refurbished .... <- this was refurbished
                                                                                                          ....U..D ..wQ....
Cy.. Y.....T
 .....
 ...I.q. X...0
Y...b. .og....
....B... X.......
                                                                                                          .n.7U.z. bS.....8
5......0.A.
                                                                                                          .....6. .0...s.c
...W.Dp. j......
I..tEuf. .tKfX.B.
00000330: 49 7c 5c 74 45 75 66 c5 - 07 74 4b 66 58 84 42 d8 00000330: cb 71 a4 a8 7e 55 e7 64 - b3 24 4f 47 aa 61 31 32 00000330: 50 f8 c1 ed 64 7a 3b 0a - 40 f6 90 a1 8e 53 65 71 00000330: 78 7f 2d 89 e0 66 471 49 - 20 65 68 f9 e0 79 f7 38 00000300: 78 f2 d8 9e 06 64 71 49 - 20 65 68 f9 e0 79 f7 38 00000300: 6f 1b 9e 6d bc 58 eb ae - 3f 43 83 49 b0 0b 13 f4 000003e0: 1d 7b 48 9a f1 a3 fb 22 - 6e 00 7a 75 d8 e3 c7 47 000003f0: 6e 0e 8a ec 43 53 4a 65 - 19 8b 85 49 e0 9b 15 fe
                                                                                                         I. tEuf. tKfX.B.
.q..U.d .0G.al2
P..dz. ....Seq
.t..H. .QtB.7.
x...dqI .eh.y.8
o.m.X. .C.I...
.H.... n.zu...G
....CSJe ....
 00000000: 00 00 00 02 c0 00 00 ff - 00 00 00 00 00 00 00 00 00 00 00000010: b4 68 3b 7f ad 57 3f 0f - 23 a2 a1 e8 11 49 f4 f5
                                                                                                          .h...W.. ....I..
..... I.Fl...
t.... i.Z.6..
00000000: f0 d7 47 7a a7 f4 a7 5b - 2d 09 78 48 e9 46 40 62 0000000b0: 00 03 00 15 00 00 00 00 0 - 39 8f 56 3b d3 c3 19 27 0000000c0: 42 f5 0b 2a 06 0d 31 64 - 18 f3 e3 8a 05 d4 15 79 00000000b0: f7 68 8a df ad 9e cd 34 - b4 c7 9f a8 c6 99 82 ee
 0000000e0: 00 03 00 15 00 00 00 00 - 04 c2 14 37 09 90 c3 3b
000000f0: 24 e0 8c 2c d8 93 14 a5 - 79 58 90 51 ff ff ff ff
                                                                                                          ....... yX.Q....
```

```
.....yX.Q....
                                              ....... 3...k.It
ff
el
ff
ff
                                        49
ff
ff
                                           74
ff
.....
00000200: 00 03 00 10 00 00 00 - 00 00 00 00 00 00 00 00
1.q...D. zd.....
00000330: de ad be af fa ce ba be - 00 00 00 00 00 00 00 00 00 00 00 00000330: d1 9b db da 69 32 00 5e - 09 2f d4 8e 22 09 97 03 00000340: 01 ab 1b d6 0e 19 41 3c - 00 b6 2c 40 07 e4 ff 45
00000380: 81 91 1f e2 fc 59 b4 fb
00000390: 41 5e 91 78 d2 4f 5c 04
                          - 43 dd 31 0f 00
- 13 71 5d 09 2f
                                      96 b6
95 4f
00000330: 43 fe b6 lc 0b 6c 4d lc - 13 0b a0 42 a9 47 2d bc 000003b0: 54 f4 f5 80 b2 57 5b a2 - 34 3e 76 0b a0 3f a8 41 000003c0: c9 9f 96 8f 9b b1 f8 bc - 3b 5d 44 a0 6a 00 38 23 000003d0: a0 b8 53 24 f8 fd 34 5e - b9 64 f0 af 6e 28 4e 23 000003f0: ab f7 6c ab 06 08 d8 c9 - 91 2f f3 8d 45 fd df 39
```

RETAIL BGA:

```
7..p1.dH ....7o..
                                                                                                                                                                                                                                                                                                                                                               .....
                                                                                                                                                                                                                                                                                                                                                               ...U.... f..R....
...8...4 .v.B....
....Mw ......
...U....e...K
.j...q.. yo.....
  .....
                                                                                                                                                                                                                                                                                                                                                                . . . . . . . . . . . . . . . . . . .
.....
                                                                                                                                                                                                                                                                                                                                                               .....dS...G
                                                                                                                                                                                                                                                                                                                                                               .....
                                                                                                                                                                                                                                                                                                                                                               .....
  .....
 00000250: de ad be af fa ce ba be - de ad be af fa ce ba be 00000250: de ad be af fa ce ba be - de ad be af fa ce ba be 00000270: de ad be af fa ce ba be - de ad be af fa ce ba be 00000270: de ad be af fa ce ba be - de ad be af fa ce ba be 00000290: de ad be af fa ce ba be - de ad be af fa ce ba be 00000290: de ad be af fa ce ba be - de ad be af fa ce ba be 00000200: de ad be af fa ce ba be - de ad be af fa ce ba be 00000220: de ad be af fa ce ba be - de ad be af fa ce ba be 00000200: de ad be af fa ce ba be - de ad be af fa ce ba be 00000200: de ad be af fa ce ba be - de ad be af fa ce ba be 00000200: de ad be af fa ce ba be - de ad be af fa ce ba be 00000200: de ad be af fa ce ba be - de ad be af fa ce ba be 00000210: de ad be af fa ce ba be - de ad be af fa ce ba be 00000210: de ad be af fa ce ba be - de ad be af fa ce ba be af 00000210: de ad be af fa ce ba be - de ad be af fa ce ba be af fa ce ba be - de ad be af fa ce ba be af fa ce ba be - de ad be af fa ce ba be af fa ce ba be - de ad be af fa ce ba be af fa ce ba be af fa ce ba be - de ad be af fa ce ba be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce da be af fa ce ba de af fa ce ba be - de ad b
                                                                                                                                                                                                                                                                                                                                                               .....
                                                                                                                                                                                                                                                                                                                                                                .....
                                                                                                                                                                                                                                                                                                                                                               .....
                                                                                                                                                                                                                                                                                                                                                               .....
 000002f0: de ad be af fa ce ba be - de ad be af fa ce ba be 00000300: de ad be af fa ce ba be - de ad be af fa ce ba be 00000310: de ad be af fa ce ba be - de ad be af fa ce ba be 00000320: de ad be af fa ce ba be - de ad be af fa ce ba be 00000330: de ad be af fa ce ba be - de ad be af fa ce ba be 00000340: de ad be af fa ce ba be - de ad be af fa ce ba be 00000350: de ad be af fa ce ba be - de ad be af fa ce ba be 00000350: de ad be af fa ce ba be - de ad be af fa ce ba be 00000350: de ad be af fa ce ba be - de ad be af fa ce ba be 00000370: de ad be af fa ce ba be - de ad be af fa ce ba be 00000370: de ad be af fa ce ba be - de ad be af fa ce ba be 00000370: de ad be af fa ce ba be - de ad be af fa ce ba be 00000370: de ad be af fa ce ba be - de ad be af fa ce ba be 00000370: de ad be af fa ce ba be - de ad be af fa ce ba be 00000370: de ad be af fa ce ba be - de ad be af fa ce ba be 00000370: de ad be af fa ce ba be - de ad be af fa ce ba be 100000370: de ad be af fa ce ba be - de ad be af fa ce ba be 00000370: de ad be af fa ce ba be - de ad be af fa ce ba be 100000370: de ad be af fa ce ba be - de ad be af fa ce ba be 100000370: de ad be af fa ce ba be - de ad be af fa ce ba be 10000370: de ad be af fa ce ba be - de ad be af fa ce ba be 10000370: de ad be af fa ce ba be - de ad be af fa ce ba be 00000370: de ad be af fa ce ba be - de ad be af fa ce ba be 00000370: de ad be af fa ce ba be - de ad be af fa ce ba be 00000370: de ad be af fa ce ba be - de ad be af fa ce ba be 00000370: de ad be af fa ce ba be - de ad be af fa ce ba be 00000370: de ad be af fa ce ba be - de ad be af fa ce ba be 00000370: de ad be af fa ce ba be - de ad be af fa ce ba be 00000370: de ad be af fa ce ba be - de ad be af fa ce ba be 00000370: de ad be af fa ce ba be - de ad be af fa ce ba be 00000370: de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be - de ad be af fa ce ba be -
                                                                                                                                                                                                                                                                                                                                                                ......
                                                                                                                                                                                                                                                                                                                                                               .....
                                                                                                                                                                                                                                                                                                                                                                .....
                                                                                                                                                                                                                                                                                                                                                               00000330: 55 30 16 67 31 b5 54 d7 - 86 42 88 08 1c 52 6a 90 00000330: f1 f3 41 44 66 11 4f 8a - 7f 63 81 16 e0 f6 fa 94 00000330: la 2f 92 e5 c3 43 49 90 - 90 4d b8 c1 81 e4 dc 31 00000330: la 37 3a c8 a0 f8 7f 5d - 90 f1 74 6f 3d f9 c5 e4 000003d0: 5f 44 e7 67 81 22 2a 7d - 72 97 c5 ed 99 76 92 ee
```

```
000003e0: 52 24 8e 52 05 cb 4c 72 - 8c 5d 3d 4c f0 a2 38 00 R.R.Lr ...L..8. 000003f0: 17 40 27 9f 99 f0 3c ea - 26 7c f0 df d0 da 72 23 .....r.
```

More samples

dead link (https://dl.dropboxusercontent.com/u/35197530/bin/eeprom.bin)

Tokens

Here are documented the different types of tokens used in PS3.

All tokens are tied to EIDo, more specifically to IDPS.

Tokens enable additional repository nodes.

List

Token	Location	Size	SPU module	Description
qa_token	sc_eeprom - 0x48D3E	0x50	spu_token_processor.self	
user_token	?copied from HDD or USB storage to VTRM?	?variable?	spu_utoken_processor.self	Encrypted/Signed
token_seed	memory (temporary data)	?	?	Used to create a QA Token with EID0.

QA Token

Used internally by Sony developpers to test the console and OS.

User Token

Used to test a usermode application.

Token Seed

Unencrypted form of QA Token.

Structure

This section has to be corrected because it is almost only based on debug strings. We need to decrypt the tokens.

Token Seed

ŗ

QA Token

Size is about 0x50 bytes.

User Token

```
struct user_token_attr {
    uint32_t type; // usually 1, 0 for last attribute
    uint32_t size; // Size of this structure
    uint8_t data[0]; // size of data can be 0 */
}

struct user_token {
    uint32_t magic; // 0x73757400 = "sut\0"
    uint32_t format_version; // usually 1
    uint64_t size;
    uint8_t idps[16];
    uint64_t capability;
    uint64_t capability;
    union {
        struct user_token_attr attribute[0];
        uint8_t dummy[0xC00];
    } attributes;
    /* 0xC30 */
    uint8_t digest[0x14]; // certainly SHA-1
}
```

Offset	Size	Description
?	?	m_magic
?	?	m_format_version
?	?	m_size
?	?	m_idps
?	?	m_expire_date
?	?	m_capability
?	?	m_attribute
?	?	m_digest

For every attribute in the token:

Offset	Size	Description
?	?	attr:m_type
?	?	attr:m_size
?	?	attr:m_data

Dumping SC EEPROM - hardware way

Warning

You can use this method at your own risk. Author is not responsible for any hardware damages and failures.

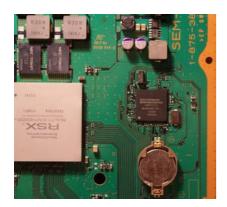
Bus Pirate 3 Solderless method

Requirements

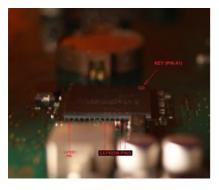
- 1) PS3 motherboard with BGA syscon chip (COK001, COK002, SEM001, DIA001, etc)
- 2) Bus Pirate v3.6 with connectors.
- 3) Wires (I used AWG32 150mm with tinned ends, see below)
- 4) PC with OS Windows7 (Should work on other windows systems, but not tested).
- 5) A sharp pencil.
- 6) Fingers;)

Hardware Part

Find the Syscon on your PS3 motherboard.



Look at the SC EEPROM Pins location and Draw serifs on the upper surface of the chip, strictly on these pins using pencil.



Draw the Pinout for convenience.



Connect Bus-Pirate and AWG32 wires using probe Kit



Connect Bus-Pirate to the SC EEPROM pins using the following table:

Bus Pirate pin	SC EEPROM pin
CLK	SKB
cs	CSB
MOSI	DI
MISO	DO
3V3	RBB
GND	Any Ground Point

Use one finger to hold the wires. The wires should be well connected with the SC EEPROM pins.



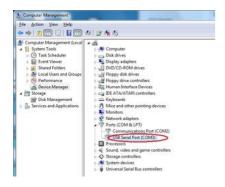
Connect Bus-Pirate to your PC with Windows 7 by USB.

Software Part

Big thanks to $\bf Dasanko$ for the hard work and for the Syscon Flasher GUI Tool!!!

Download and Install the driver for Bus-Pirate.

Open the Device Manager and find the port number of your USB Serial Port.



Download and Run Syscon Flasher.exe

 $Download\ link: \underline{https://www.sendspace.com/file/es86dh}$

MD5=D59A8AA9E7BB1AEB753D7C6391CE17B1



There are 6 simple steps to obtain the dump.

- 1) Select the correct USB Serial Port for Bus-Pirate.
- 2) Press "Send settings to device" button. If done correctly, then "Mode" Led on the Bus Pirate will be Green.
- 3) Press "Power on" button. If done correctly, then "VREG" Led on the Bus Pirate will be Red.
- 4) Press "Browse" button and specify location and file name for your SC EEPROM dump.
- 5) Specify Offset and Length. Offset=o Length=ox8000 for full dump of the SC EEPROM.
- 6) Press "Fast Read" button and wait about 15sec.
- 7) Enjoy

 $\label{limited} \mbox{My dump, for example: download link (https://mega.co.nz/\#!E1kHgSZJ!4e7TdNLdkQQzinwlnRO2KmaBdoGeBliHuHFe2tkmBgQ)} \\ \mbox{My dump, for example: download link (https://mega.co.nz/\#!E1kHgSZJ!4e7TdNLdkQQzinwlnRO2KmaBdoGeBliHuHFe2tkmBgQ)} \\ \mbox{My dump, for example: download link (https://mega.co.nz/\#!E1kHgSZJ!4e7TdNLdkQQzinwlnRO2KmaBdoGeBliHuHFe2tkmBgQ)} \\ \mbox{My dump, for example: download link (https://mega.co.nz/#!E1kHgSZJ!4e7TdNLdkQQzinwlnRO2KmaBdoGeBliHuHFe2tkmBgQ)} \\ \mbox{My dump, for example: download link (https://mega.co.nz/#!E1kHgSZJ!4e7TdNLdkQZinwlnRO2KmaBdoGeBliHuHFe2tkmBgQ)} \\ \mbox{My dump, for example: download link (https://mega.co.nz/#!E1kHgSZJ!4e7TdNLdkQZinwlnRO2KmaBdoGeBliHuHFe2tkmBgQ)} \\ \mbox{My dump, for example: download link (https://mega.co.nz/#!E1kHgSZJ!4e7TdNLdkQZinwlnRO2KmaBdoGeBliHuHFe2tkmBgQ)} \\ \mbox{My dump, for example: download link (https://mega.co.nz/#!E1kHgSZJ!4e7TdNLdkQZInwlnRO2KmB$

Bus Pirate 3 method by: (ZeroTolerance)

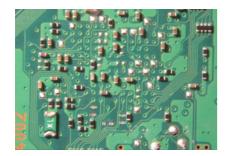
Requirements

- 1) PS3 motherboard. I am using DIA-001. Maybe we can dump it from another motherboard, but it is unknown yet.
- ${\bf 2)}\ Device\ that\ can\ work\ with\ SPI\ interface\ and\ send\ any\ commands.\ I\ am\ using\ a\ Bus\ Pirate\ v3.6\ with\ connectors.$
- 3) Soldering station.
- 4) Wires (see below).
- $5) \ Personal \ conputer \ with \ installed \ HxD, \ Putty \ (or \ other \ terminal \ supporting \ serial \ port \ connections, \ e.g.: Tera \ Term) \ , \ Notepad++$
- 6) /dev/Hands ;)

Preparation

Find the test points on the motherboard using the picture corresponding to your motherboard.

for DIA-001:



for DECR-1400:



All points are covered with varnish. You need to carefully remove the varnish to the copper and solder the wires to it.

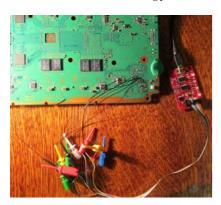
Attach a Bus Pirate to the wires using the following table:

Bus Pirate pin	Test Point
CLK	SKB
CS	CSB
MOSI	DI
MISO	DO
3V3	RBB
GND	WCB, Any Ground Point

Make sure that the battery is attached to the motherboard.

Plug your Bus Pirate to the USB port on your PC using mini_USB_to_USB cable. (I am using the cable from the ps3 gamepad)

It should be done like on the following picture:



Setup software

1) Install the driver for the Bus Pirate and setup your virtual COM port for it using the following table:

Parameter	Value	
Bits per second	115200	
Data bits	8	
Parity	None	
Stop bits	1	
Flow control	None	

Open Putty and set it up:

session params:

connection type: Serial

Serial line: COM3 (choose your virtual COM port)

Speed (baud rate): 115200

Logging params:

Session logging: All session output

 $Log \ file \ name: click \ Browse \ button \ and \ specify \ your \ log file, for \ example: C:\ PS3 \setminus Logs \setminus log file. log \ file \ log file \ for \ example: C:\ PS3 \setminus Logs \setminus log file. log \ file \ for \ example: C:\ PS3 \setminus Logs \setminus log file. log \ file \ for \ example: C:\ PS3 \setminus Logs \setminus log file. log \ file \ for \ example: C:\ PS3 \setminus Logs \setminus log file. log \ for \ file \ file \ for \ file \ file \ file \ for \ file \ f$

Now click Open button and setup mode for bus pirate using following commands:

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Command	Description
m	(mode)
5	(SPI)
4	(Set speed: 1MHz)
2	(Clock polarity: Idle high)
1	(Output clock edge: Idle to active
1	(Input sample phase: Middle *default)
2	(CS: /CS)
2	(Select output type: Normal (H=3.3V, L=GND))

Enable 3.3V: Just type: W (big leter) and press Enter.

Obtain the dump

Type: [oxa8 oxoo oxoo r:32768] and press Enter.

Wait until dumping process will be completed and close Putty.

Thats all. The dump must be into your logfile.log

You can use Notepad++ and Hex Editor like HxD to convert the dump to binary format.

Read Command is oxA8 oxXX oxXX, XX XX is a block id to be read, the full SC EEPROM is 32768 bytes length (ox8000), [r:] are syntax command of the Bus Pirate for start, read byte and end

Arduino Mega method by: (Abkarino)

I had build my own Syscon EEPROM flasher based on open source hardware "Arduino Mega" and some resistors.

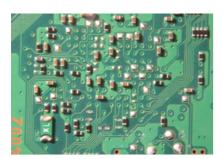
This flasher will allow you to fully read/write to your Syscon EEPROM (FAT consoles only till now).

Requirements

- 1) PS3 motherboard. I had used **SEM-0001** board by desoldering Syscon chip form it but you can use, **DIA-001** for example without desoldering Syscon chip since all SC EEPROM pins had a test points in the board it self.
- 2) Arduino Mega or any Arduino board.
- $3) \ 6 \ x \ 3.6 \ KOHM \ resistors + 6 \ x \ 1.8 \ KOHM \ resistors (work \ together \ as \ a \ voltage \ dividers \ since \ Arduino \ IO \ logic \ is \ 5.0v \ and \ Syscon \ EEPROM \ IO \ Logic \ is \ 3.3v).$
- 4) Soldering station.
- 5) Wires & Bread board (optional).
- $6) Any \ PC \ that \ have \ terminal \ software \ like \ Putty, \ Real Term \ and \ so \ on \ to \ access \ serial \ port, \ and \ any \ Hex \ Editor \ like \ HxD.$

Preparation

Find the test points on the motherboard using this picture.



Or if you have very good soldering skills and tools to desolder your SysCon then you can desolder your SysCon and solder your wires to it directly.

All points are covered with varnish. You need to carefully remove the varnish to the copper and solder the wires to it.

Attach a Arduino Mega to the wires using the following table:

Arduino Mega pin	Test Point	Syscon Pin
SCL (52)	SKB	E16
SS (53)	CSB	F16
MOSI (51)	DI	G16
MISO (50)	DO	H16
WP (48)	WCB	J15
RB (49)	RBB	J16
VCC (3.3v)	Not needed if you used battery power	G11
GND	Any Ground Point	C15

- Make sure that the battery is attached to the motherboard if you will dump/flash SysCon EEPROM in board.
- Make sure the pins are compatible or edited if using other arduino Board.
- Arduino Mega: MISO is 50, MOSI is 51, SCK is 52 and SS is usually 53
 - Arduino Leonardo: the SPI pins are on the ICSP header pins
 - Arduino Duemilanove/Uno: SS is digital 10, MOSI is 11, MISO is 12, SCK is (usually) 13

Wiring Diagram & Photos



PS3 SysCon EEPROM Flasher



Fat SysCon Desoldered And Attached To Arduino



SysCon EEPROM Dumping Process

Arduino Sketch Source Code

 $Here is my Arduino Mega sketch source code to allow you to read/write/erase PS3 Syscon EEPROM. \\ \underline{dead link (http://pastie.org/10004682\#8,19)}$

v·e (https://www.psdevwiki.com/ps3/edit/Template:Reverse_engineering) Reverse engineering Bluedisk EID0 reDRM · Boot Order · Bugs & Vulnerabilities · Dumping Bootldr · Dumping Metldr · Files on the PS3 · KaKaRoTo_Kind_of_'Jailbreak' · PS3Cobra Payload Reverse Engineering · PS3UserCheat · QA Flagging · ReDRM / Piracy dongles · General Revoke List · RSOD Fix · rtcalarm.dat · Whitelisting · VTRM Hypervisor Hypervisor Reverse Engineering · Repository Nodes Appliance Information Manager · AV Manager · Dispatcher Manager · Factory Data Manager · Indi Info Manager · SB Manager · SC Manager · Secure LPAR Loader · Secure Profile Loader · Secure RTC Manager · Security Policy Manager · Storage Manager · Services Update Manager · Updater Frontend · USB Dongle Authenticator · User Token Manager · Virtual TRM Manager ap_plugin · audiop_alugin · audiop_plugin dummy · audiop_plugin mini · auth_plugin · autodownload_plugin · autoupdateconf_plugin · avc_plugin · avc_util · avc2_game_plugin · avc2_game_video_plugin · avc2_text_plugin · bdp_disccheck_plugin · bdp_plugin · bdp_storage_plugin · campaign_plugin · category_setting_plugin · comboplay_plugin · Plugin Interfaces custom_render_plugin · bap_plugin · bap_storage_plugin · campaign_plugin · category_setting_plugin · comboplay_plugin · custom_render_plugin · data_copy_plugin · deviceconf_plugin · dina_plugin · download_plugin · dtopip_util · edy_plugin · esebttp · eseibrd · eseibld · eselock · eula_cddb_plugin · eula_hcopy_plugin · eula_net_plugin · explore_plugin · explore_plugin in explore_plugin · explore_plugin · game_ext_plugin · friendfml_plugin · friendfml_plugin · friendfrophy_plugin · game_ext_plugin · game_indicator_plugin · game_plugin · gamedata_plugin · gamelib_plugin · gameupdate_plugin · hknw_plugin · idle_plugin · impose_plugin · kensaku_plugin · msgdialog_plugin · mtpinitiator_plugin · musicbrowser_plugin · nas_plugin · netconf_plugin · newstore_plugin · np_eula_plugin · np_matching_plugin · np_multisignin_plugin · np_sns_plugin · npsignin_plugin · np_trophy_ingame · np_trophy_plugin · osk · oskfullkeypanel · oskpanel · pesm_plugin · photo_network_sharing_plugin · photolist_plugin · photolist_plugin · photolist_plugin · pnotelist_plugin · ps3_savedata_plugin · ps3_savedata_plugin_game · ps3_savedata_plugin_psp · rec_plugin · regcam_plugin · remotedownload_plugin · sacd_plugin · scenefolder_plugin · screenshot_plugin · software_update_plugin · soundvisualizer_plugin · strviewer_plugin · $sysconf_plugin \cdot system_plugin \cdot thumthum_plugin \cdot upload_util \cdot user_info_plugin \cdot user_plugin \cdot videodownloader_plugin \cdot upload_util \cdot user_info_plugin \cdot user_plugin \cdot upload_util \cdot user_info_plugin \cdot user_plugin \cdot user_$

videoeditor_plugin · videoplayer_util · videoplayer_plugin · vmc_savedata_plugin · wboard_plugin · webbrowser_plugin · $webbrowser_service \cdot webrender_plugin \cdot xai_plugin \cdot xmb_ingame \cdot xmb_plugin$ PS1 Emulation · PS2 Emulation · PSP Emulation Emulation Printer support · Remote Play · String Viewer · Web Browser · XMB In-game background music · PS3 and PSVita Cross Functions · Widgets · Life with PlayStation · PlayView · XMB Manuals Extended features Online Consoleban · Environments · Online Connections · PSN · PSN Handshake Signup · X-I-5-Ticket CELL Cell Configuration Ring · CELL Reset Exploit · CellBE Hardware Implementation Registers · Unlocking the 8th SPE · SPU Isolated Modules Reverse Engineering · SPU LS Overflow Exploit $\textbf{RAM} \quad \underline{\textbf{XDR Configuration}} \cdot \underline{\textbf{Rambus Registers}}$ Hardware SB ENCDEC Device Reverse Engineering HDD Encryption BD Bluray disc · Basic Bluray disc authentication procedure · BD Drive Reverse Engineering · Disc Identification/Serialization Data · ODE · Remarry Bluray Drive $IDA \ pro \ disassembler \ and \ debugger \cdot CCAPI \cdot 0x000EAEB0 \cdot Ps3.xml \cdot Nids.txt \cdot Nids \ all.txt \cdot Unknown \ nids.txt \cdot Fnids.idh$ Tools files emer_init · aim spu module · lv1 · hdd_copy · eurus_fw · lv0 · factory data mngr Strings dumps Iv2 dump (Rebug 4.46) · Iv1 dump (Rebug 4.46) · bootldr dump (2.70) · Network Loading of Iv1Idr and above executables Reference Archaic · Drk notes · Canaries Keys · Per Console Keys · Seeds · ECDSA binaries · AES binaries · DES binaries · Cryptography Tricks Keys & Seeds

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