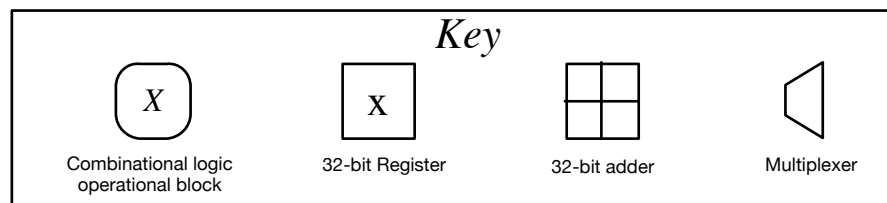
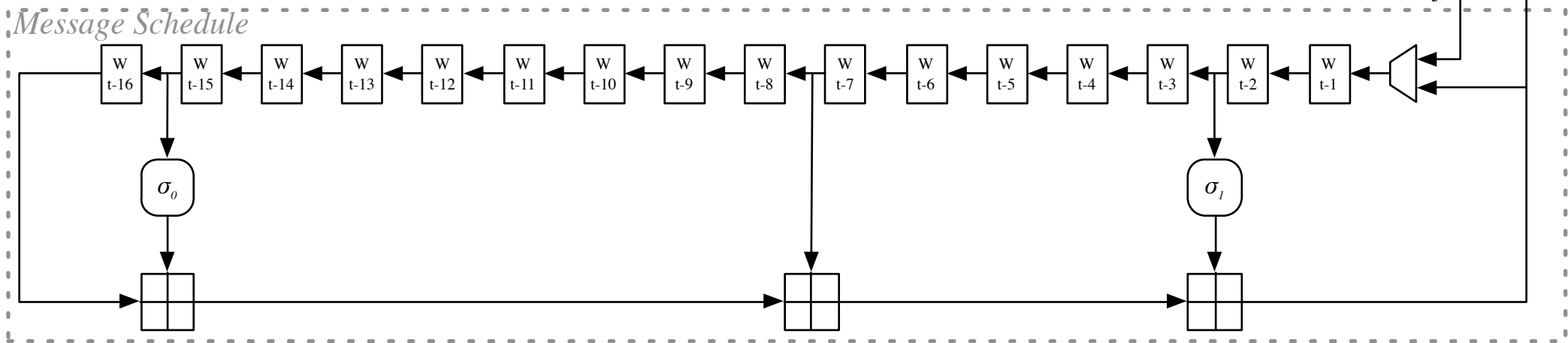


# *SHA-256 Block Diagram*



$X$   
Combinational logic  
operational block

$X$   
32-bit Register

$X$   
32-bit adder

$X$   
Multiplexer