

CHIH-HSIANG CHEN

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<https://chenchihhsiang.github.io/>

EDUCATION

- [University of California, Davis \(2020 - 2021\)](#)
 - Master of Science in Computer Science, GPA: 3.83
- [National Sun Yat-Sen University \(2017 - 2019\)](#)
 - Master of Science in Electrical Engineering, GPA: 3.89
- [Chung Yuan Christian University \(2013 - 2017\)](#)
 - Bachelor of Science in Electrical Engineering, GPA: 3.64

WORK EXPERIENCE

[Firmware Engineer, Solidigm \(Formerly Intel SSD Dept.\) — 2022 Feb. - 2023 Aug.](#)

- Designed and validated product firmware in the Firmware Engineering department using C language
- 6% performance improvement
- Successfully resolved bugs between architecture and firmware calculation
- Led discussions on implementing a new and important feature to SSD products
- Mentored new employees from other teams, specifically the Validation team
- Designed test plans for firmware validation using YAML and Python
- Responsible for verification of memory subsystem design and architecture with advanced verification methodologies
- Experience in customer service
- Had experience in the entire product life cycle

[Co-Op / Intern Engineer \(CSIM\), Advanced Micro Devices — 2021 Sep. - 2021 Dec.](#)

- Contributed to the Graphic Pipeline
- Optimized programs to reduce memory usage by 40%
- Familiar with Doxygen, Linux Environment, and Google Test Framework Setup

[Project Manager, Self-own Studio — 2015 - 2019.](#)

- Planer and advisor
- Project delivery (on-time, on-budget, high-quality)
- Post-sales support (customer service)
- Design-in → Design-win
- Customers: Taiwan Yamaha Motor, Suzuki Motor, Ministry of Economic Affairs ROC, etc.

SKILLS AND LANGUAGE

- [Computer Science](#)
 - Linux Kernel, Operating System, RTOS, Data Structure
 - C Programming Language, Python
- [Electrical Engineering](#)
 - Communication System, Embedded System, Circuit Design
- [Language](#)
 - Chinese (Native); English (Fluent)

HONORS AND AWARDS

- [Master's Thesis \(2019\)](#)
 - Bettering of Supervisory Control System and Communication for Direct Grid-Connected Excited Synchronous Wind Power Generators with Servo Motor Control.
 - Designed and implemented a **remote control system**, **UI**, and **database** for a turbine generation system
 - Configured communication environments using **UART**, **MODBUS**, and **RS-485**
 - Improved system performance and efficiency by reducing transmission time by **72%**
 - Collaborated with team members to troubleshoot and resolve technical issues
 - **Skills:** C, C#, MySQL, Circuit Design, UART, SPI, I2C, MODBUS, SoC, System Integration, TI DSP