

## Marking Scheme for CIS 415 Project 2

### Design (40)

- design diagram showing dataflows and interactions in your system (16)
- appropriate sizing of bounded buffers/other data structures (4)
- appropriate behaviour when there is a shortage of PacketDescriptor's for receiving packets (4)
- explanation of blocking behaviour matching the requirements (8)
- novel design features (8)

### Implementation (60)

- honest statement of the state of the solution (10)
- *workable* implementation (30)
  - appropriate synchronization (6)
  - appropriate number and initialization of threads (4)
  - appropriate return of PacketDescriptor's (4)
  - low complexity search for next PacketDescriptor associated with the redeeming thread (6)
  - appropriate initialization of data structures (6)
  - sufficient commentary in the code (4)
- *working* implementation (10)
- excellent commentary (10)

### TOTAL (100)