Cheng-Hsiang Chiu

https://cheng-hsiang-chiu.github.io/

**EDUCATION** 

• University of Utah

Ph.D. in Electrical and Computer Engineering

Salt Lake City, USA

Mobile: +1-657-348-3118

Email: cheng-hsiang.chiu@utah.edu

Aug. 2020 - Present

• École Polytechnique Fédérale de Lausanne

Master of Science in Computer Science

Lausanne, Switzerland

Sep. 2013 - Feb. 2016

• National Chiao Tung University

Master of Science in Communication Engineering

Hsinchu, Taiwan

Sep. 2005 - Aug. 2007

• National Chung Cheng University

Bachelor of Science in Electrical Engineering

Chiayi, Taiwan Sep. 2001 – Jun. 2005

Ongoing Projects

• Taskflow: Developing a pipeline scheduling framework (Pipeflow) on top of Taskflow (https://taskflow.github.io).

• syclFlow: Leveraging a task graph algorithm of CUDA graph into a SYCL runtime.

EXPERIENCE

• UiT

• Cadence Texas, USA

Software Intern

May 2021 - Aug. 2021

• Buffer Insertion Acceleration: Accelerated the executions of buffer insertion algorithm by 16%.

Build institution received the executions of same institution algorithm by 10%.

Doctoral Researcher Feb. 2019 - Dec. 2019

• Edge computing: Implemented an energy efficient framework to classify Arctic wild animals in-situ.

• Power data: Performed data cleansing and developed visualization framework of power data in Tromso, Norway.

• University of Khalifa

Abu Dhabi, UAE

Tromso, Norway

 $Assistance\ Researcher$ 

Jan. 2018 - Nov. 2018

- Graphene: Automated python-meep for materials modeling and designed data visualization frameworks for it.
- Sand classification: Developed classification techniques to obtain the components of sands in Nigeria.

• CERN Geneve, Switzerland

Software Developer Mar. 2015 - Aug. 2015

• Consistency checking: Developed a kernel package to discover devices and perform consistency checking.

## **Publications**

- C.H. Chiu, T. W. Huang, Z. Guo, and Y. Lin, "Pipeflow: An Efficient Task-Parallel Pipeline Programming Framework using Modern C++," *Arxiv*, https://arxiv.org/abs/2202.00717.
- C.H. Chiu, D.L. Lin, and T.W. Huang, "An Experimental Study of SYCL Task Graph Parallelism for Large-Scale Machine Learning Workloads," *Euro-Par*, 2021.
- One submitted in HPC, two in WSN, two in load balancing, two in computer vision.

## SKILLS

• Language: C, C++, Python, Javascript, HTML, SQL

Unit Test: doctestProfiler: gprof, perf

• Programming Model: Taskflow, SYCL, oneTBB (Pipeline)