



1

2

3

4

A

A

B

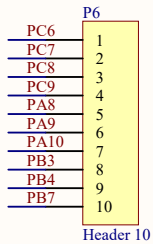
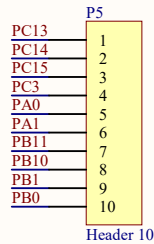
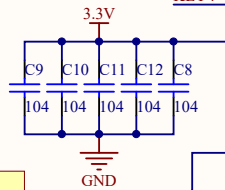
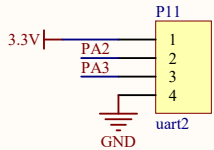
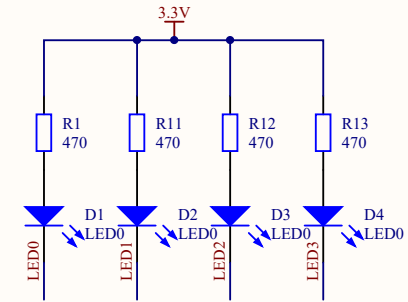
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C

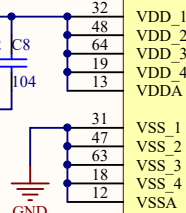
C

D

D



PA0	14	PA0/WKUP/USART2_CTS/ADC0/TIM2_CH1
PA1	15	PA1/USART2_RTS/ADC1/TIM2_CH2
PA2	16	PA2/USART2_TX/ADC2/TIM2_CH3
PA3	17	PA3/USART2_RX/ADC3/TIM2_CH4
CS	20	PA4/SPI1_NSS/USART2_CK/ADC4
SCK	21	PA5/SPI1_SCK/ADC5
MISO	22	PA6/SPI1_MISO/ADC6/TIM3_CH1
MOSI	23	PA7/SPI1_MOSI/ADC7/TIM3_CH2
PA8	41	PA8/USART1_CK/TIM1_CH1/MCO
PA9	42	PA9/USART1_TX/TIM1_CH2
PA10	43	PA10/USART1_RX/TIM1_CH3
USB_DM	44	PA11/USART1_CTS/USBDM/CAN_RX/TIM1_CH4
USB_DP	45	PA12/USART1_RTS/USBDM/CANTX/TIM1_ETR
SWDIO	46	PA13/JTMS_SWDAT
SWCLK	49	PA14/JTCK_SWCLK
LED0	50	PA15/JTDI/TIM2_CH1/SPI1_NSS
PB0	26	PB0/ADC8/TIM3_CH3
PB1	27	PB1/ADC9/TIM3_CH4
PB3	55	PB3/JTDO/TIM2_CH2/SPI1_SCK
PB4	56	PB4/JRST/TIM3_CH1/SPI1_MISO
CAN2_RX	57	PB5/TIM3_CH2/SPI1_MOSI
CAN2_TX	58	PB6/I2C1_SCL/TIM4_CH1/USART1_TX
PB7	59	PB7/I2C1_SDA/TIM4_CH2/USART1_RX
CANRX	61	PB8/TIM4_CH3/I2C1_SCL/CANRX
CANTX	62	PB9/TIM4_CH4/I2C1_SDA/CANTX
PB10	29	PB10/I2C_SCL/USART3_TX/TIM2_CH3
PB11	30	PB11/I2C_SDA/USART1_RX/TIM4_CH2
KEY1	33	PB12/SPI2_NSS/I2C2_SMBAL/USART3_CK/TIM1_BK1N
KEY2	34	PB13/SPI2_SCK/USART3_CTS/TIM1_CH1N
KEY3	35	PB14/SPI2_MISO/USART3_RTS/TIM1_CH2N
KEY4	36	PB15/SPI2_MOSI/TIM1_CH3N



STM32

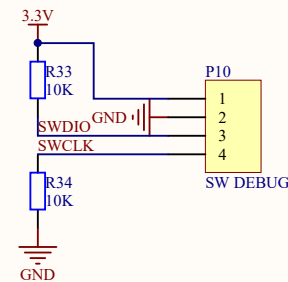
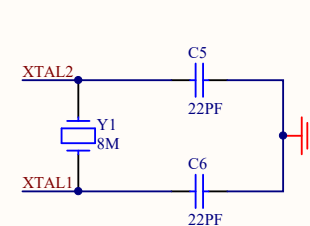
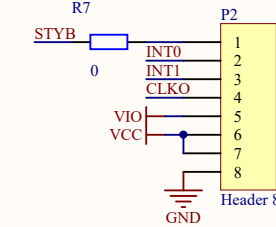
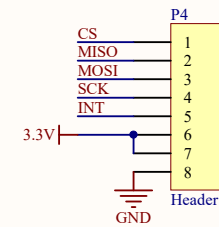
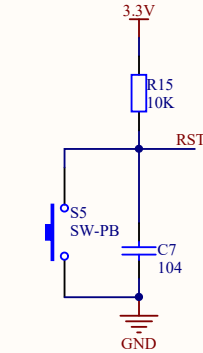
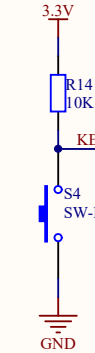
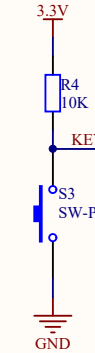
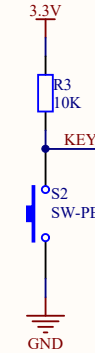
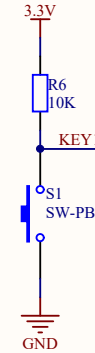
STM32F105R8

PC0/ADC10	8	INT1	RX_INT
PC1/ADC11	9	INT0	TX_INT
PC2/ADC12	10	STYB	
PC3/ADC13	11	PC3	
PC4/ADC14	24	INT	
PC5/ADC15	25	USB_EN	
PC6	37	PC6	
PC7/TIM3_CH1	38	PC7	
PC7/TIM3_CH2	39	PC8	
PC8/TIM3_CH3	40	PC9	
PC9/TIM3_CH4	51	LED1	
PC10/USART3_TX	52	LED2	
PC11/USART3_RX	53	LED3	
PC12/USART3_CK	2	PC13	
PC13/ANTI_TAMP	3	PC14	
PC14/OSC32_IN	4	PC15	
PC15/OSC32_OUT			

PD0/OSC\_IN  
PD1/OSC\_OUT  
PD2

BOOT0

NRST



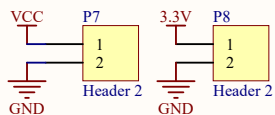
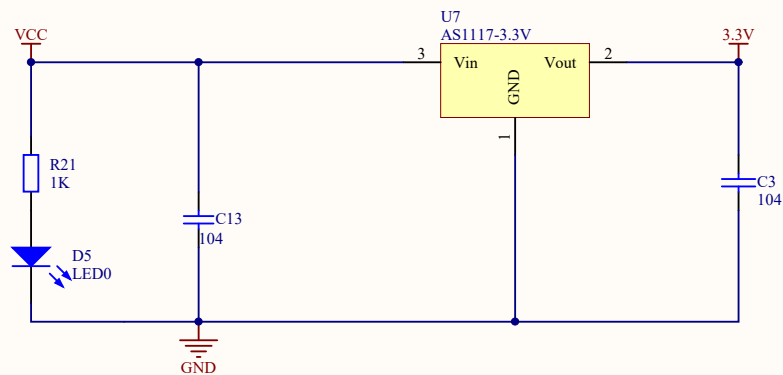
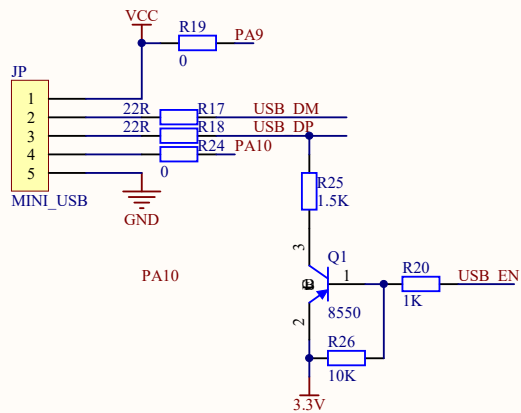
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Title		
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