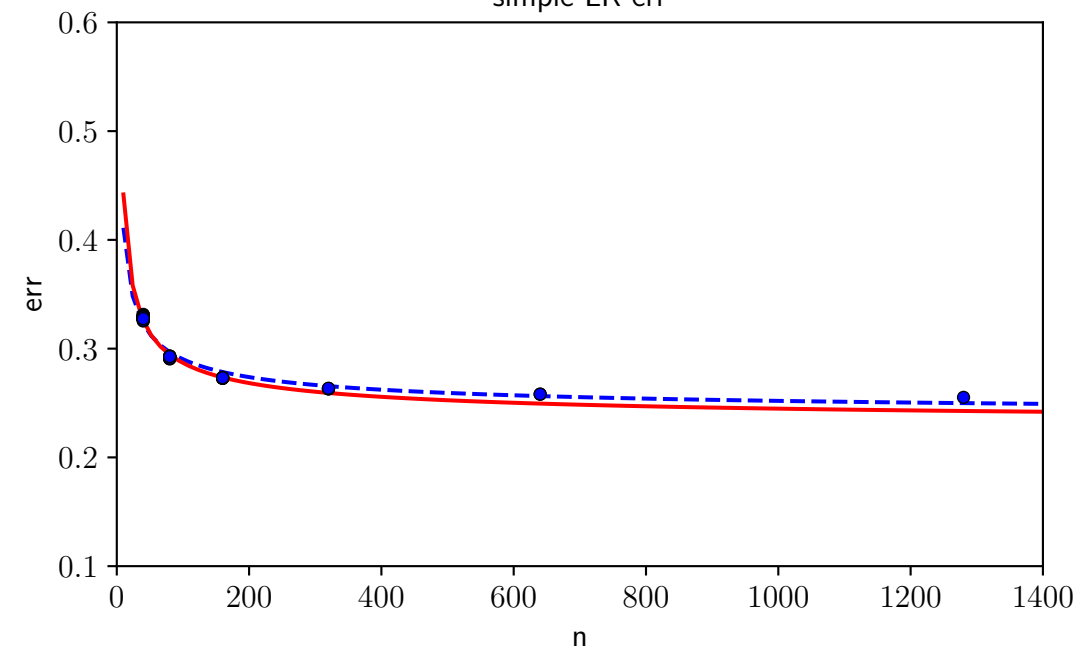
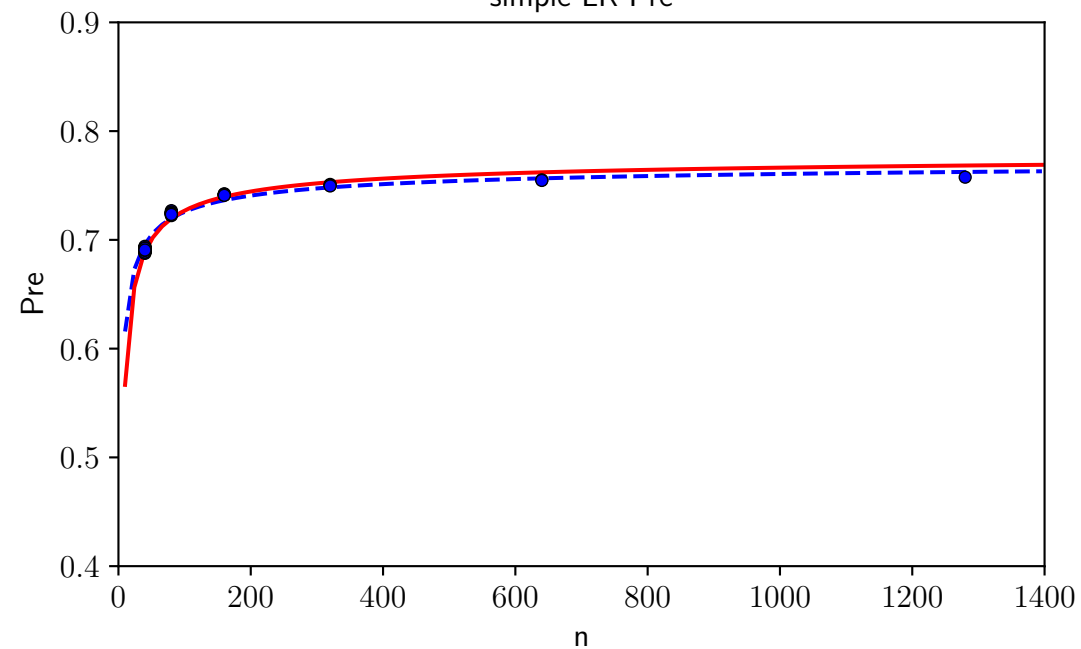


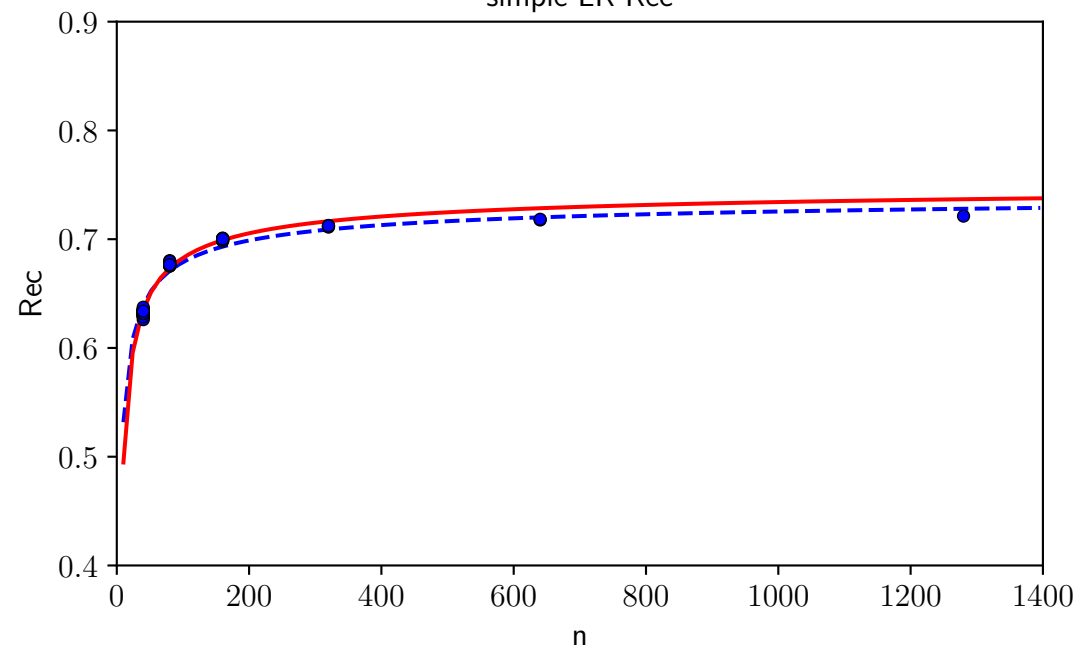
simple-LR-err



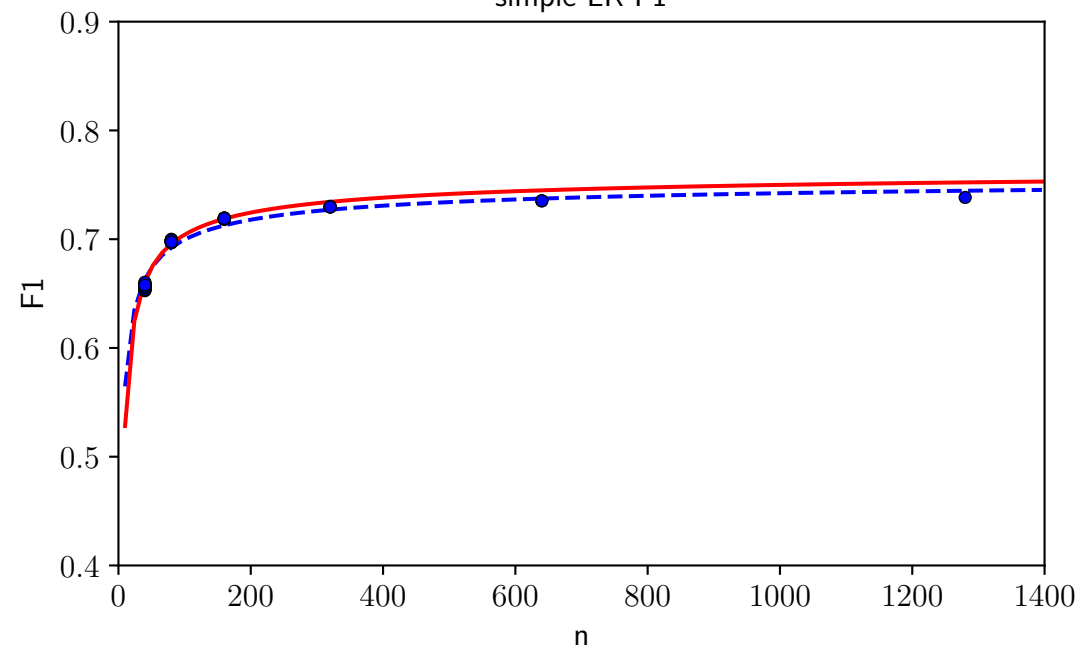
simple-LR-Pre



simple-LR-Rec



simple-LR-F1



● test err (Pre, Rec, F1)    - - - POW3    — proposed