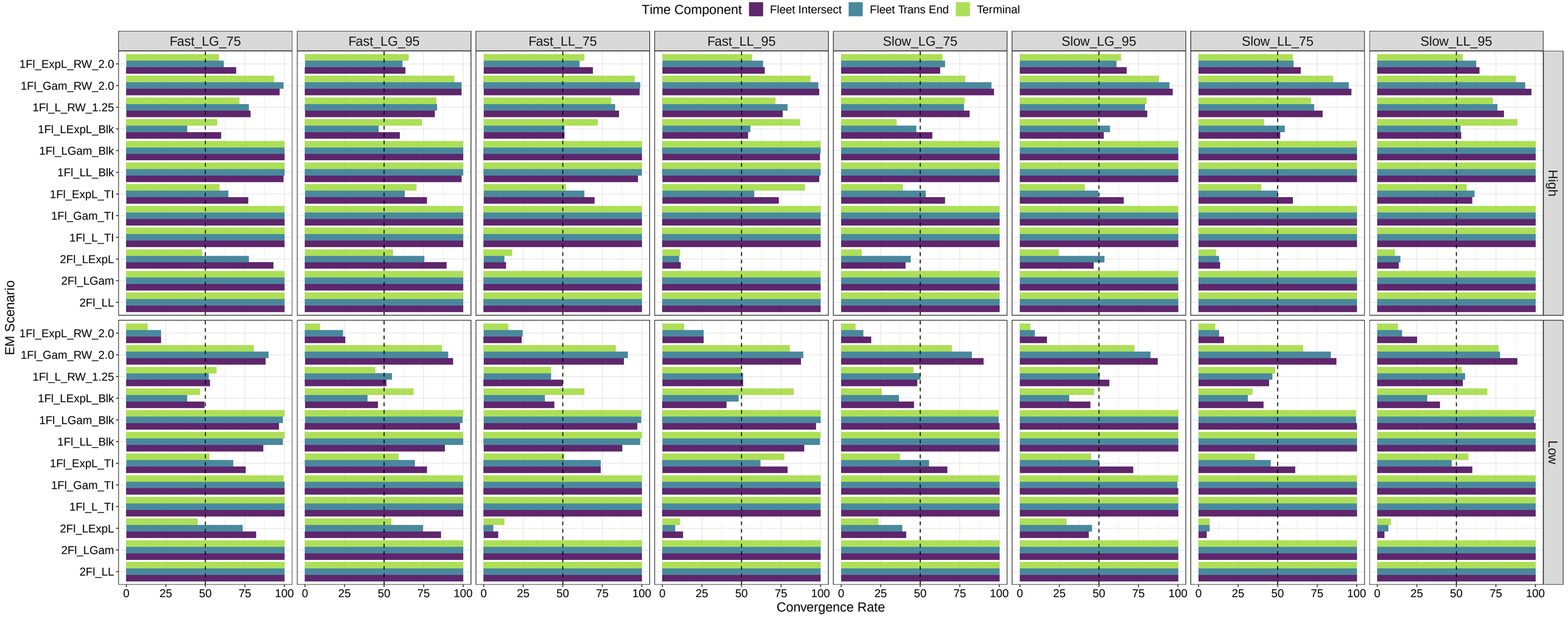
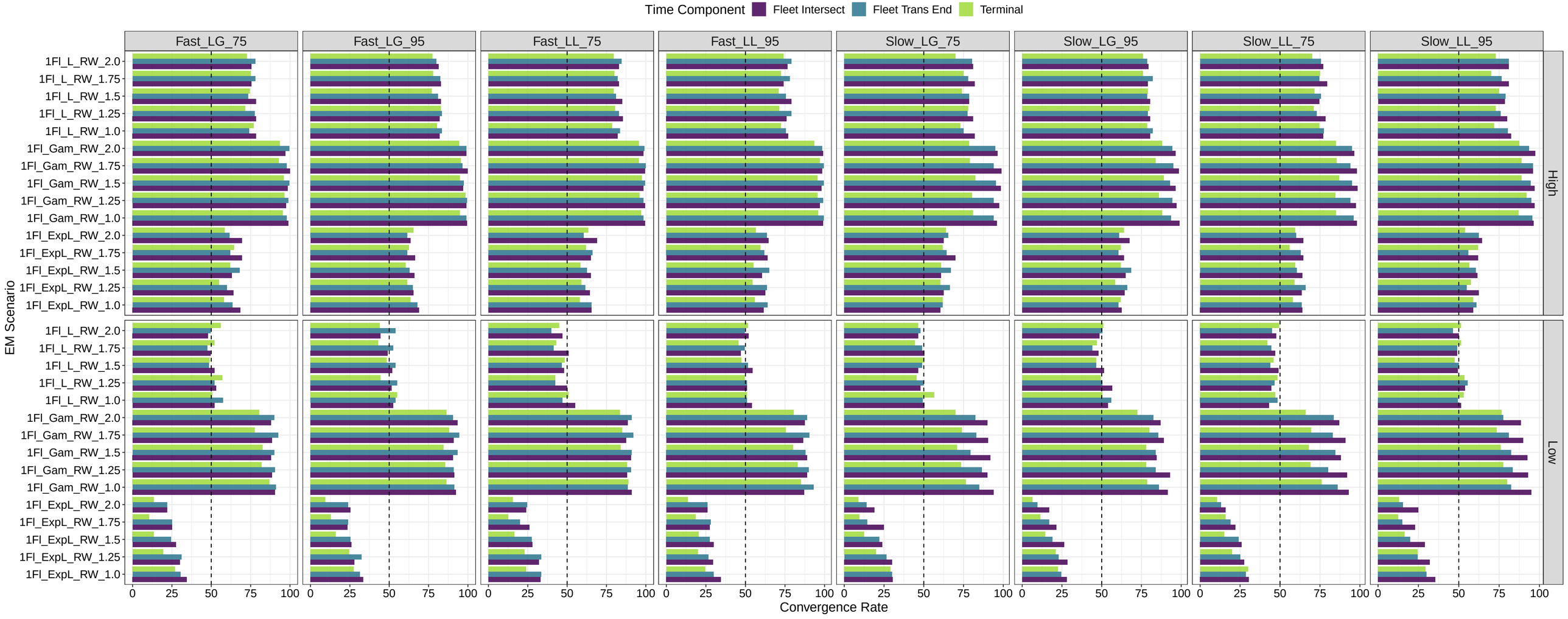

Time Component
Fleet Intersect
Fleet Trans End
Terminal
Fast_LG_75
Fast_LG_95
Fast_LL_75
Fast_LL_95
Slow_LG_75
Slow_LG_95
Slow_LL_75
Slow_LL_95
High
Low
EM Scenario
1Fl_ExpL_RW_2.0
1Fl_Gam_RW_2.0
1Fl_L_RW_1.25
1Fl_LExpL_Blk
1Fl_LGam_Blk
1Fl_LL_Blk
1Fl_ExpL_TI
1Fl_Gam_TI
1Fl_L_TI
2Fl_LExpL
2Fl_LGam
2Fl_LL
0
25
50
75
100
Convergence Rate


Time Component
Fleet Intersect
Fleet Trans End
Terminal
Fast_LG_75
Fast_LG_95
Fast_LL_75
Fast_LL_95
Slow_LG_75
Slow_LG_95
Slow_LL_75
Slow_LL_95
High
Low
EM Scenario
1Fl_L_RW_2.0
1Fl_L_RW_1.75
1Fl_L_RW_1.5
1Fl_L_RW_1.25
1Fl_L_RW_1.0
1Fl_Gam_RW_2.0
1Fl_Gam_RW_1.75
1Fl_Gam_RW_1.5
1Fl_Gam_RW_1.25
1Fl_Gam_RW_1.0
1Fl_ExpL_RW_2.0
1Fl_ExpL_RW_1.75
1Fl_ExpL_RW_1.5
1Fl_ExpL_RW_1.25
1Fl_ExpL_RW_1.0
0
25
50
75
100
Convergence Rate