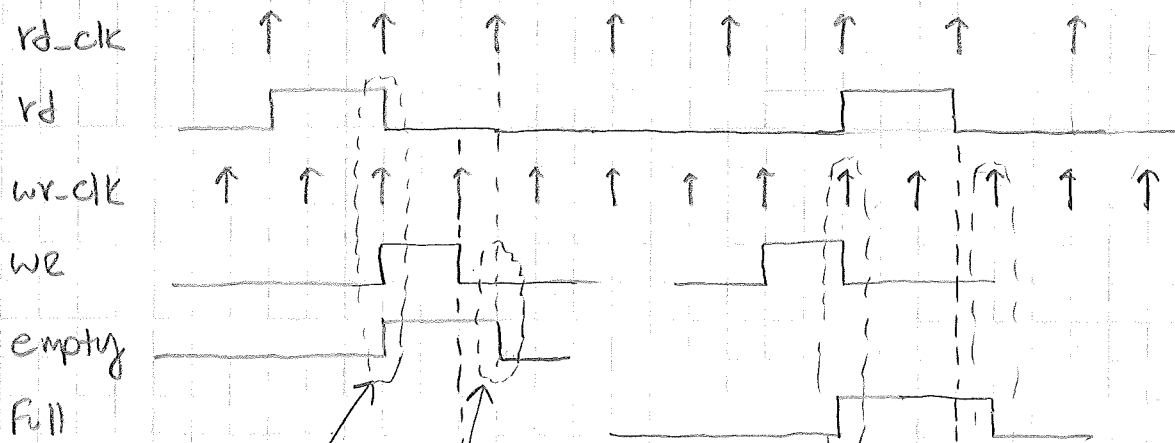


# Fifo, full & empty flag timing



Assertion of empty is naturally sync. to rd\_clk

deassertion of empty must be synchronized to rd\_clk.

Actually not empty here

Assertion of full is naturally sync. to wr\_clk

deassertion of full must be synchronized to wr\_clk

Actually not full here