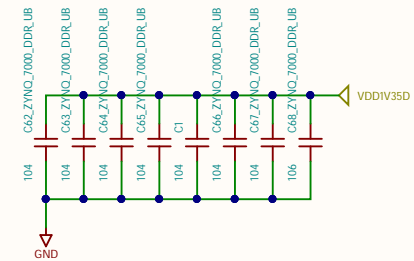
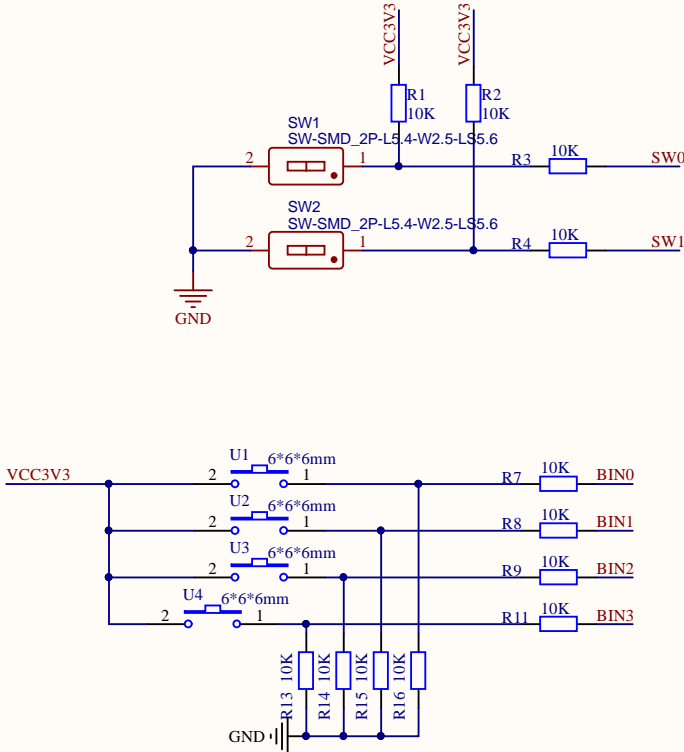
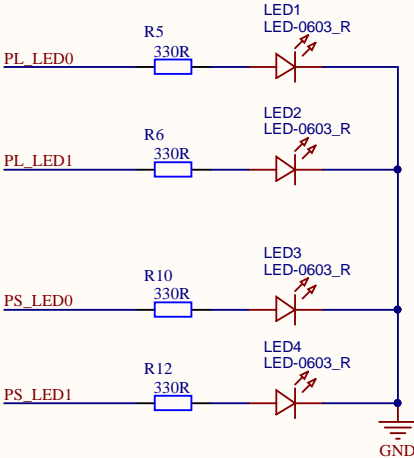
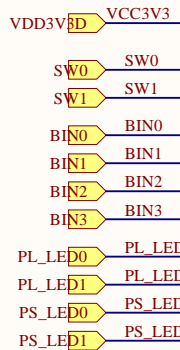
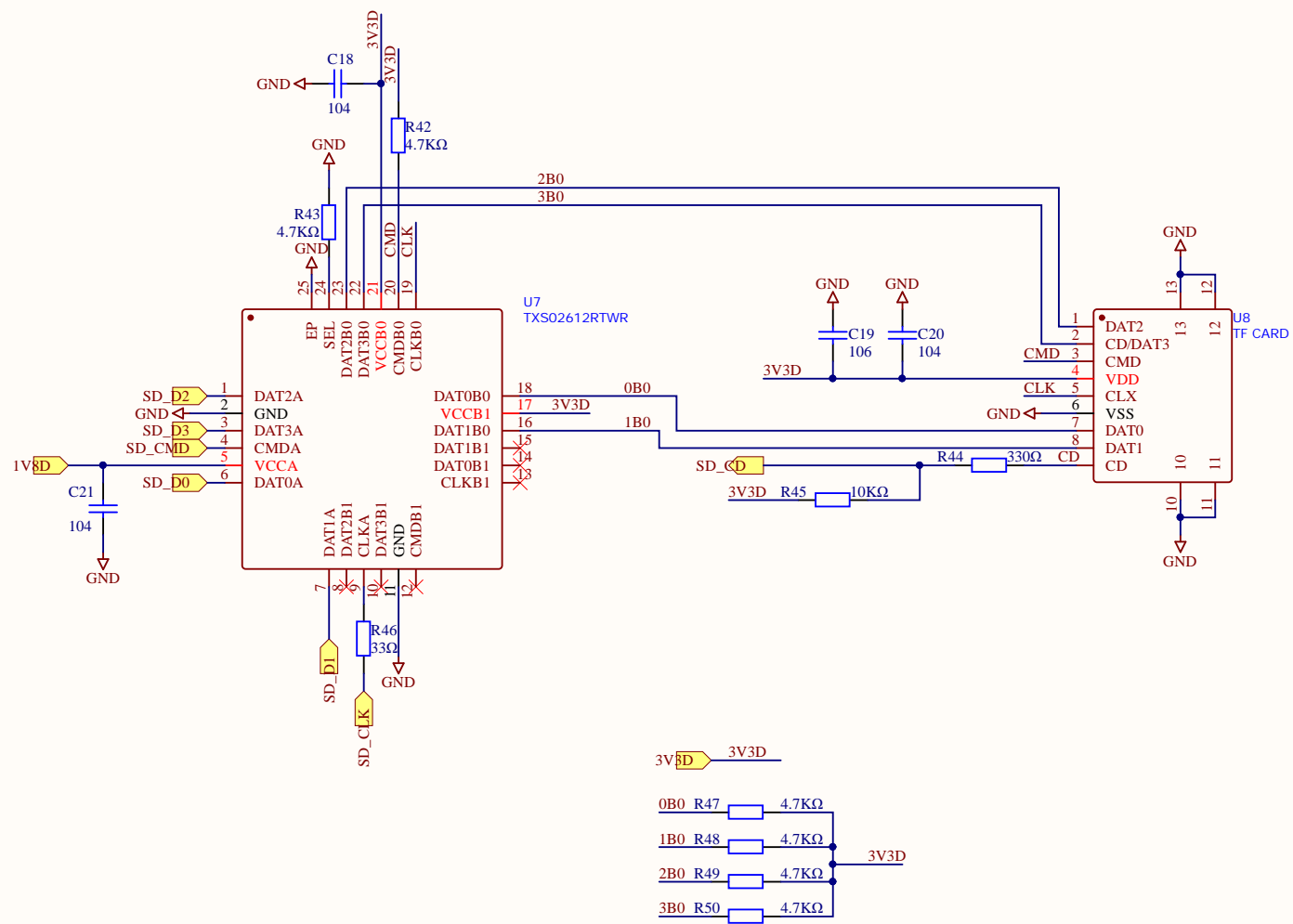


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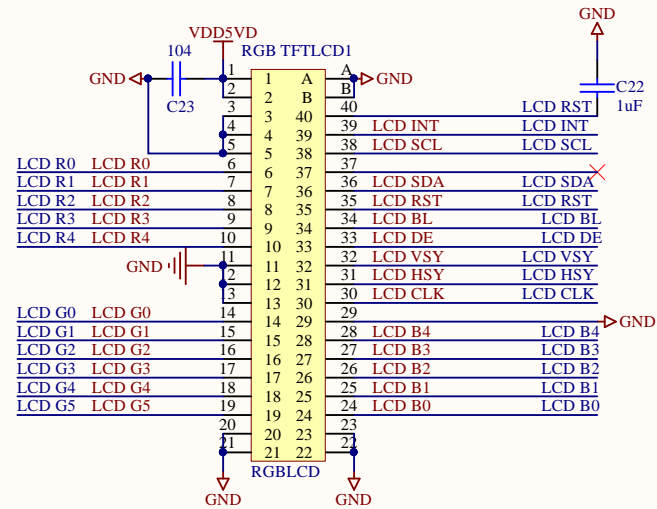




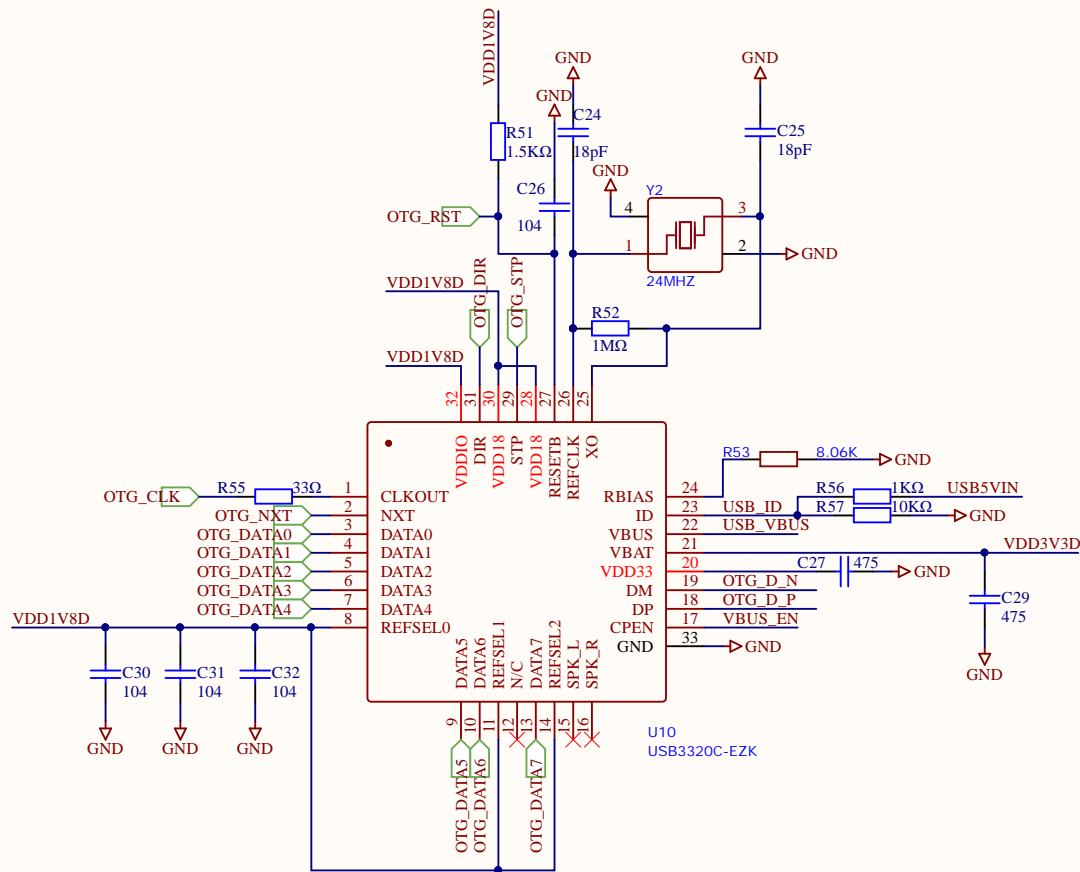
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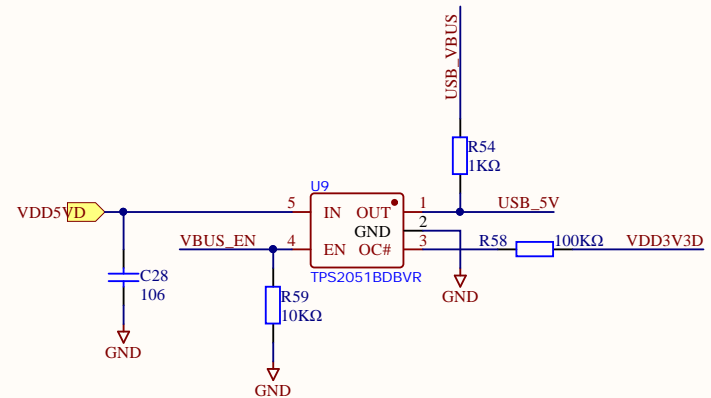
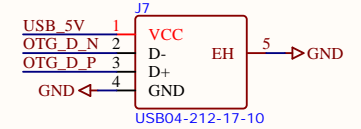
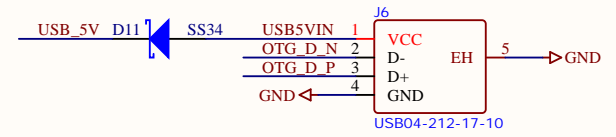
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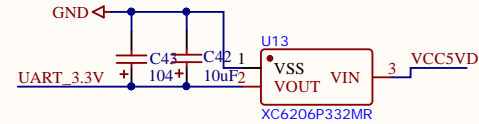
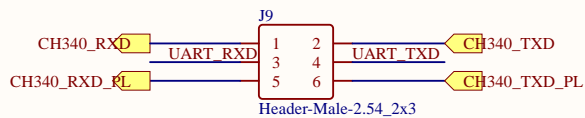
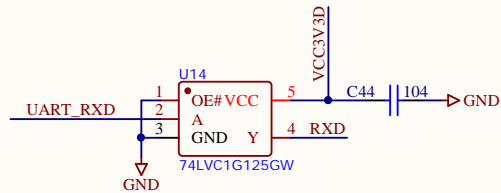
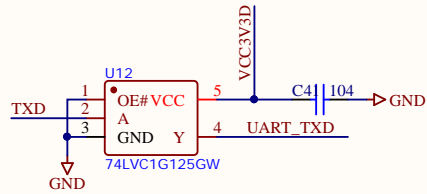
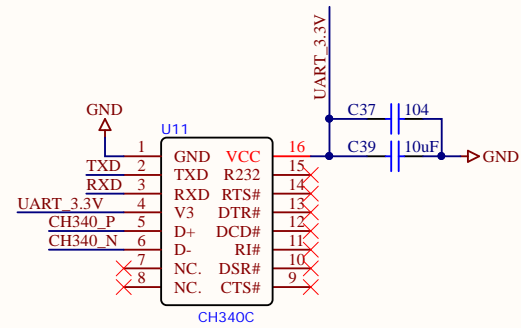
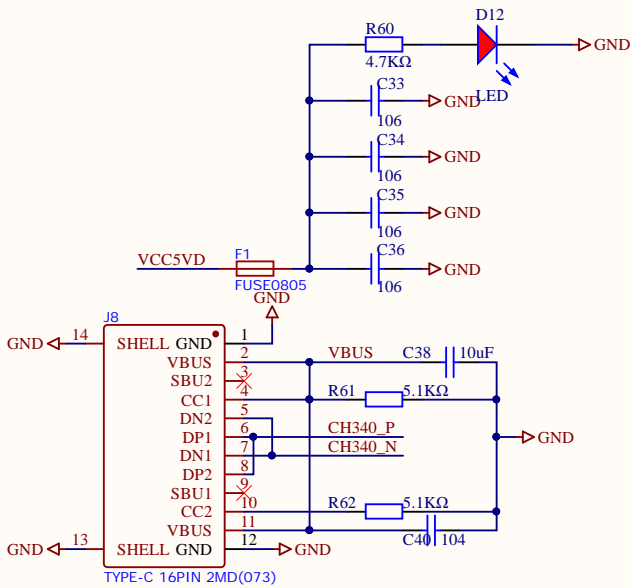
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VDD1V8D VDD1V8D
VDD3V3D VDD3V3D



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Size	Number	Revision
A4		
Date:	2024/3/15	Sheet of
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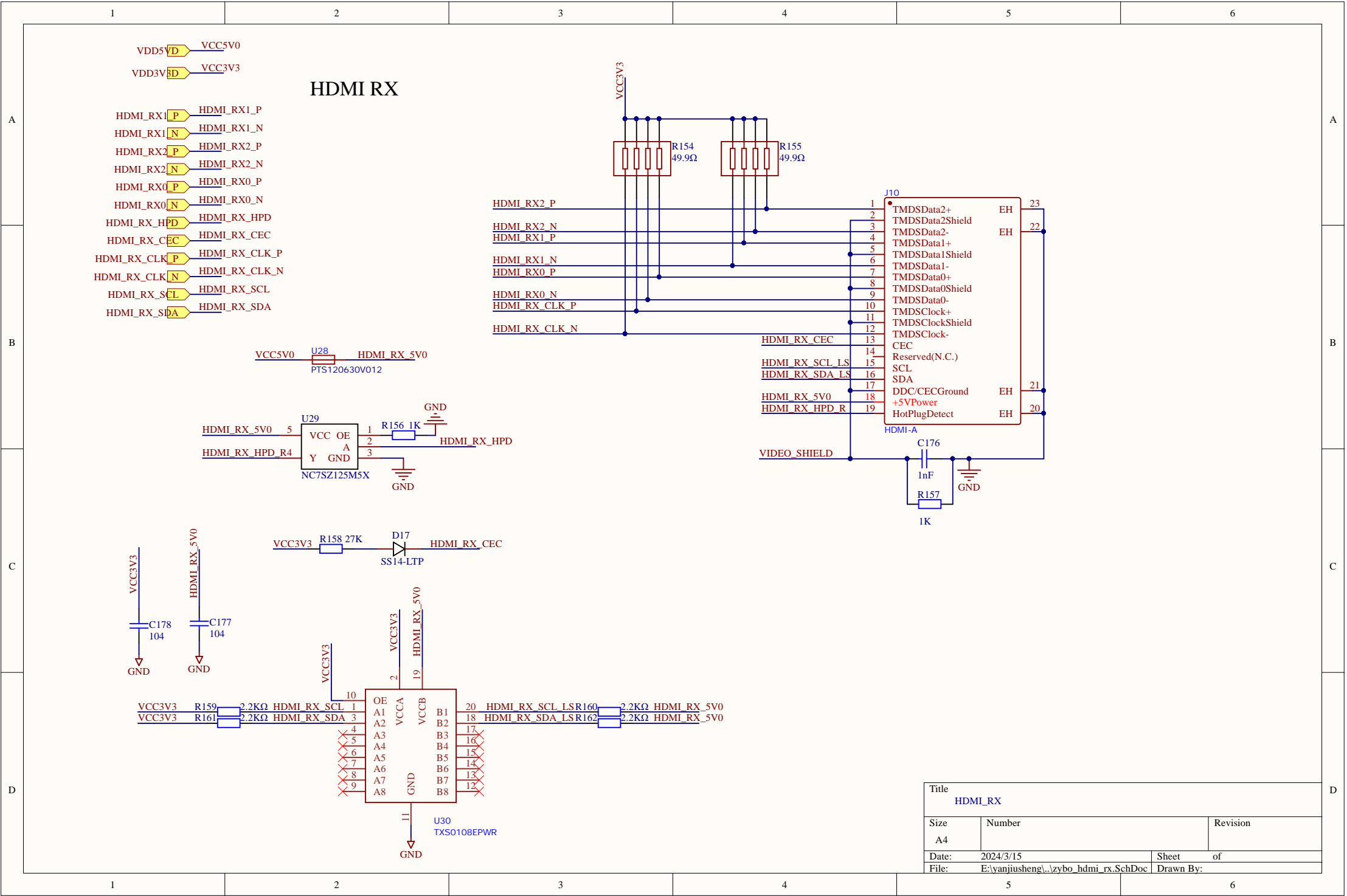


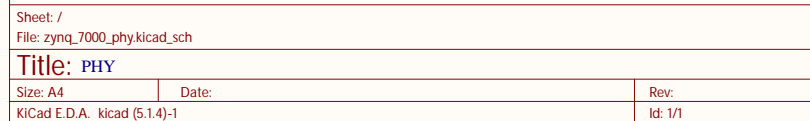
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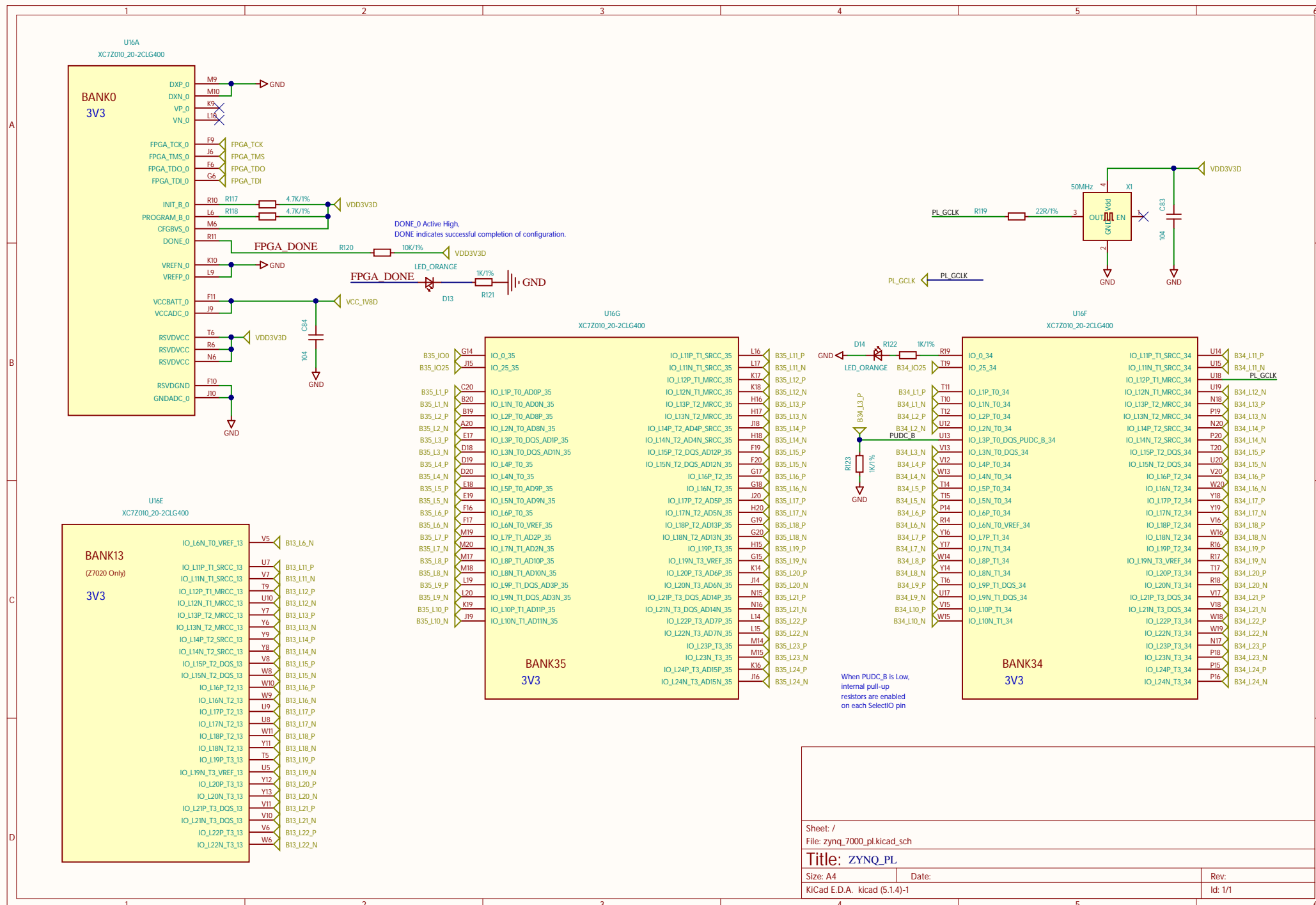
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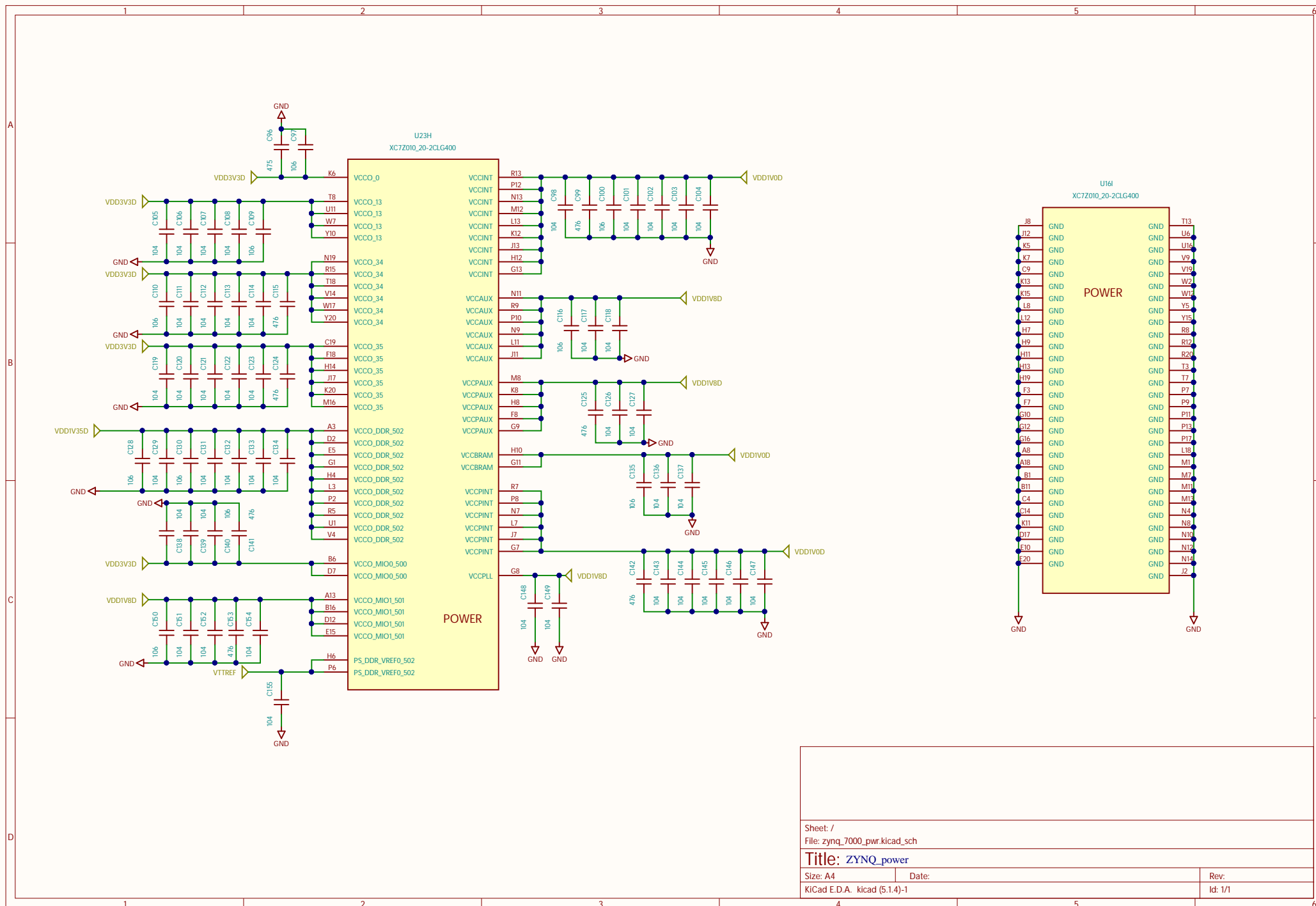
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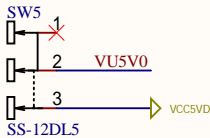
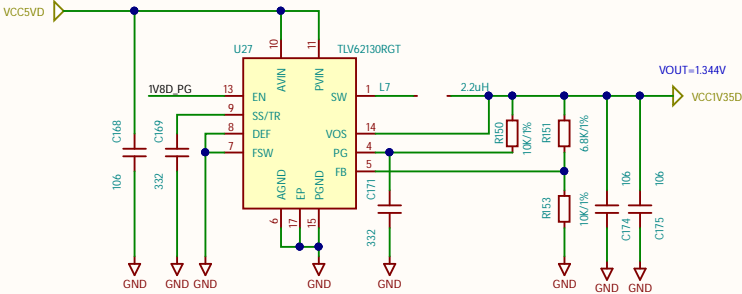
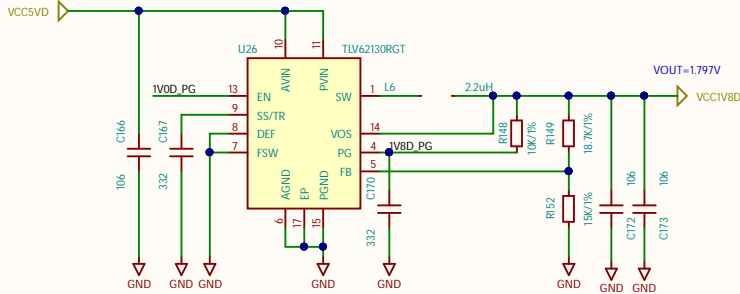
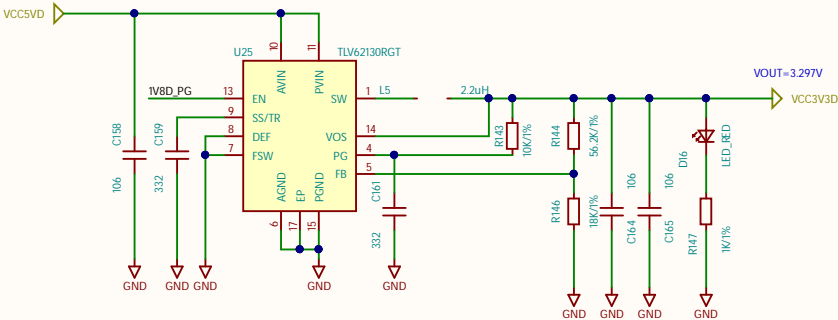
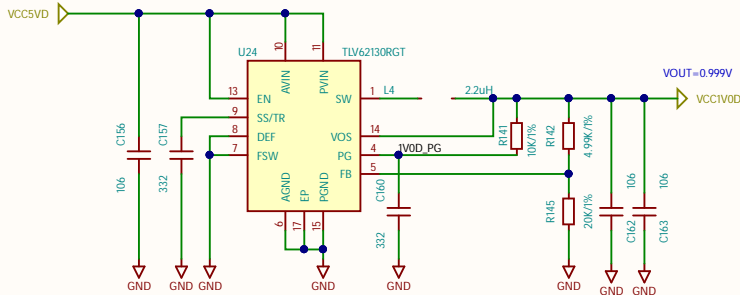






TLV62130RGT EN High Level Input Threshold Voltage=0.9V

$$((R1/R2) + 1) \cdot 0.8V = V_{OUT}$$



$$1.0V > 1.8V > 1.35V \& 3V3V$$

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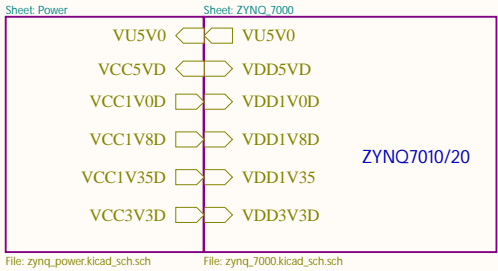
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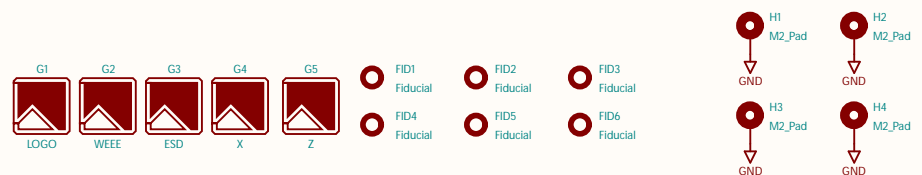
Date:

Rev:

Id: 1/1



ZYNQ7010/20



H1

M2_Pad

GND

H3

M2_Pad

GND

H2

M2_Pad

GND

H4

M2_Pad

GND

ZYNQ 7010/20

Sheet: /
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Title:

Size: A4

Date:

Rev:

KiCad E.D.A. kicad (5.1.4)-1

Id: 1/1

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光电工程学院

XILINX



ZYNQ

BOOT_CFG	1	2
JTAG	ON	OFF
QSPI/FLASH	ON	OFF
SD_CARD	ON	OFF

PS_TX	PS_RX
RXD	TXD
PL_TX	PL_RX

R69	R71	R70
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PS_RST		
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RGB TFTLCD1

40
C22

