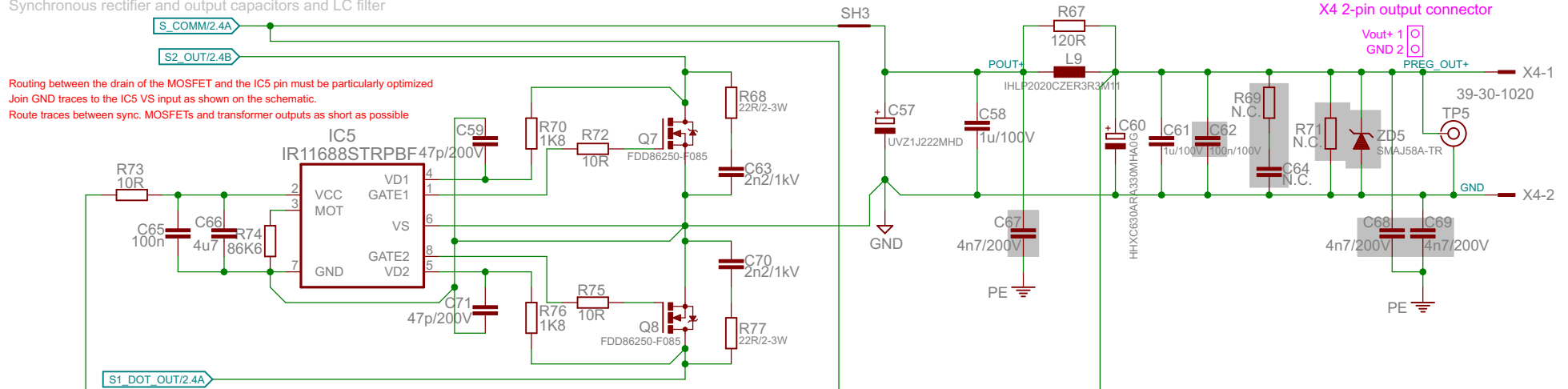
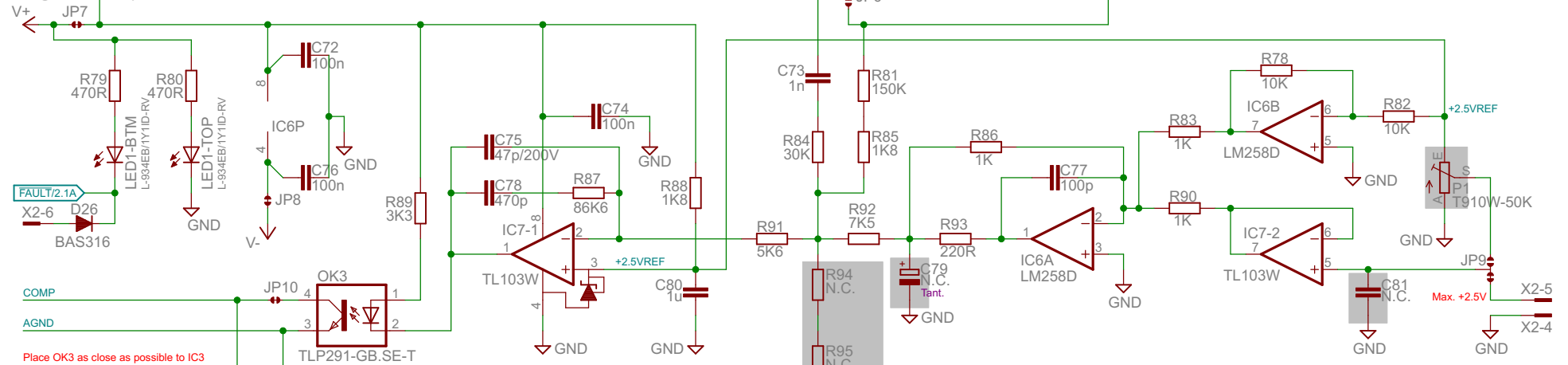


## Synchronous rectifier and output capacitors and LC filter

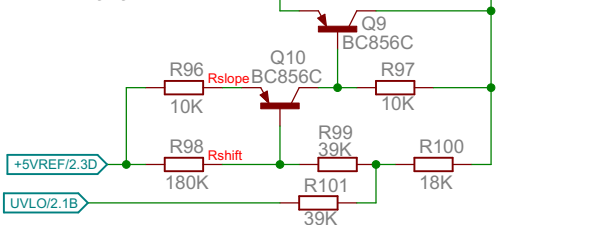
Routing between the drain of the MOSFET and the IC5 pin must be particularly optimized  
Join GND traces to the IC5 VS input as shown on the schematic.  
Route traces between sync. MOSFETs and transformer outputs as short as possible



## Voltage control loop + tracker



## Active Duty cycle limiter



CF-DIC secondary side: SR driver  
NFB with Vout tracker, Duty cycle limiter

TITLE: EEZ CF-DIC r5B7

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REV:

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