

MC-ISAR_AS42x_TC3xx_1.30.0_Patch_1

Release Notes

Product name: MC_AURIX2G_SW_MCAL

Release number: 1.30.0_PATCH_1

Type of release: PATCH*

Release method: via Release Area AUTOSAR specification: 4.2.2

Compiler support: Tasking 6.2r2p2, HighTec GNU 4.9.2.0, Greenhills v2018.1.5 DENSO PSA

Release R9.15.1, WindRiver 5.9.6.4 + CSO-31481 + Diab5964_TCDIAB-14824 +

Diab5964 TCDIAB-14933

Processor platform: TC37xEDAA, TC37xEDAB, TC37xAA and TC35xAA

Date: 2019-12-16

Patch Applicability: 1.30.0

^{*} This patch has passed delta testing, but has not been subject to the full validation process for a production release. Customers must perform their own integration testing.



1. Scope and purpose

This patch is to provide the fix for following issue:

Issue Id	0000053912-8827
Title	TC377x MCAL: Dio config issue for P22.6 and P22.7.

Issue Id	0000053912-8948
Title	Resource properties issues identified in TC37x, TC37xED and TC35x derivatives In the following modules Dio, FlsLoader, Pwm, Ocu, Dsadc, Lin, Uart, Sent, Eth_17_GEthMac, Can and Port

The patch is applicable to following packages

Date of Release	Package
2019-10-24	MC-ISAR_AS42x_TC3xx_BASIC_1.30.0
2019-10-24	MC-ISAR_AS42x_TC3xx_CD_1.30.0
2019-10-24	MC-ISAR_AS42x_TC3xx_COM-E_1.30.0
2019-10-24	MC-ISAR_AS42x_TC3xx_DEMO_1.30.0



2. Details of Issue Fixed

Issue Id	0000053912-8827
Title	TC377x MCAL: Dio config issue for P22.6 and P22.7
Impact	Port pins 22.6 and 22.7 cannot be used with DIO module configuration.
Description	Code generation error is observed when DioPort_Ch_P22_6 and P22_7 are configured in Tresos.
Solution	Dio Mask value is updated in the device resource properties files to make P22.6 and P22.7 usable

Issue Id	0000053912-8948
Title	Resource properties issues identified in TC37x, TC37xED and TC35x derivatives In the following modules Dio, FlsLoader, Pwm, Ocu, Dsadc, Lin, Uart, Sent, Eth_17_GEthMac,
Impact	Customer will not be able to use the following properties, Pwm, Ocu(TC37x, TC37xED): Wrong configuration of GTM TOUT which leads to un utilization of Ports mapped to the GTM channels. Pins which are not targeted for GTM TOUT were mapped here. Dio(TC35x, TC37x, TC37xED): User will not be able to use pins which are there in the HW Port(TC37x, TC37xED): PCSR support was missing for some of the pins, Alternate mode selections were wrong for some of the pins. Lin,Uart(TC37xED): Wrong RX pin selection is mapped Dsadc(TC37x, TC37xED): Dsadc Eru mapping literal correction, no functional impact. Eth(TC37xED): Missing EthSpeed support Can(TC37xED): Wrong RX line selection is mapped. Sent(TC37x,TC37xED): Wrong RX pin selection is mapped. FIsLoader(TC37x, TC37xED): FIsLoader: PF3 size is mentioned wrongly.
Description	Resource properties issues identified in TC37x, TC37xED and TC35x derivatives In the following modules Dio, FlsLoader, Pwm, Ocu, Dsadc, Lin, Uart, Sent, Eth_17_GEthMac, Can and Port
Colution	Updated TC37x, TC37xED and TC35x device resource properties and BMD impacted modules files to fix the reported issues. SW driver version is modified for the BMD file impacted modules.



3. Contents of 1.30.0_PATCH_1

Following items are supplied as part of 1.30.0_PATCH 1 (MC-ISAR_AS42x_TC3xx_1.30.0_Patch_1.zip)

- 1. Releasenote_MC-ISAR_AS42x_TC3xx_1.30.0_PATCH_1.pdf
- 2. Folder MC-ISAR_AS42x_TC3xx_1.30.0_Patch_1 \protect

Module	File	Purpose
	AURIX2G_TC356_ADAS properties	Dio Resource properties section is updated.
	AURIX2G_TC375.properties	Gtm, Eth, FlsLoader, Dsadc, Sent, Resource properties is updated
	AURIX2G_TC377.properties	Gtm, Dio, FlsLoader Resource properties is updated.
ResourceM	AURIX2G_TC375_ED.properties	Gtm, Port, Eth, FlsLoader, Dsadc, Sent, Resource properties is updated
	AURIX2G_TC377_ED.properties	Gtm, Dio, Port, FlsLoader Resource properties is updated
	AURIX2G_TC375_ED_EX.properties	Gtm, Dio, Port, Lin, Uart, Can, FlsLoader Resource properties is updated
	Dsadc.bmd	Parameter literal value is updated
	Dsadc.xdm	SW patch version(10.30.2) is updated
Dsadc	Dsadc.c	SW patch version(10.30.2) check is updated
	Dsdc_Bswmd.arxml	SW patch version(10.30.2) is updated
	plugin.xml	SW patch version(10.30.2) is updated
	Pwm_17_GtmCcu6.bmd	Parameter literal value is updated
	Pwm_17_GtmCcu6.xdm	SW patch version(10.30.2) is updated
Pwm_17_GtmCcu6	Pwm_17_GtmCcu6.c	SW patch version(10.30.2) check is updated
	Pwm_17_GtmCcu6_Bswmd.arxml	SW patch version(10.30.2) is updated
	plugin.xml	SW patch version(10.30.2) is updated
	Ocu.bmd	Parameter literal value is updated
	Ocu.xdm	SW patch version(10.30.2) is updated
Ocu	Ocu.c	SW patch version(10.30.2) check is updated
	Ocu_Bswmd.arxml	SW patch version(10.30.2) is updated
	plugin.xml	SW patch version(10.30.2) is updated
	Lin_17_AscLin.bmd	Parameter literal value is updated
	Lin_17_AscLin.xdm	SW patch version(10.30.2) is updated
Lin_17_AscLin	Lin_17_AscLin.c	SW patch version(10.30.2) check is updated
	Lin_17_AscLin_Bswmd.arxml	SW patch version(10.30.2) is updated
	plugin.xml	SW patch version(10.30.2) is updated
	Eth_17_GEthMac.bmd	Parameter literal value is updated



Module	File	Purpose
Eth_17_GEthMac	Eth_17_GEthMac.xdm	SW patch version(10.30.2) is updated
	Eth_17_GEthMac.c	SW patch version(10.30.2) check is updated
	Eth_17_GEthMac_Bswmd.arxml	SW patch version(10.30.2) is updated
	plugin.xml	SW patch version(10.30.2) is updated
	Sent.bmd	Parameter literal value is updated
	Sent.xdm	SW patch version(10.30.2) is updated
Sent	Sent.c	SW patch version(10.30.2) check is updated
	Sent_Bswmd.arxml	SW patch version(10.30.2) is updated
	plugin.xml	SW patch version(10.30.2) is updated
	Uart.bmd	Parameter literal value is updated
	Uart.xdm	SW patch version(10.30.2) is updated
Uart	Uart.c	SW patch version(10.30.2) check is updated
	Uart_Bswmd.arxml	SW patch version(10.30.2) is updated
	plugin.xml	SW patch version(10.30.2) is updated

4. Steps to use 1.30.0_PATCH_1

- 1. Ensure that MC-ISAR_AS42x_TC3xx_BASIC_1.30.0 and/or MC-ISAR_AS42x_TC3xx_CD_1.30.0 and/or MC-ISAR_AS42x_TC3xx_CD_1.30.0 and/or MC-ISAR_AS42x_TC3xx_DEMO_1.30.0 BASE packages are installed.
- 2. Extract MC-ISAR AS42x TC3xx 1.30.0 Patch 1.zip
- 3. Replace files present in BASE packages from the files present under MC-ISAR_AS42x_TC3xx_1.30.0_PATCH_1\patched_files. Please see following example for replacing files:

xcopy /F /E /Y <*PATCH EXTRACTED PATH>* patched_files <*BASE PACKAGE INSTALLED PATH>*MC-ISAR_AS42x_TC3xx_BASIC_1.30.0\

4. Clean and build the release package. (Refer TC3xx_SW_MCAL_UM_Basic.pdf for details)



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