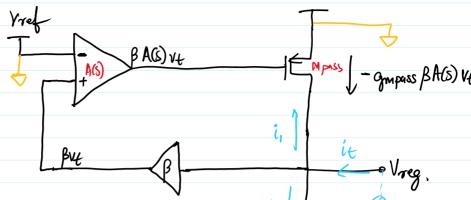
Output Impedance of LDD





Step-1:D. Switch of all independent Power supply
2). Apply by, Check it

Rout(dc) = rdspass || RI+RZ || RL iz= Vt Rout }

in= gmpss BAG) V+

it = Vt + grapas BAG) Vt
Rout

it = Vt (1+ ampass BAG) Rout) Sampass BAG) Rout = BAO(S) }

iz = Vz(It BAS))
Rout

Rout = R_{th} = Vt = Rout it = HBA₀(S)

i) at DC $h_{o}(s) = h_{o} p$ ii) as $w \rightarrow w p$ $h_{o}(s) \rightarrow v e mains constant$ $h_{o}(s) \rightarrow v e mains constant$ $h_{o}(s) \rightarrow v e mains$ $h_{o}(s) \rightarrow v e mains$

iii) as w→wugb Aucs) - drops and approaches OdB So Port in creases

iv) as $\omega \to \omega p_2$ γ) Ao(s) - const and same as that of wownsb

as W> WPZ
The output Cap becomes
Short so Zout = D

