






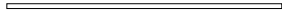


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15. USB WIFI

PCB POWER WIRE WIDTH INDICATE

	above 80 miles
	above 50 miles
	above 30 miles
	above 16 miles
No indicate	Under needs

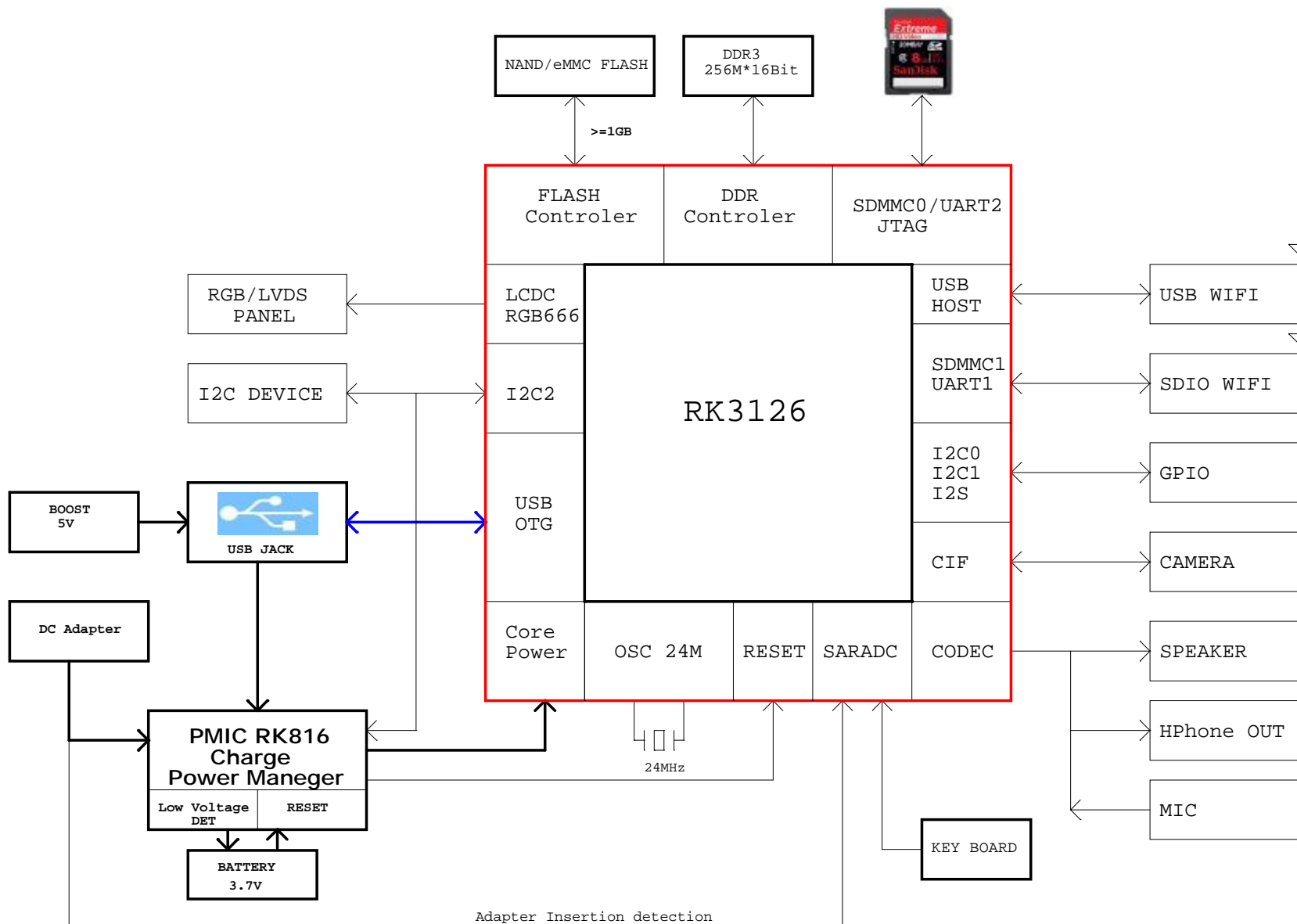
4 LAYERS PCB STACK

TOP		Hoz(18um) + plating copper(18um)
	Prepreg 3313*1 4.0MIL(0.10mm)	
GND		1oz(35um)
	Adjust	
POWER		1oz(35um)
	Prepreg 3313*1 4.0MIL(0.10mm)	
BOTTOM		Hoz(18um) + plating copper(18um)

Title			BCL-RK3026-86SZ
Size	Document Name	Rev	
A3		A.0	
Date:	2013-07-23	Sheet	1 of 14

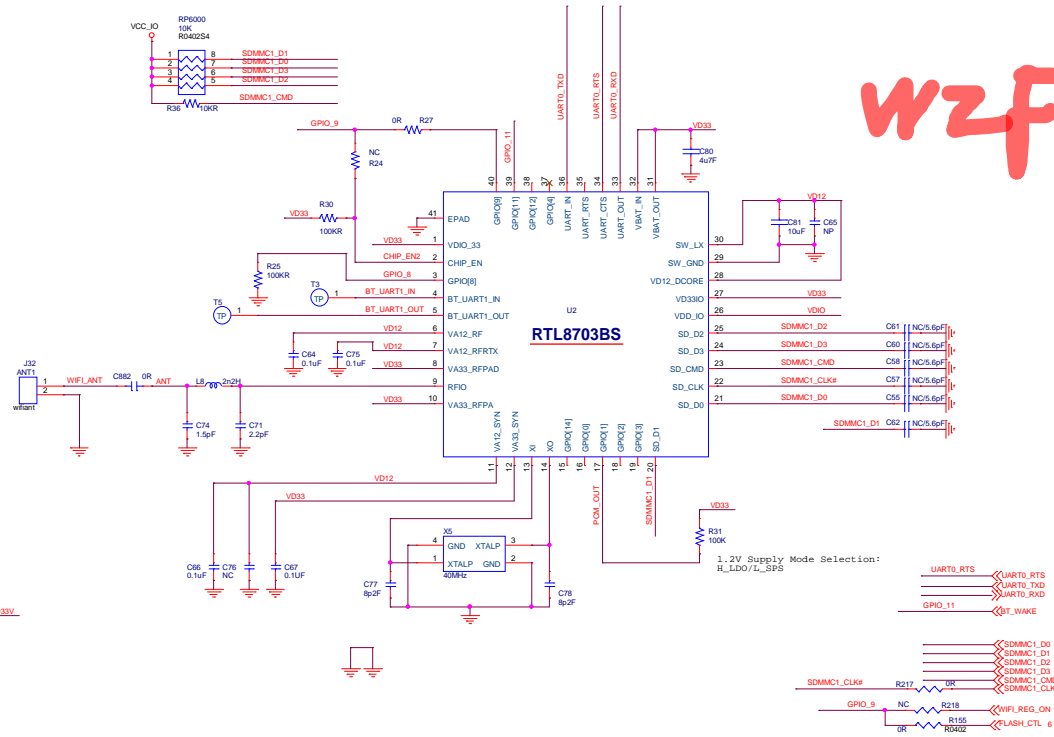
Version	Date	Author	Change Note	Note
V1.0	20120718	LX	First editor	
V1.1	20120822	LGL	1. Change PMU VDIG1 to VCC1.5_CIF and connect it to camera DVDD 2. Correct the reset key and power key, Change D16, D12 footprint to SOD123, And C356 to C0805 3. Add NC/8.2K resister between PWM_LOG and GND 4. Update the part of RK2928L 5. Change the value of R57 to 10K, and R132 to 2.2K 6. Add test point and mark 6. Add R144(NC/0R) between the net of DCIN and VDC	
V1.1	20120911	HXS	1. Remove PMIC_GPIO 2. Remove VIB Circuit 3. Change TP_RST to RC Circuit	
V2.0	20151109	LT	1. Change AXP192 to RK816 2. Change RK3026 to RK3126	

Title			BCL-RK3026-86SZ
Size	Document Name	Rev	
A3		A0	
Date:	2013-07-23	Sheet	2 of 14



WIFI/WIFI ac/BT

RTL8703BS



WZFL

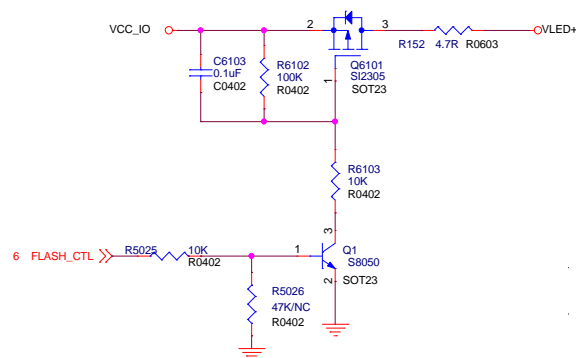
U18

VIP Module

CIF_D0/TS_D0_d	70	CIF_D0
CIF_D1/TS_D1_d	71	CIF_D1
CIF_D2/TS_D2_d	72	CIF_D2
CIF_D3/TS_D3_d	73	CIF_D3
CIF_D4/TS_D4_d	74	CIF_D4
CIF_D5/TS_D5_d	75	CIF_D5
CIF_D6/TS_D6_d	76	CIF_D6
CIF_D7/TS_D7_d	77	CIF_D7
CIF_VSYNC/TS_VSYNC_d	78	CIF_VSYNC
CIF_HREF/TS_FAIL_d	79	CIF_HREF
CIF_PDN1/GPIO3_B3_u	83	CIF_PDN
CIF_CLKI/TS_VALID_d	80	CIF_CLKI
CIF_CLKO/TS_CLKO_d	81	CIF_CLKO

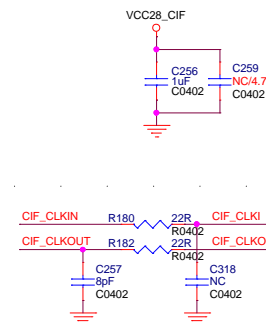
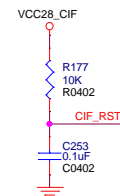
RK3126

LQFP176_16X16

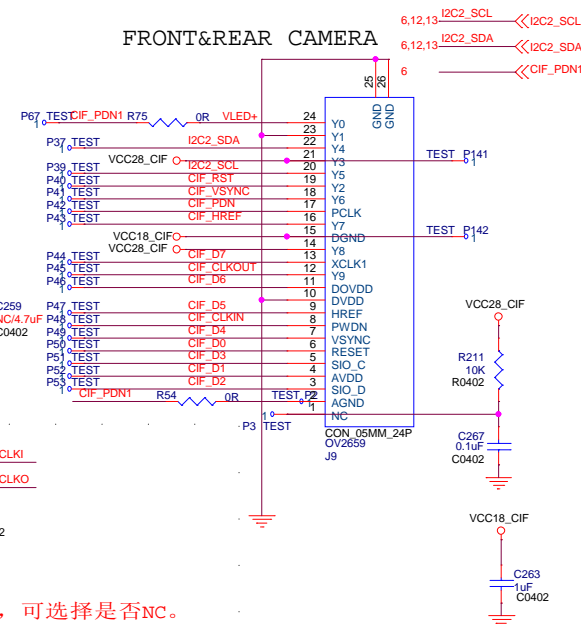


RK3026-B

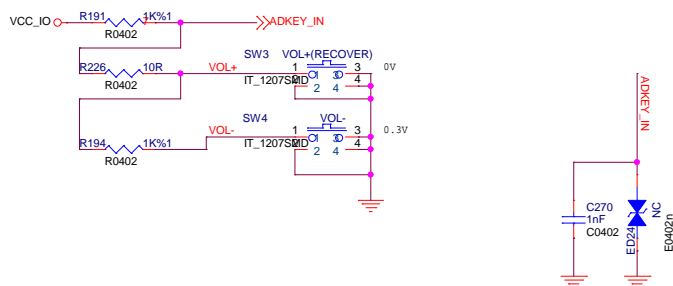
Note: 四个器件, 根据调试结果, 可选择是否NC.



FRONT&REAR CAMERA

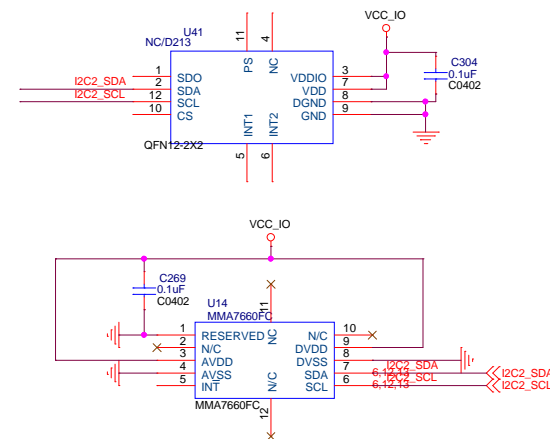


KEY BAORD



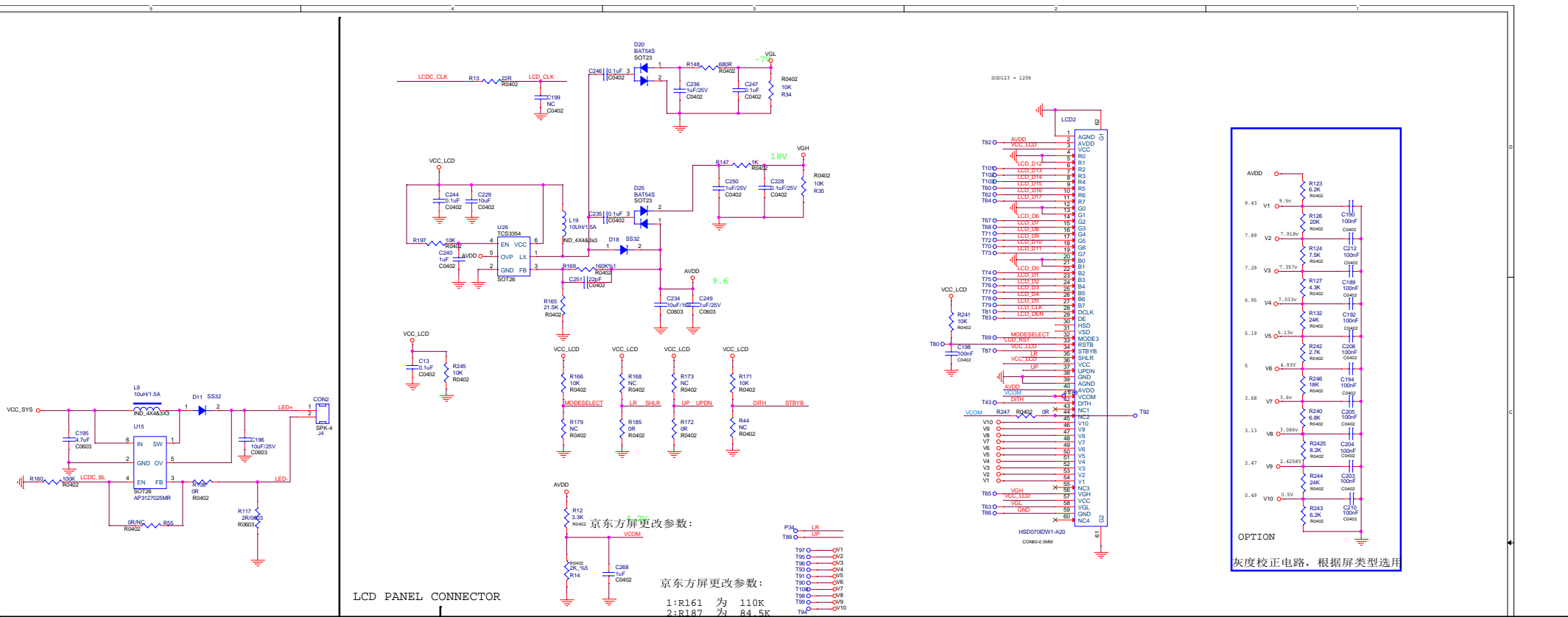
When the system power on, the Adkey_in level is 0V,
RK2928 enter into loader mode.

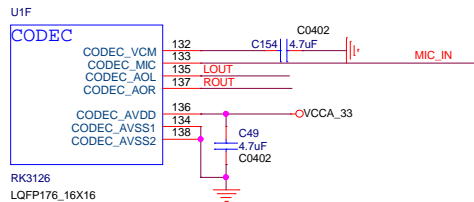
3D G-Sensor



Note:
The first pin of G-sensor must be
place on the lower left corner of PCB.

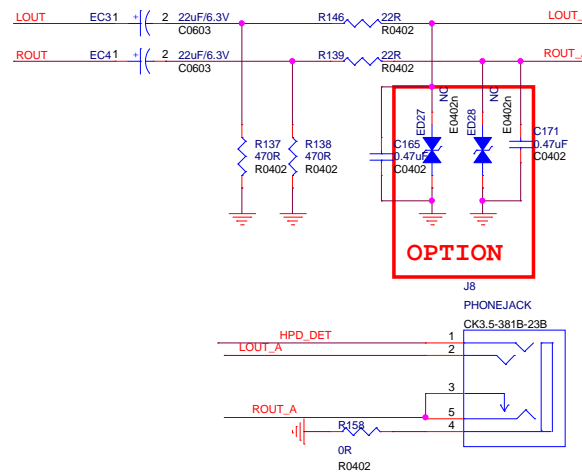
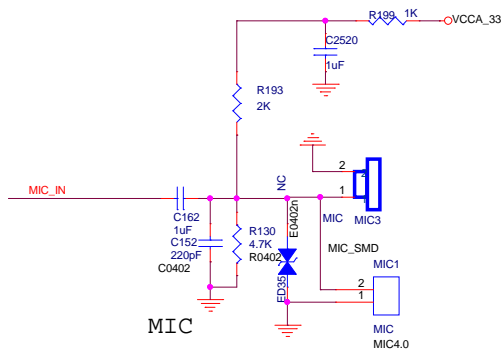
Rockchip 福州瑞芯微电子有限公司	
Title: Camera/G_Sensor/Key	
File: 3026-86SZ	REV: V1.0
Create Date: Friday, October 15, 2010	Page Num: 4
Modify Date: Tuesday, August 14, 2018	Page Total: 25





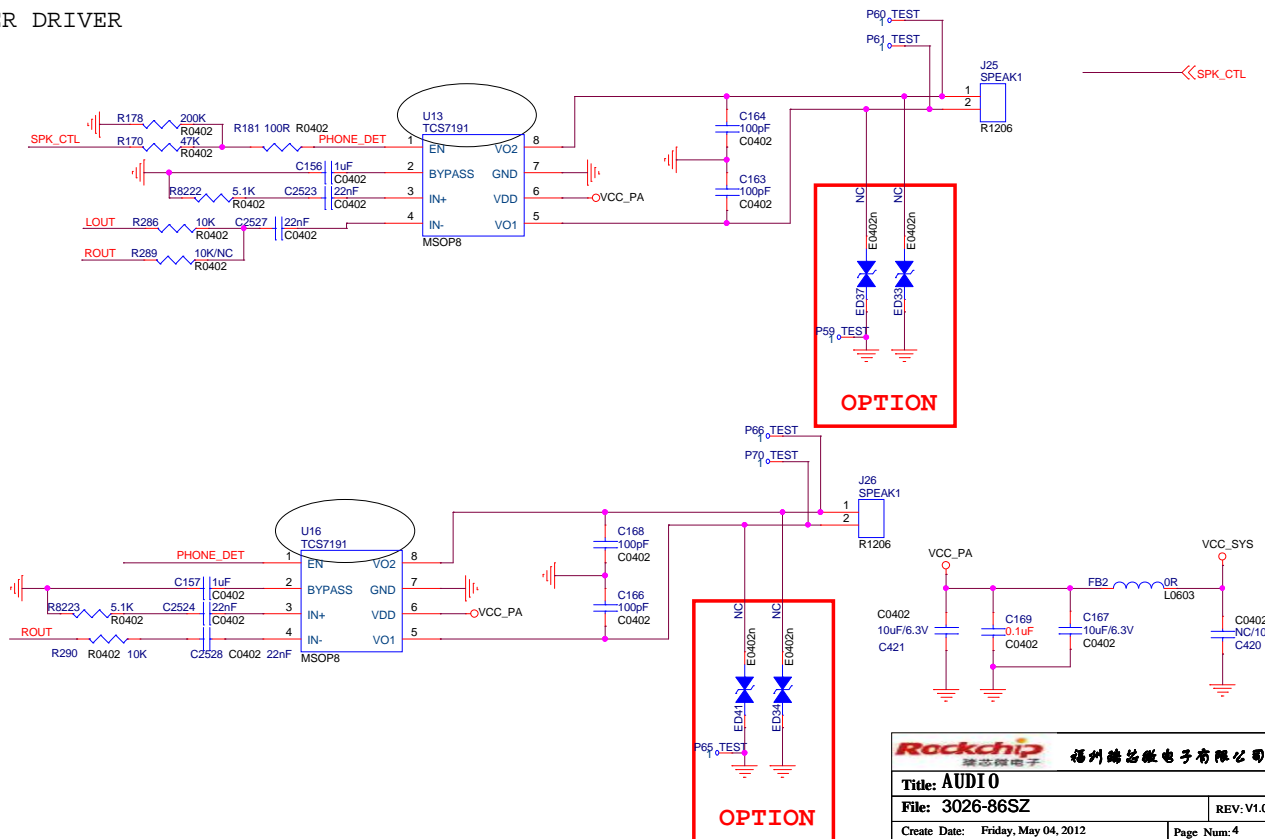
RK3026-F

MIC

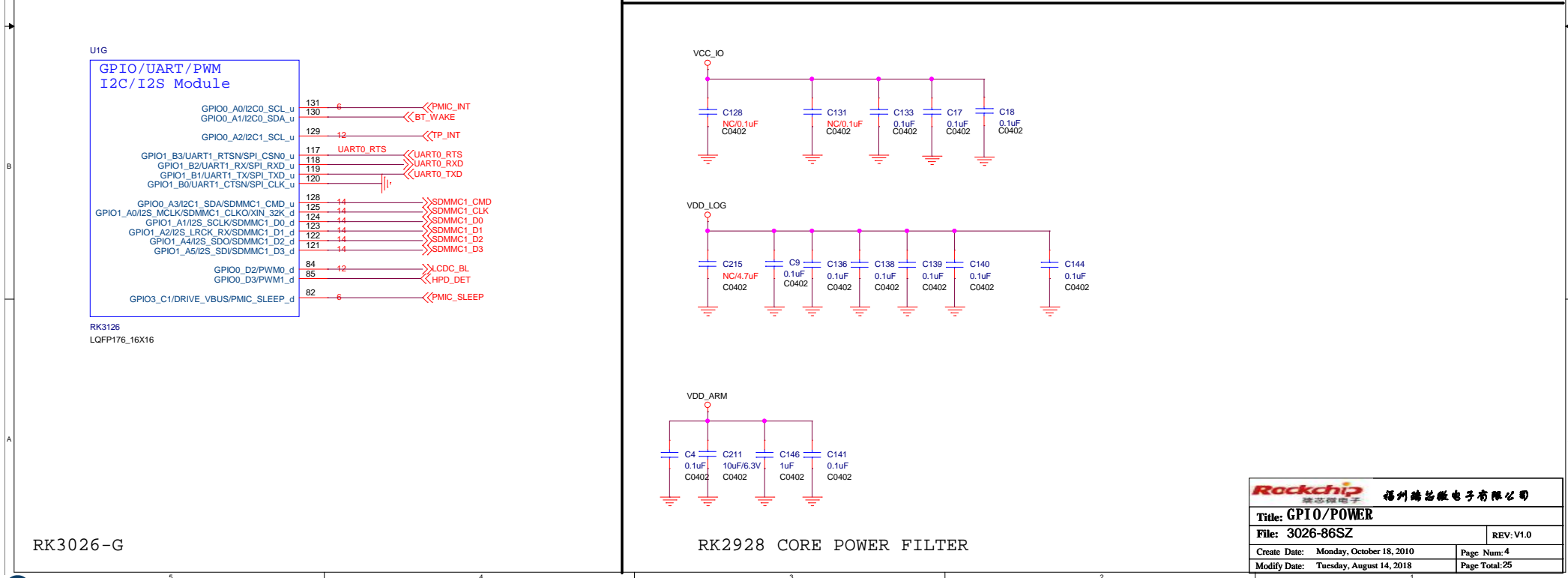
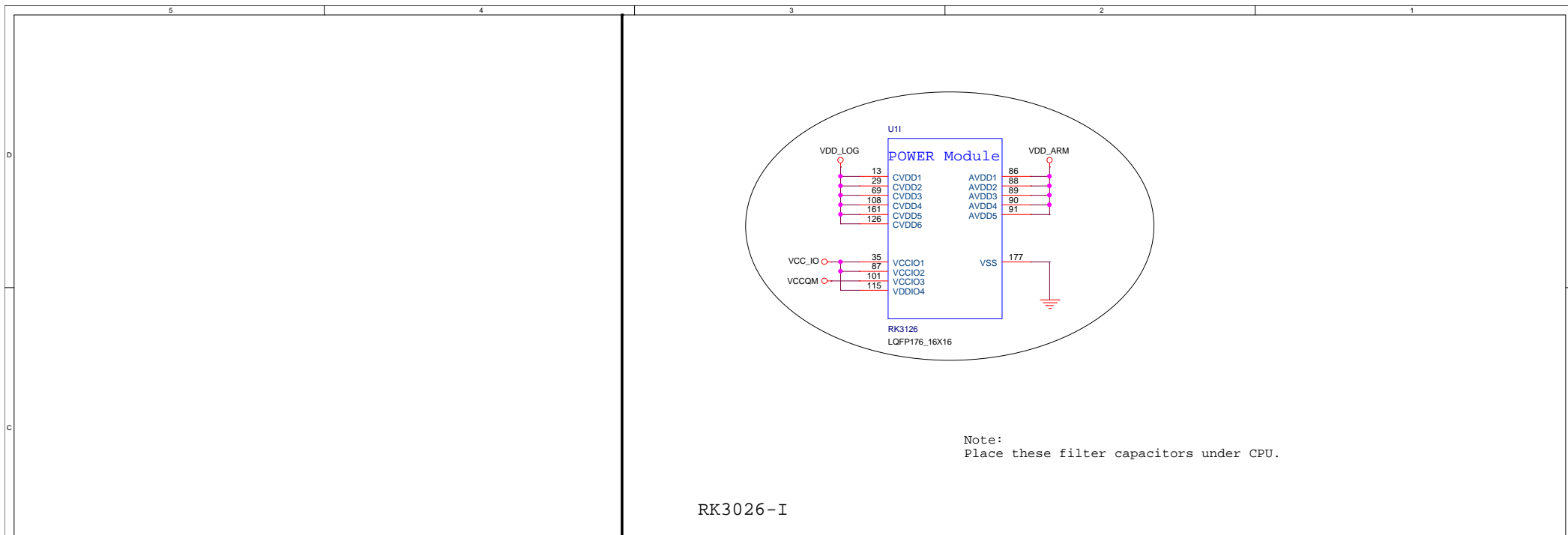


HEADPHONE

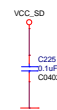
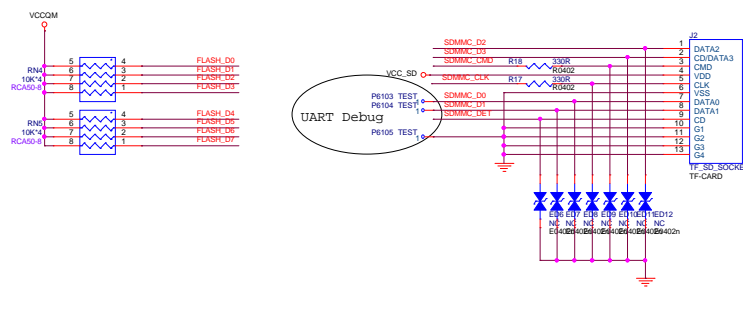
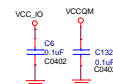
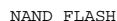
SPEAKER DRIVER




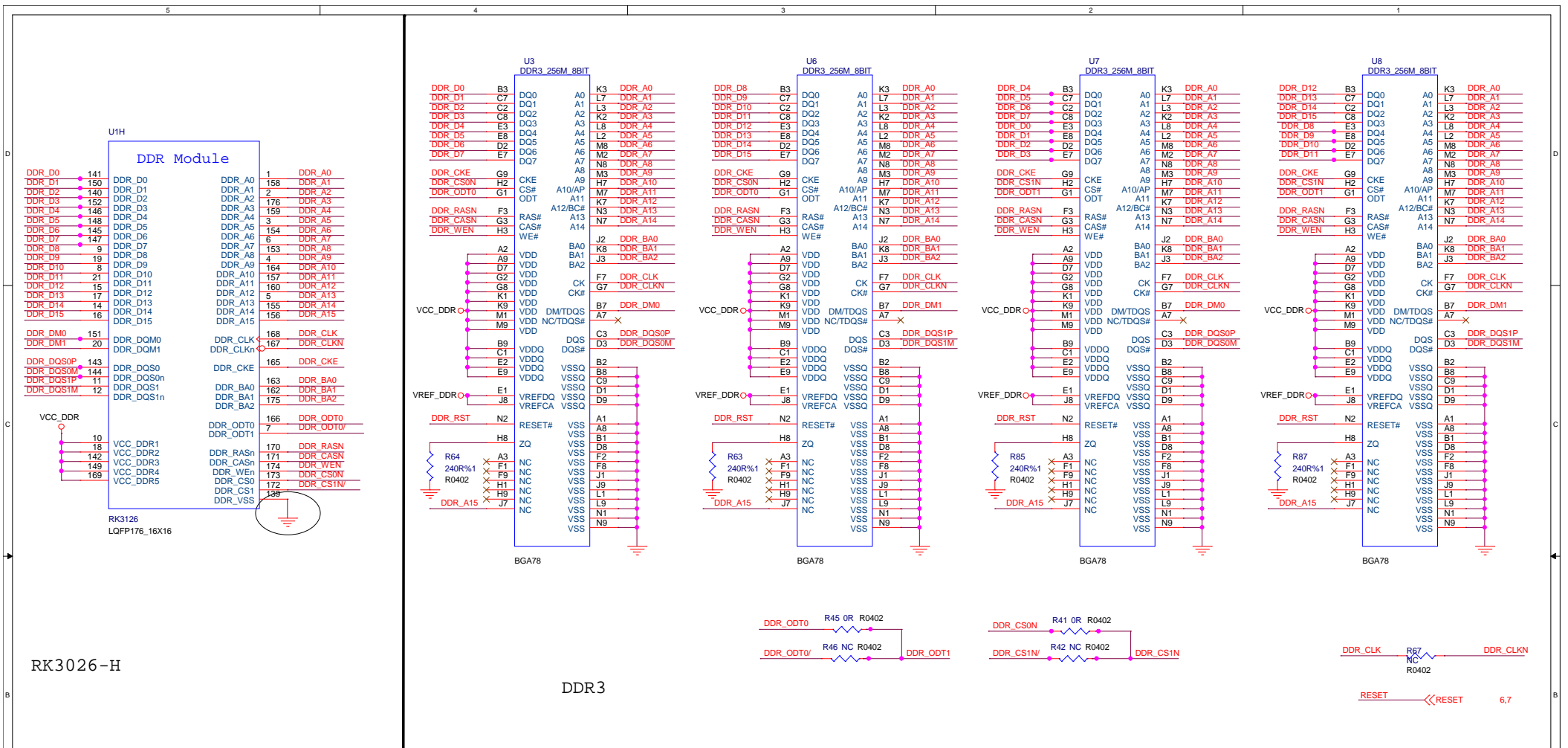
Rockchip 福州瑞芯微电子有限公司	
Title: AUDIO	
File: 3026-86SZ	REV: V1.0
Create Date: Friday, May 04, 2012	Page Num: 4
Modify Date: Tuesday, August 14, 2018	Page Total: 25



Rockchip 瑞芯微电子 福州瑞芯微电子有限公司	
Title: GPIO/POWER	
File: 3026-86SZ	REV: V1.0
Create Date: Monday, October 18, 2010	Page Num: 4
Modify Date: Tuesday, August 14, 2018	Page Total: 25

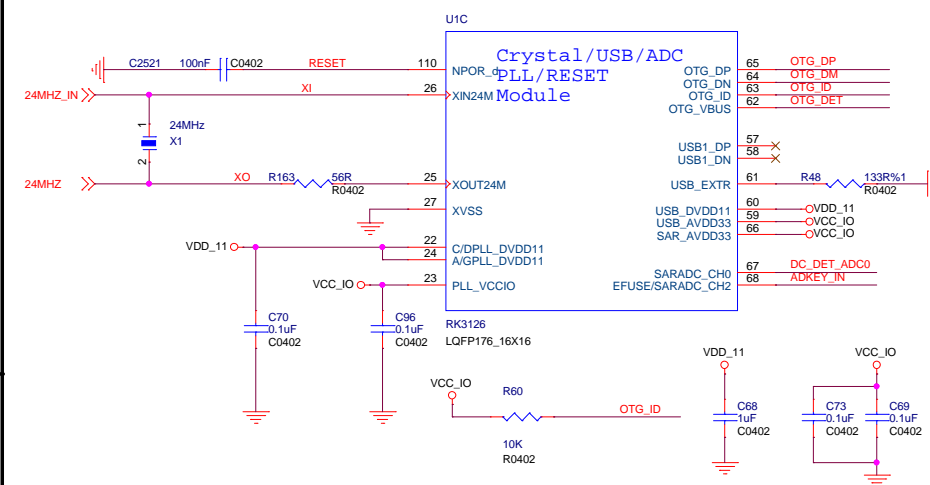


 福州瑞芯微电子股份有限公司	
Title: Flash/TF Card	
File: 3026-86SZ	REV:V1.0
Create Date: Thursday, October 14, 2010	Page Num:4
Modify Date: Tuesday, August 14, 2013	Page Total:25



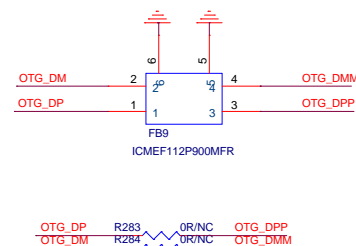
5 — ADKEY_IN << DC_DET_ADC0
13 — RESET << ADKEY_IN
6,8,9 — RESET
5 — OTG_ID << OTG_ID

Note:
Adjusted the load capacitance
according to the crystal specification.



RK3026-C

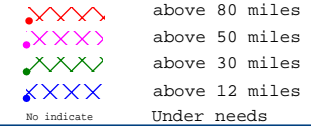
USB OTG CONNECTOR



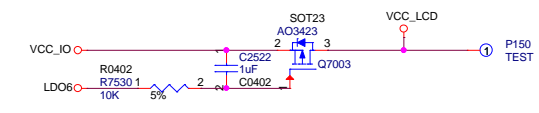
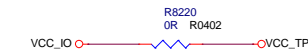
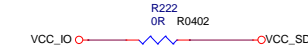
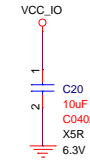
VIBRATION

Rockchip 福州瑞芯微电子有限公司	
Title: USB OTG/VIB	
File: 3026-86SZ	REV: V1.0
Create Date: Tuesday, November 29, 2011	Page Num: 4
Modify Date: Tuesday, August 14, 2018	Page Total: 25

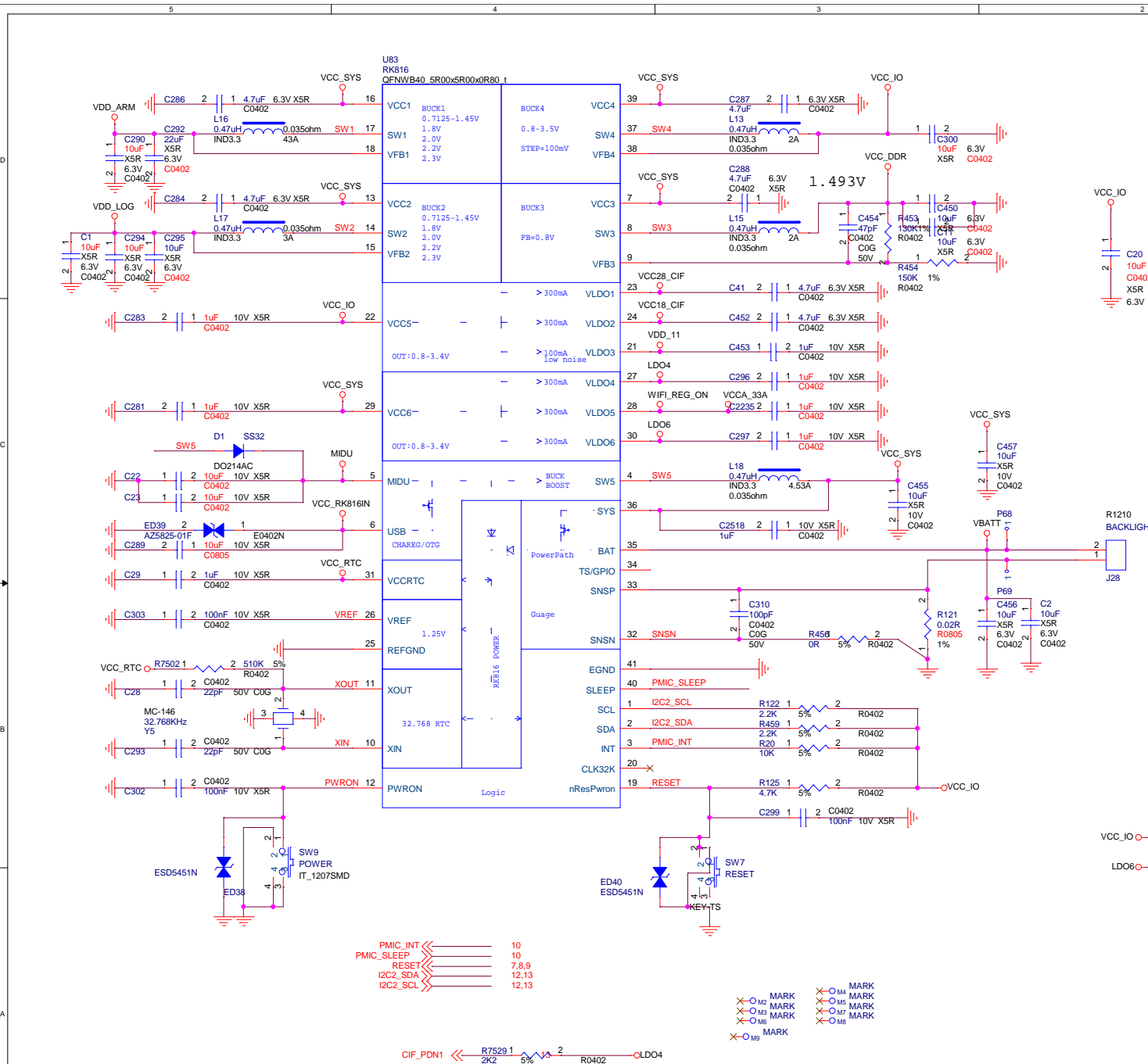
PCB POWER WIRE WIDTH INDICATE



PMU Channel	TIMER (2ms)	default ON/OFF	default voltage	RK3126
DCDC1	solit:12	ON	1.1V	VDD_ARM
DCDC2	solit:13	ON	1.1V	VDD_DG
DCDC3	solit:14	ON	3.3V	VCC_IO/VCCIO_ML/VCC_SD
DCDC4	solit:15	ON	1.1V	FLASH/VCCA
VLD01	X	OFF	DDR3 1.5V	VCC_DDR
VLD02	solit:11	ON	DDR3L 1.35V	VCC_DDR
VLD03	solit:11	ON	LPDDR 1.245V	VCC_DDR
VLD04	X	OFF	1.8V	VCC18_CIF
VLD05	solit:14	ON	1.1V	VDD_11
VLD06	solit:14	ON	3.0V	CHP_PWR1
RESET	5ms	ON	3.0V	WD_PWE
			OD	VCC_LCD
				RESET



Rackchip 瑞芯微电子 福州瑞芯微电子有限公司	
Title: PMIC RK816	
File: 3026-86SZ	REV: V1.0
Create Date: Wednesday, July 18, 2012	Page Num: 4
Modify Date: Tuesday, August 14, 2018	Page Total: 25



SYSTEM POWER

Default:USB only

VCC50_USB ○ ———— ○VCC_RK816IN

充电方案:

1、单USB, 只贴R7536

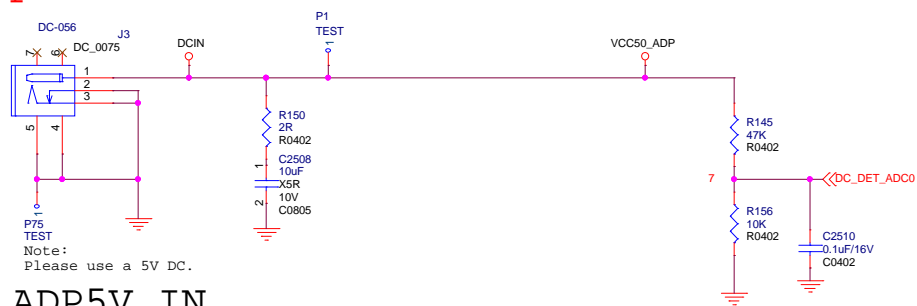
2、ADP+USB

极低成本: 只贴R7536+D2501

低成本2: 只贴虚框2内器件 (包括DNP器件,R7536=NC)

全功能: 虚框1+虚框2 (不包括DNP器件和R7536)

Option:USB+ADP



ADP5V IN

虚框1

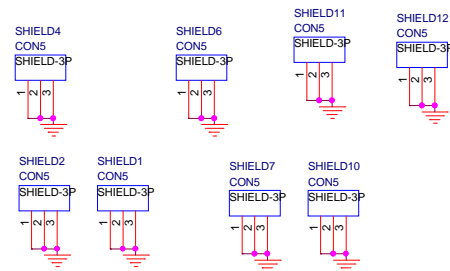


虚框2

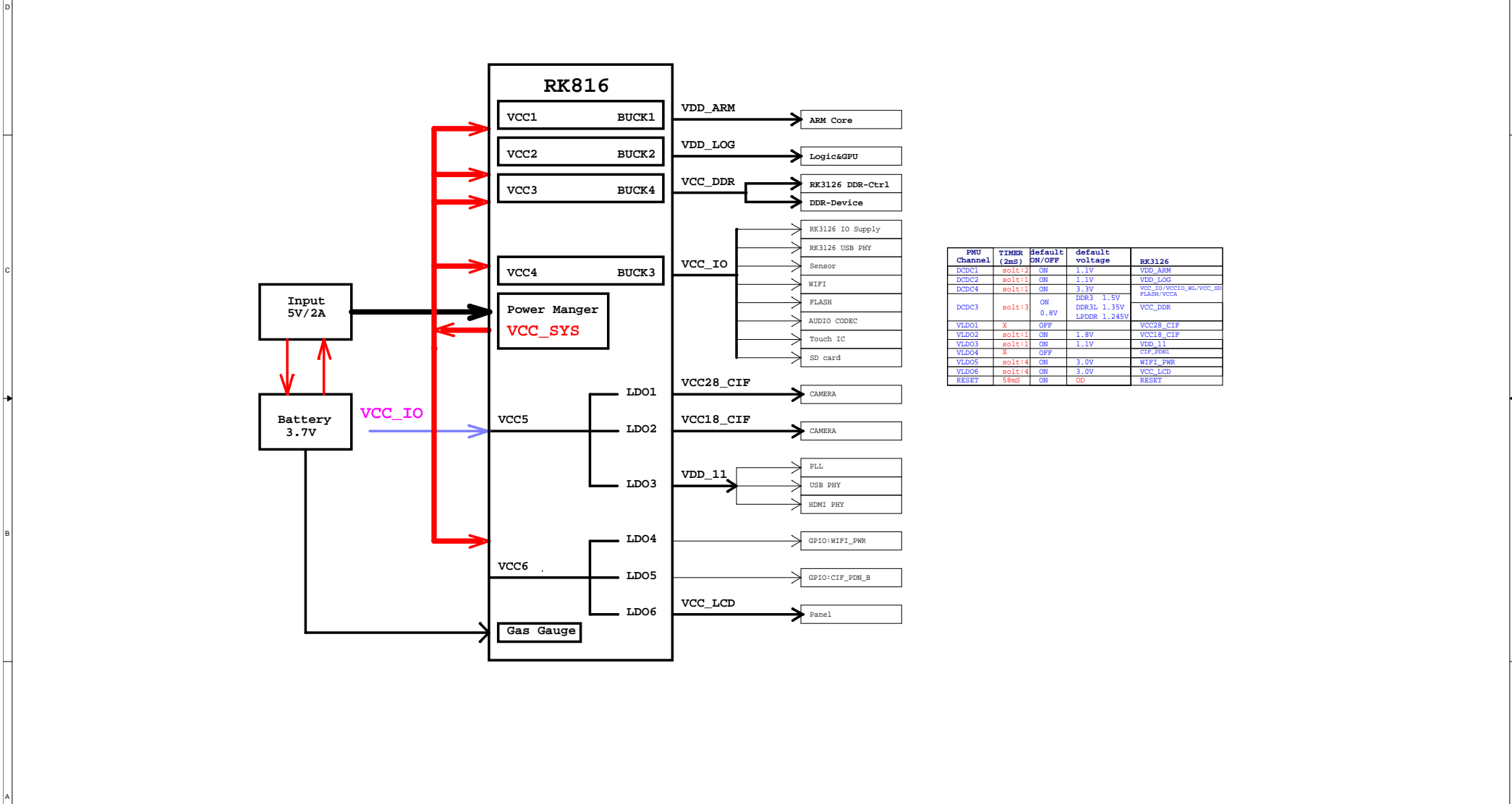
只贴本虚线框也可支持双口充电:

缺点: ADP插入时OTG从ADP直接取电, 没有对ADP做过流保护

Adapte、USB双口充电控制:全功能, 带完善保护。(标有DNP的不贴)



 福州瑞芯微电子有限公司			
Title: DC/Charge			
File: 3026-86SZ			REV: V1.0
Create Date:	Thursday, November 24, 2011	Page Num:	4
Modify Date:	Tuesday, August 14, 2018	Page Total:	25



PMU Channel	TIMER (2ms)	On/Off	default voltage	RK3126
DCDC1	0x0122	ON	1.1V	VDD_ARM
DCDC2	0x0121	ON	1.1V	VDD_LDO
DCDC3	0x0123	ON	1.3V	VDD_VDDIO_NL/VDD_SRAM/VCCA
DCDC4	0x0123	ON	DDR3 1.5V DDR3L 1.35V LPDDR 1.245V	VDD_DDR
VDDO1	X	OFF		VCC2B_CIF
VDDO2	0x0121	ON	1.8V	VCC2B_CIF
VDDO3	X	ON	1.1V	VDD_I1
VDDO4	X	OFF		CIF_PWR1
VDDO5	0x0124	ON	3.0V	CIF_PWR2
VDDO6	0x0124	ON	3.0V	VCC_LCD
RESET	58ms	ON	ODV	RESET