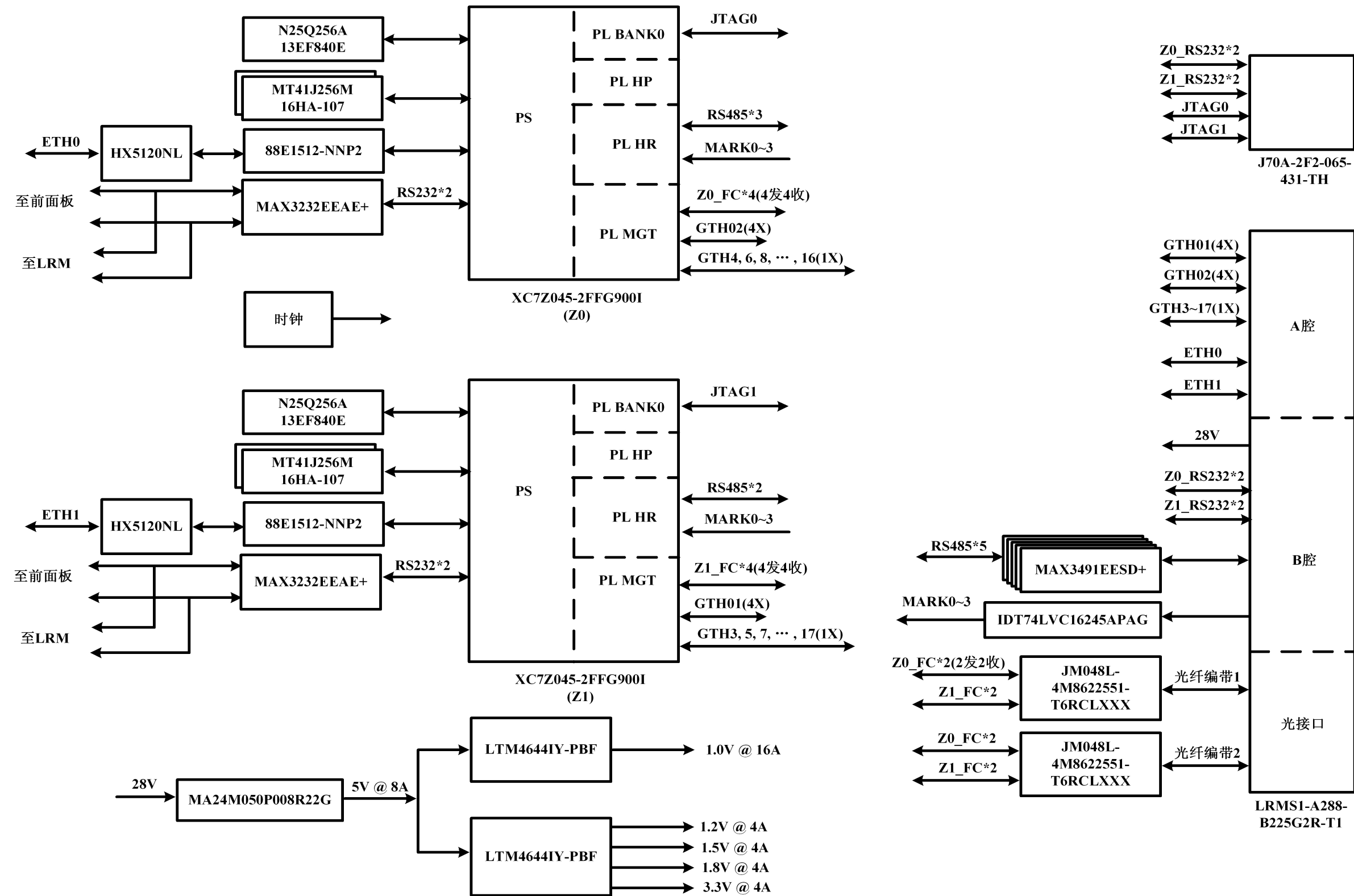


## FCU-NIM



$\overset{\circ}{\mathfrak{m}}\overset{\circ}{\mathfrak{z}}\overset{\circ}{\mathfrak{i}}\overset{\circ}{\mathfrak{u}}\overset{\circ}{\mathfrak{A}}$	
$\overset{\circ}{\mathfrak{a}}\overset{\circ}{\mathfrak{i}}$	
$\overset{\circ}{\mathfrak{A}}\overset{\circ}{\mathfrak{i}}\overset{\circ}{\mathfrak{A}}\overset{\circ}{\mathfrak{a}}\overset{\circ}{\mathfrak{A}}$	
$\overset{\circ}{\mathfrak{E}}\overset{\circ}{\mathfrak{m}}\overset{\circ}{\mathfrak{i}}\overset{\circ}{\mathfrak{x}}\overset{\circ}{\mathfrak{U}}\overset{\circ}{\mathfrak{A}}$	
$\overset{\circ}{\mathfrak{m}}\overset{\circ}{\mathfrak{i}}\overset{\circ}{\mathfrak{x}}\overset{\circ}{\mathfrak{U}}\overset{\circ}{\mathfrak{A}}$	
$\overset{\circ}{\mathfrak{E}}\overset{\circ}{\mathfrak{O}}\overset{\circ}{\mathfrak{E}}\overset{\circ}{\mathfrak{U}}$	$\overset{\circ}{\mathfrak{C}}\overset{\circ}{\mathfrak{O}}\overset{\circ}{\mathfrak{A}}\overset{\circ}{\mathfrak{U}}$

±ê¼Ç	ÊÿÃ¿	, ü, Äµ¥°Å	Ç©Äú	ÈÕÆÜ						
Éè	¼Æ					½×¶Î±ê¼Ç	ÖÊ	Á¿	±È	ÀŸ
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±ê ×¼ » <sup>-</sup>						μŮ	ÕǺ	¹²	Õǻ	
Ǻú	×¼									

LRM

XS3	
■ ETH0 <sub>0</sub> RX-	AA1
■ DGND AA2	AA2
AA3	AA3
AA4	AA4
AA5	AA5
■ ETH1 <sub>0</sub> RX-	AA6
■ DGND AA7	AA7
■ ETH2 <sub>0</sub> RX-	AA8
■ DGND AA9	AA9
■ ETH3 <sub>0</sub> RX-	AA10
■ DGND AA11	AA11
AA12	AA12
AA13	AA13
■ DGND AA14	AA14
AA15	AA15
AA16	AA16
AA17	AA17
AA18	AA18
AA19	AA19
AA20	AA20
AA21	AA21
AA22	AA22
AA23	AA23
AA24	AA24
AA25	AA25
AA26	AA26
AA27	AA27
AA28	AA28
AA29	AA29
AA30	AA30
AA31	AA31
■ DGND AA32	AA32

LRMS1-A288-B225G2R-T1

XS3	
■ ETH0 <sub>0</sub> RX+	AB1
■ ETH0 <sub>0</sub> TX-	AB2
AB3	AB3
■ SYSMAIN#	AB4
AB5	AB5
■ ETH1 <sub>0</sub> RX+	AB6
■ ETH1 <sub>0</sub> TX-	AB7
■ ETH2 <sub>0</sub> RX+	AB8
■ ETH2 <sub>0</sub> TX-	AB9
■ ETH3 <sub>0</sub> RX+	AB10
■ ETH3 <sub>0</sub> TX-	AB11
AB12	AB12
AB13	AB13
AB14	AB14
AB15	AB15
AB16	AB16
AB17	AB17
AB18	AB18
AB19	AB19
AB20	AB20
AB21	AB21
AB22	AB22
AB23	AB23
AB24	AB24
AB25	AB25
AB26	AB26
AB27	AB27
AB28	AB28
AB29	AB29
AB30	AB30
AB31	AB31
AB32	AB32

LRMS1-A288-B225G2R-T1

XS3	
■ DGND AC1	AC1
■ ETH0 <sub>0</sub> TX+	AC2
■ DGND AC3	AC3
■ GSE#4	AC4
■ DGND AC5	AC5
■ DGND AC6	AC6
■ ETH1 <sub>0</sub> TX+	AC7
■ DGND AC8	AC8
■ ETH2 <sub>0</sub> TX+	AC9
■ DGND AC10	AC10
■ ETH3 <sub>0</sub> TX+	AC11
AC12	AC12
AC13	AC13
■ DGND AC14	AC14
AC15	AC15
AC16	AC16
AC17	AC17
AC18	AC18
AC19	AC19
AC20	AC20
AC21	AC21
AC22	AC22
AC23	AC23
AC24	AC24
AC25	AC25
AC26	AC26
AC27	AC27
AC28	AC28
AC29	AC29
AC30	AC30
AC31	AC31
■ DGND AC32	AC32

LRMS1-A288-B225G2R-T1

XS3	
■ GTH0 <sub>0</sub> T0-	AD1
■ DGND AD2	AD2
■ GTH0 <sub>0</sub> T2-	AD3
■ DGND AD4	AD4
■ GTH0 <sub>0</sub> T0-	AD5
■ DGND AD6	AD6
■ GTH0 <sub>0</sub> T2-	AD7
■ DGND AD8	AD8
■ GTH2 <sub>0</sub> T0-	AD9
■ DGND AD10	AD10
■ GTH5 <sub>0</sub> T0-	AD11
■ DGND AD12	AD12
■ GTH7 <sub>0</sub> T0-	AD13
■ DGND AD14	AD14
■ GTH9 <sub>0</sub> T0-	AD15
■ DGND AD16	AD16
■ GTH1 <sub>0</sub> T0-	AD17
■ DGND AD18	AD18
■ GTH1 <sub>3</sub> T0-	AD19
■ DGND AD20	AD20
■ GTH1 <sub>5</sub> T0-	AD21
■ DGND AD22	AD22
■ GTH1 <sub>6</sub> T0-	AD23
■ DGND AD24	AD24
AD25	AD25
AD26	AD26
AD27	AD27
AD28	AD28
AD29	AD29
AD30	AD30
AD31	AD31
AD32	AD32

LRMS1-A288-B225G2R-T1

XS3	
■ GTH0 <sub>0</sub> T0-	AE1
■ GTH0 <sub>0</sub> T1-	AE2
■ GTH0 <sub>0</sub> T2-	AE3
■ GTH0 <sub>0</sub> T3-	AE4
■ GTH0 <sub>0</sub> T0-	AE5
■ GTH0 <sub>0</sub> T1-	AE6
■ GTH0 <sub>0</sub> T2-	AE7
■ GTH0 <sub>0</sub> T3-	AE8
■ GTH2 <sub>0</sub> T0-	AE9
■ GTH4 <sub>0</sub> T0-	AE10
■ GTH5 <sub>0</sub> T0-	AE11
■ GTH6 <sub>0</sub> T0-	AE12
■ GTH7 <sub>0</sub> T0-	AE13
■ GTH8 <sub>0</sub> T0-	AE14
■ GTH9 <sub>0</sub> T0-	AE15
■ GTH1 <sub>0</sub> T0-	AE16
■ GTH1 <sub>3</sub> T0-	AE17
■ GTH1 <sub>5</sub> T0-	AE18
■ GTH1 <sub>6</sub> T0-	AE19
■ GTH1 <sub>7</sub> T0-	AE20
■ GTH1 <sub>8</sub> T0-	AE21
■ GTH1 <sub>9</sub> T0-	AE22
■ GTH1 <sub>0</sub> T0-	AE23
AE24	AE24
AE25	AE25
AE26	AE26
AE27	AE27
AE28	AE28
AE29	AE29
AE30	AE30
AE31	AE31
AE32	AE32

LRMS1-A288-B225G2R-T1

XS3	
■ DGND AF1	AF1
■ GTH0 <sub>0</sub> T1-	AF2
■ DGND AF3	AF3
■ GTH0 <sub>0</sub> T3-	AF4
■ DGND AF5	AF5
■ DGND AF6	AF6
■ DGND AF7	AF7
■ GTH0 <sub>0</sub> T3-	AF8
■ DGND AF9	AF9
■ GTH4 <sub>0</sub> T0+	AF10
■ DGND AF11	AF11
■ GTH6 <sub>0</sub> T0+	AF12
■ DGND AF13	AF13
■ GTH8 <sub>0</sub> T0+	AF14
■ DGND AF15	AF15
■ GTH1 <sub>0</sub> T0+	AF16
■ DGND AF17	AF17
■ GTH1 <sub>3</sub> T0+	AF18
■ DGND AF19	AF19
■ GTH1 <sub>4</sub> T0+	AF20
■ DGND AF21	AF21
■ GTH1 <sub>5</sub> T0+	AF22
■ DGND AF23	AF23
AF24	AF24
AF25	AF25
AF26	AF26
AF27	AF27
AF28	AF28
AF29	AF29
AF30	AF30
AF31	AF31
AF32	AF32

LRMS1-A288-B225G2R-T1

XS3	
■ GTH0 <sub>0</sub> R0-	AG1
■ DGND AG2	AG2
■ GTH0 <sub>0</sub> R2-	AG3
■ DGND AG4	AG4
■ GTH0 <sub>0</sub> R0-	AG5
■ DGND AG6	AG6
■ GTH0 <sub>0</sub> R2-	AG7
■ DGND AG8	AG8
■ GTH2 <sub>0</sub> R0-	AG9
■ DGND AG10	AG10
■ GTH5 <sub>0</sub> R0-	AG11
■ DGND AG12	AG12
■ GTH7 <sub>0</sub> R0-	AG13
■ DGND AG14	AG14
■ GTH9 <sub>0</sub> R0-	AG15
■ DGND AG16	AG16
■ GTH1 <sub>0</sub> R0-	AG17
■ DGND AG18	AG18
■ GTH1 <sub>3</sub> R0-	AG19
■ DGND AG20	AG20
■ GTH1 <sub>4</sub> R0-	AG21
■ DGND AG22	AG22
■ GTH1 <sub>5</sub> R0-	AG23
■ GTH1 <sub>6</sub> R0-	AG24
AG25	AG25
AG26	AG26
AG27	AG27
AG28	AG28
AG29	AG29
AG30	AG30
AG31	AG31
■ DGND AG32	AG32

LRMS1-A288-B225G2R-T1

XS3	
■ GTH0 <sub>0</sub> R1-	AH1
■ GTH0 <sub>0</sub> R2-	AH2
■ GTH0 <sub>0</sub> R3-	AH3
■ GTH0 <sub>0</sub> R0-	AH4
■ DGND AH5	AH5
■ GTH0 <sub>0</sub> R1-	AH6
■ GTH0 <sub>0</sub> R2-	AH7
■ GTH0 <sub>0</sub> R3-	AH8
■ GTH2 <sub>0</sub> R0-	AH9
■ GTH4 <sub>0</sub> R0-	AH10
■ GTH5 <sub>0</sub> R0-	AH11
■ GTH6 <sub>0</sub> R0-	AH12
■ GTH7 <sub>0</sub> R0-	AH13
■ GTH8 <sub>0</sub> R0-	AH14
■ GTH9 <sub>0</sub> R0-	AH15
■ GTH1 <sub>0</sub> R0-	AH16
■ GTH1 <sub>3</sub> R0-	AH17
■ GTH1 <sub>4</sub> R0-	AH18
■ GTH1 <sub>5</sub> R0-	AH19
■ GTH1 <sub>6</sub> R0-	AH20
■ GTH1 <sub>7</sub> R0-	AH21
■ GTH1 <sub>8</sub> R0-	AH22
■ GTH1 <sub>9</sub> R0-	AH23
■ DGND AH24	AH24
AH25	AH25
AH26	AH26
AH27	AH27
AH28	AH28
AH29	AH29
AH30	AH30
AH31	AH31
AH32	AH32

LRMS1-A288-B225G2R-T1

XS3	
■ DGND AI1	AI1
■ GTH0 <sub>0</sub> R1+	AI2
■ DGND AI3	AI3
■ GTH0 <sub>0</sub> R3+	AI4
■ DGND AI5	AI5
■ GTH0 <sub>0</sub> R1+	AI6
■ DGND AI7	AI7
■ GTH0 <sub>0</sub> R3+	AI8
■ DGND AI9	AI9
■ GTH4 <sub>0</sub> R0+	AI10
■ DGND AI11	AI11
■ GTH5 <sub>0</sub> R0+	AI12
■ DGND AI13	AI13
■ GTH6 <sub>0</sub> R0+	AI14
■ DGND AI15	AI15
■ GTH1 <sub>0</sub> R0+	AI16
■ DGND AI17	AI17
■ GTH1 <sub>3</sub> R0+	AI18
■ DGND AI19	AI19
■ GTH1 <sub>4</sub> R0+	AI20
■ DGND AI21	AI21
■ GTH1 <sub>5</sub> R0+	AI22
■ DGND AI23	AI23
AI24	AI24
AI25	AI25
AI26	AI26
AI27	AI27
AI28	AI28
AI29	AI29
AI30	AI30
AI31	AI31
■ DGND AI32	AI32

LRMS1-A288-B225G2R-T1

XS3	
■ MARK BA1	BA1
■ MARK BA2	BA2
■ MARK BA3	BA3
■ MARK BA4	BA4
BA5	BA5
BA6	BA6
BA7	BA7
BA8	BA8
■ RST BA9	BA9
■ DGND BA10	BA10
BA11	BA11
BA12	BA12
BA13	BA13
BA14	BA14
BA15	BA15
BA16	BA16
BA17	BA17
BA18	BA18
BA19	BA19
■ PGND BA20	BA20
BA21	BA21
■ +28V BA22	BA22
■ +28V BA23	BA23
■ +28V BA24	BA24
■ +28V BA25	BA25

LRMS1-A288-B225G2R-T1

XS3		
■ DGND BB1	BB1	
■ RS485U4 RX BB2	BB2	
■ RS485U4 TX BB3	BB3	
■ RS485U4 TX BB4	BB4	
■ RS485U4 TX BB5	BB5	
■ RS485U5 TX BB6	BB6	
■ RS485U5 TX BB7	BB7	
■ RS485U5 RX BB8	BB8	
■ RS485U5 RX BB9	BB9	
■ DGND BB10	BB10	
BB11	BB11	
BB12	BB12	
BB13	BB13	
BB14	BB14	
BB15	BB15	
BB16	BB16	
BB17	BB17	
BB18	BB18	
BB19	BB19	
■ PGND BB20	BB20	
BB21	BB21	
■ +28V BB22	BB22	
■ +28V BB23	BB23	
■ +28V BB24	BB24	
■ +28V BB25	BB25	

LRMS1-A288-B225G2R-T1

XS3		
■ RS485U2 RX BC1	BC1	
■ RS485U2 RX BC2	BC2	
■ RS485U2 TX BC3	BC3	
■ RS485U2 TX BC4	BC4	
■ RS485U3 TX BC5	BC5	
■ RS485U3 TX BC6	BC6	
■ RS485U3 RX BC7	BC7	
■ RS485U3 RX BC8	BC8	
■ DGND BC9	BC9	
■ DGND BC10	BC10	
BC11	BC11	
BC12	BC12	
BC13	BC13	
BC14	BC14	
BC15	BC15	
BC16	BC16	
BC17	BC17	
BC18	BC18	
BC19	BC19	
■ PGND BC20	BC20	
BC21	BC21	
BC22	BC22	
BC23	BC23	
BC24	BC24	
BC25	BC25	

LRMS1-A288-B225G2R-T1

XS3		
■ DGND BD1	BD1	
BD2	BD2	
BD3	BD3	
BD4	BD4	
■ DGND BD5	BD5	
■ RS485U1 TX BD6	BD6	
■ RS485U1 TX BD7	BD7	
■ RS485U1 RX BD8	BD8	
■ RS485U1 RX BD9	BD9	
■ DGND BD10	BD10	
BD11	BD11	
BD12	BD12	
BD13	BD13	
BD14	BD14	
BD15	BD15	
BD16	BD16	
BD17	BD17	
BD18	BD18	
BD19	BD19	
■ PGND BD20	BD20	
BD21	BD21	
BD22	BD22	
BD23	BD23	
BD24	BD24	
BD25	BD25	

LRMS1-A288-B225G2R-T1

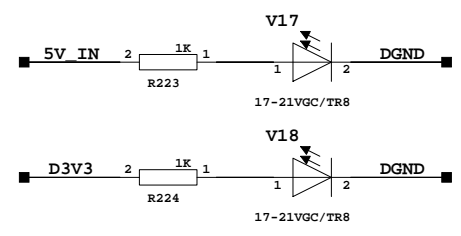
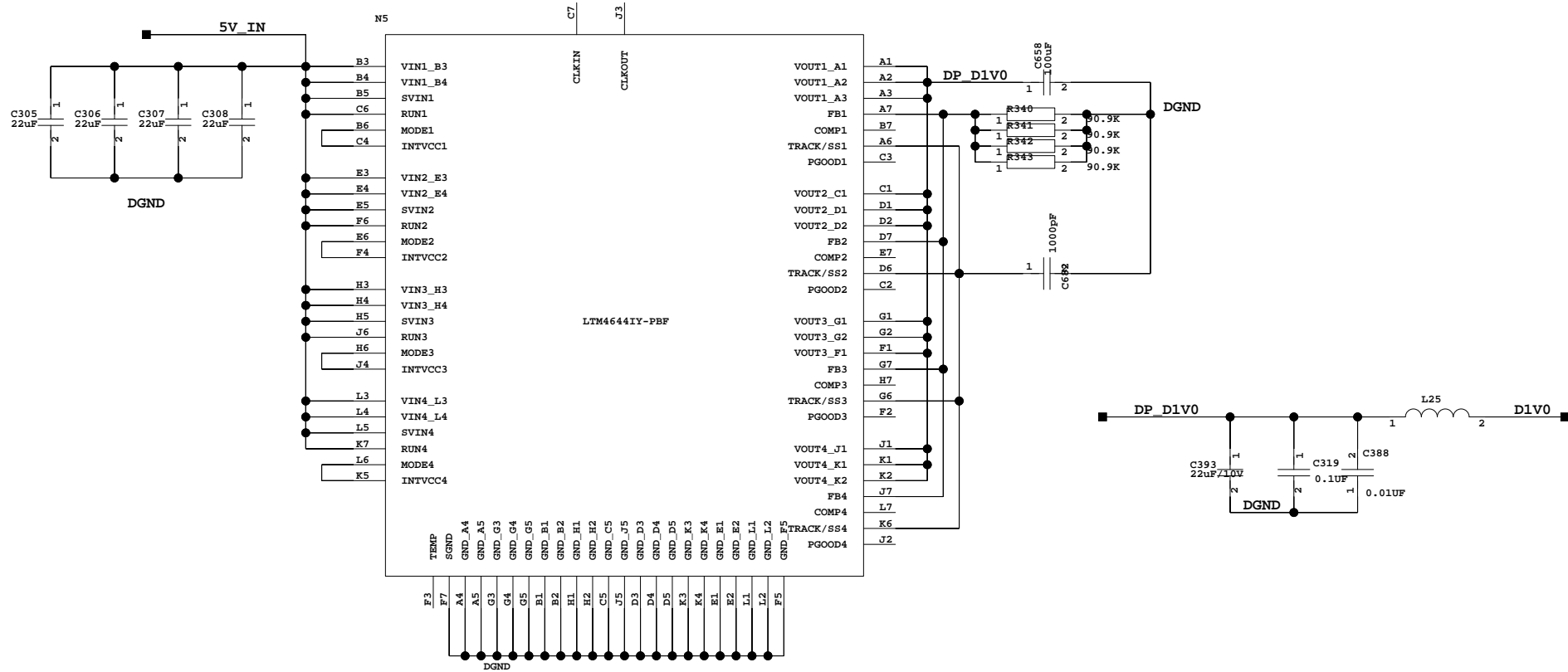
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BE1	BE1	
BE2	BE2	
BE3	BE3	
BE4	BE4	
BE5	BE5	
BE6	BE6	
BE7	BE7	
BE8	BE8	
■ DGND BE9	BE9	
BE10	BE10	
BE11	BE11	
BE12	BE12	
BE13	BE13	
BE14	BE14	
BE15	BE15	
BE16	BE16	
BE17	BE17	
BE18	BE18	
BE19	BE19	
■ PDND BE20	BE20	
■ PDND BE21	BE21	
■ PDND BE22	BE22	
■ PDND BE23	BE23	
■ PDND BE24	BE24	
■ PDND BE25	BE25	

LRMS1-A288-B225G2R-T1

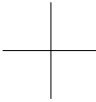
XS3		
■ DGND BF1	BF1	
■ DGND BF2	BF2	
■ DGND BF3	BF3	
■ DGND BF4	BF4	
■ DGND BF5	BF5	
■ DGND BF6	BF6	
■ DGND BF7	BF7	
■ DGND BF8	BF8	
■ DGND BF9	BF9	
■ DGND BF10	BF10	
■ DGND BF11	BF11	
■ DGND BF12	BF12	
■ DGND BF13	BF13	
■ DGND BF14	BF14	
■ DGND BF15	BF15	
■ DGND BF16	BF16	
■ DGND BF17	BF17	
■ DGND BF18	BF18	
■ DGND BF19	BF19	
■ DGND BF20	BF20	
■ DGND BF21	BF21	
■ DGND BF22	BF22	
■ DGND BF23	BF23	
■ DGND BF24	BF24	
■ DGND BF25	BF25	

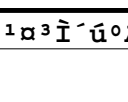
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XS3
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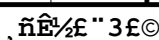
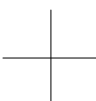
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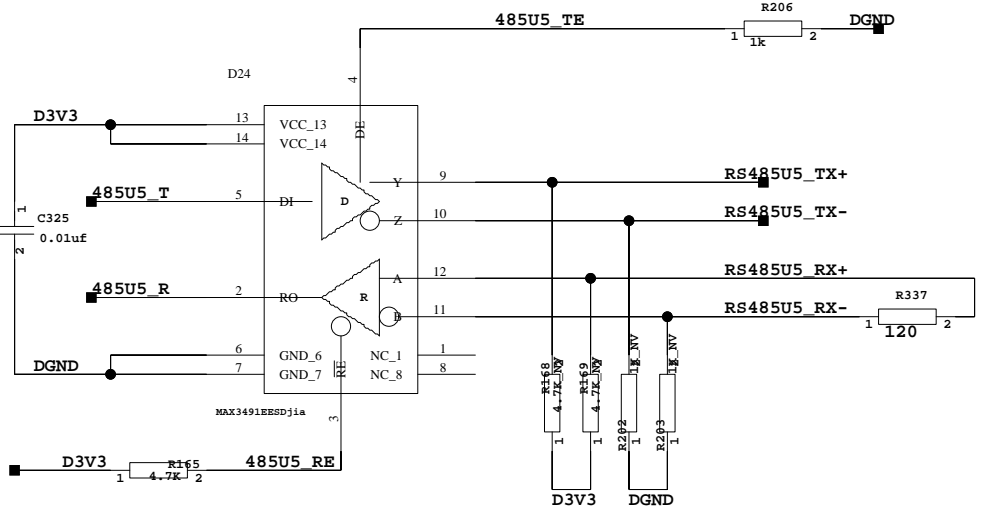


## A blank coordinate plane with a horizontal x-axis and a vertical y-axis intersecting at the origin. There are no tick marks or labels on the axes.

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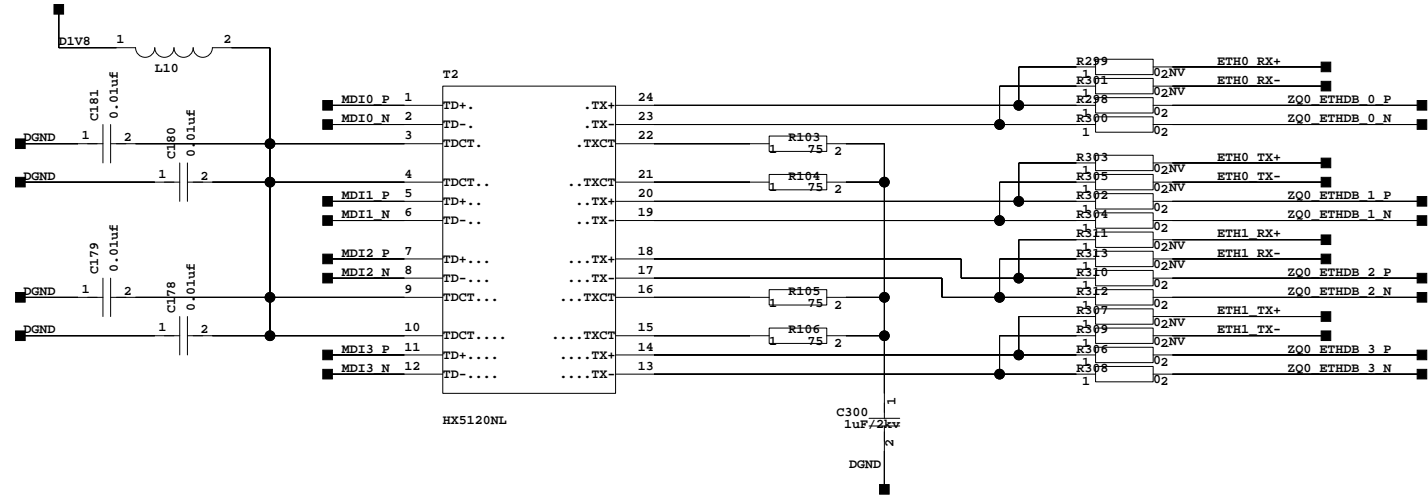
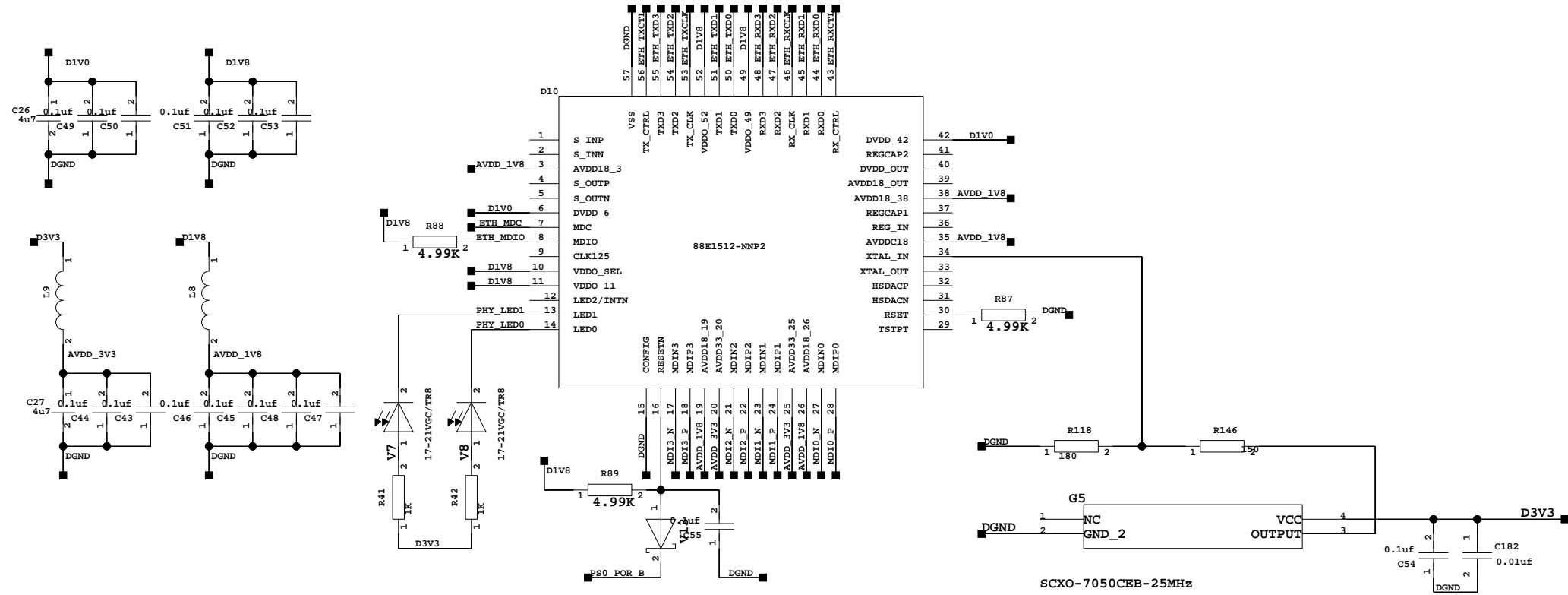
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AA19  
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AE20  
  
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AG22  
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AF19  
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W21  
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Y22  
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AA24  
AB24  
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AC23  
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XC7Z045\_2FFG900I\_C

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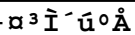


Z0\_ETH



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±ê¼Ç	ÊÿÁ¿	, ü , ÄµŸ°Ă	Ç©Ãû	ÈÕÆÚ		
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±ê x¼ »⁻						
Ǻū	x¼					



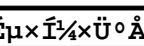
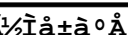
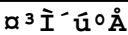
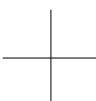
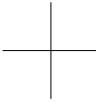
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$$\lambda_{1/2} \approx 10^3 \text{ Å}$$
$$\hat{E}_\mu \times \hat{I}^{1/4} \times \ddot{U}^{\circ} \text{Å}$$
 $1 \times \hat{I}^{1/4} \times \ddot{U}^{\circ} \text{ \AA}$ 

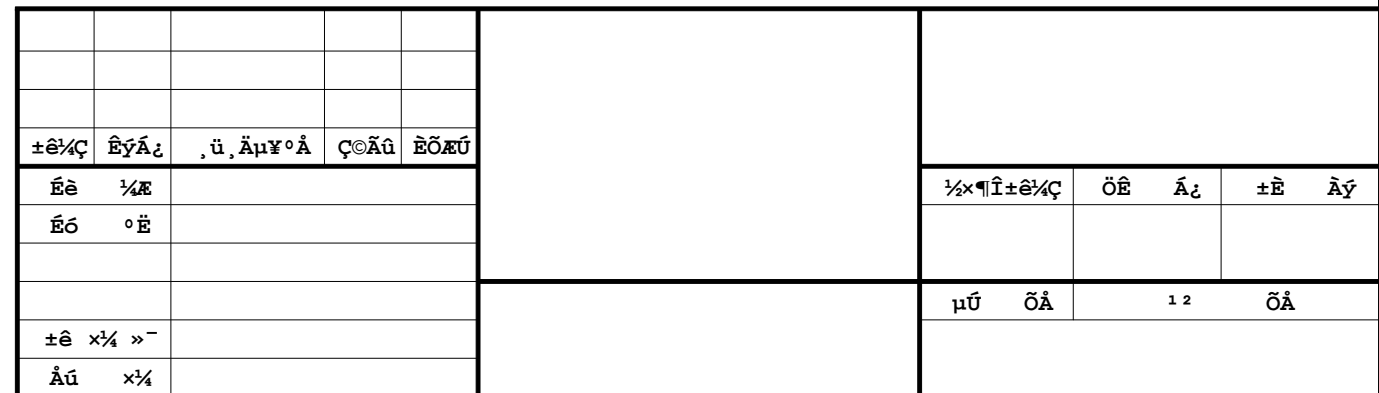
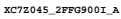
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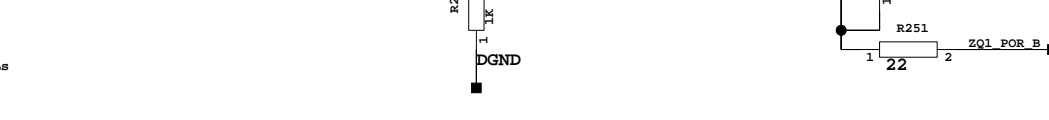
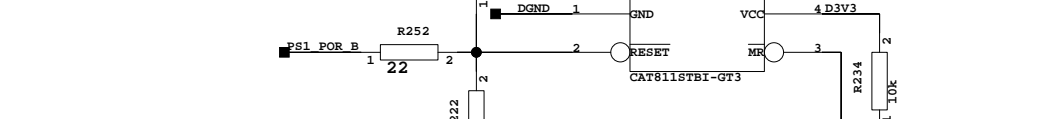
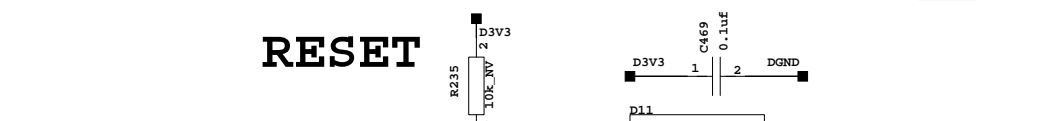
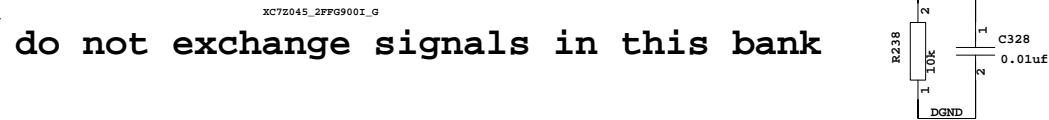
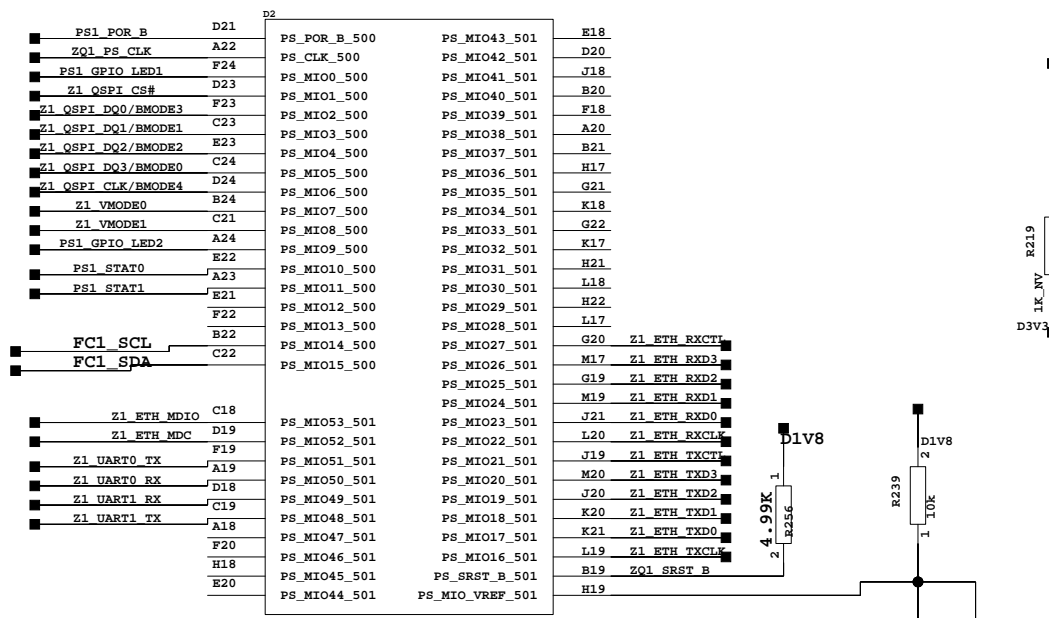
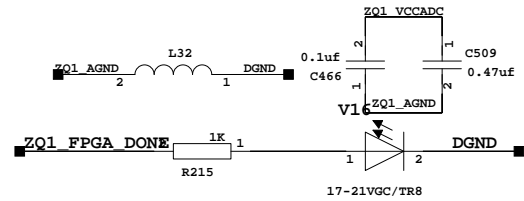
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Z0\_PS\_MIO



**RESET**



[illegible]

# CLOCK

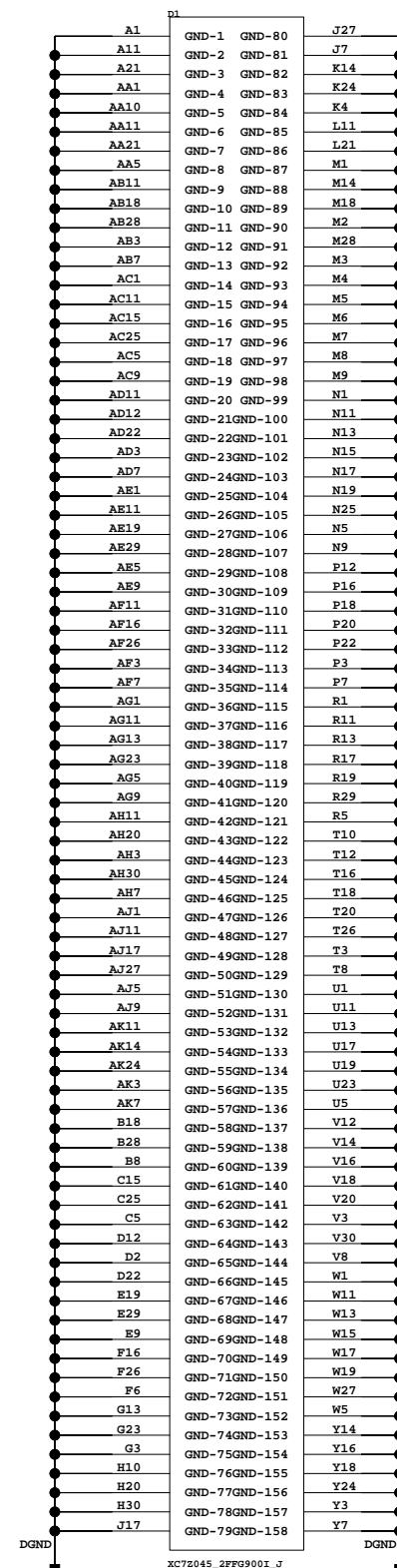
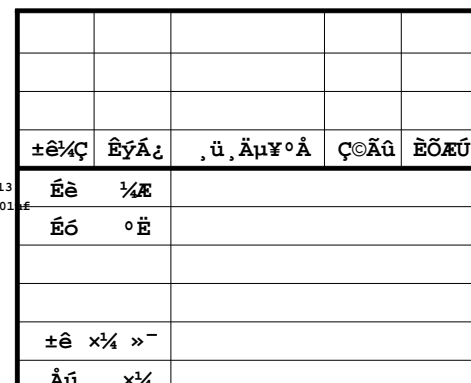
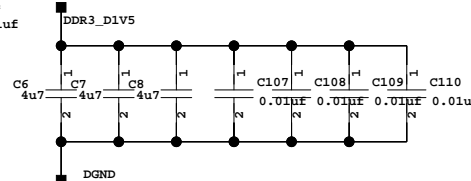
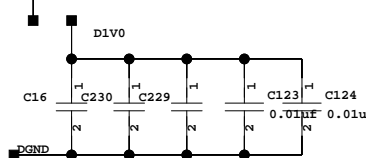
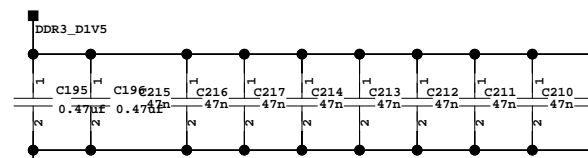
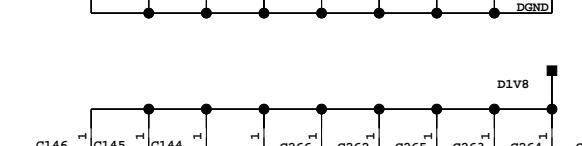
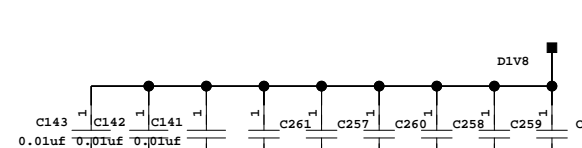
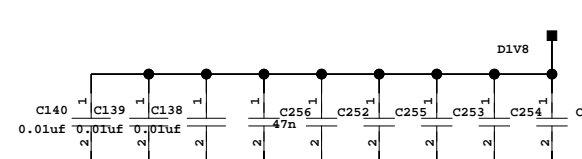
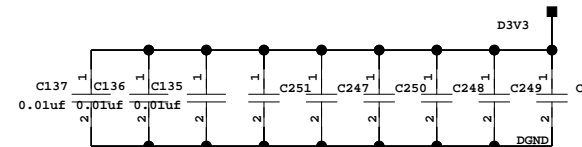
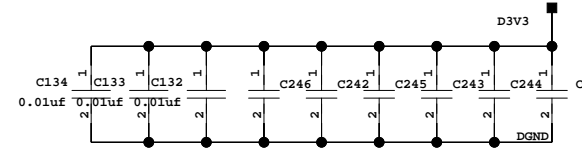
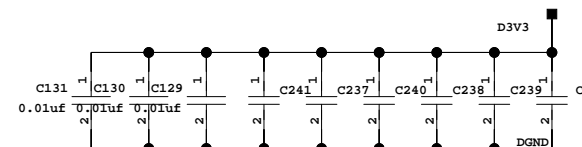
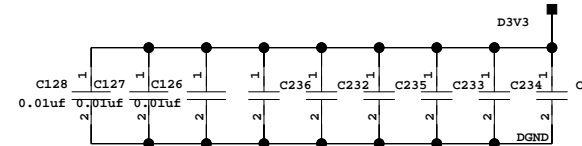
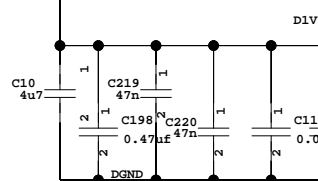
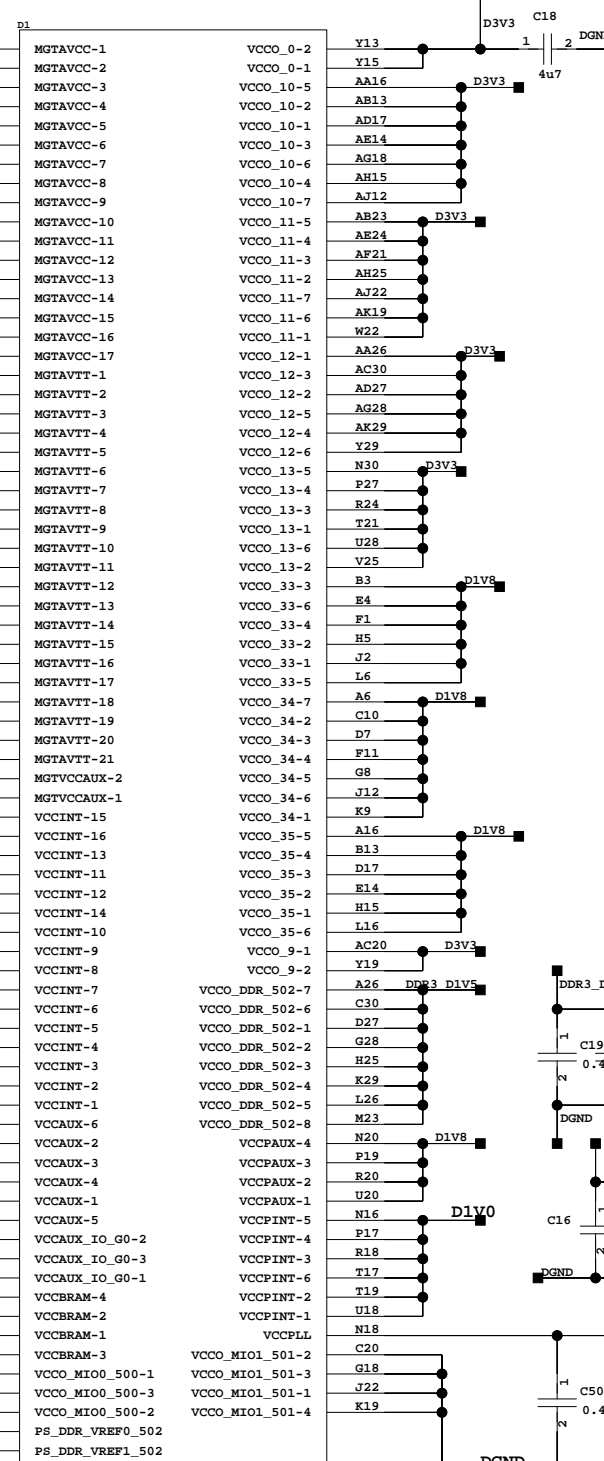
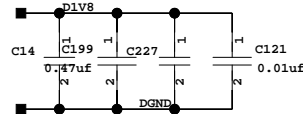
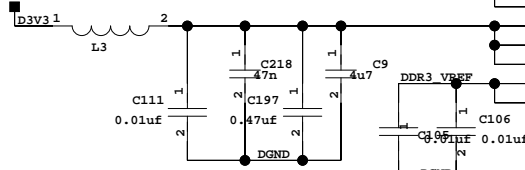
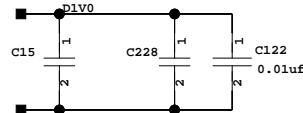
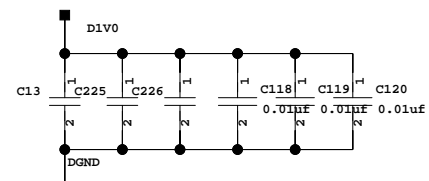
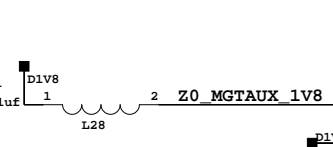
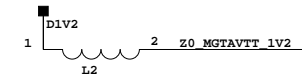
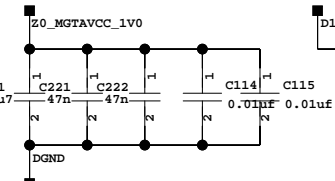
The diagram illustrates the clock circuit for the PS module. It features a crystal oscillator (G4) connected to a 3.3V supply (D3V3) via an inductor (L31). The crystal's output is connected to a buffer (ZQ1) through a 22 ohm resistor (R250). A 0.1uF capacitor (C465) is connected between the crystal's output and ground (DGND). The buffer's output is connected to the PS\_CLK pin.

# QSPI FLASH

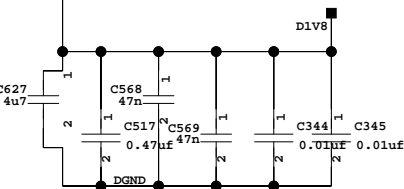
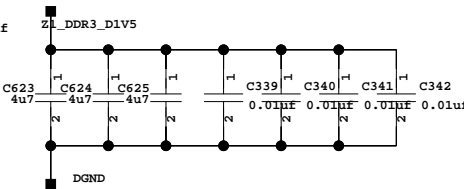
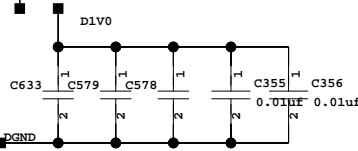
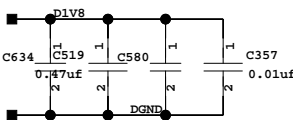
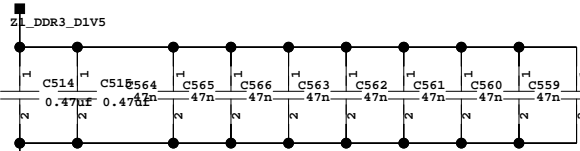
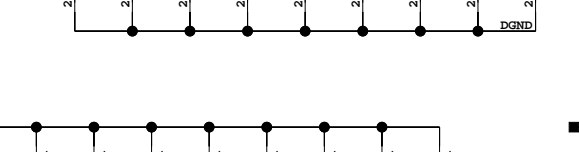
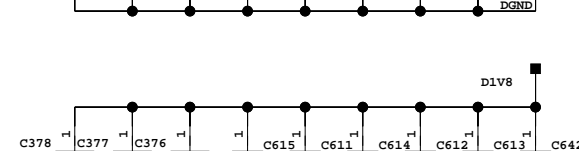
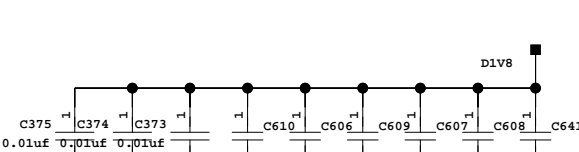
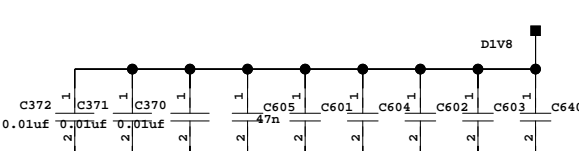
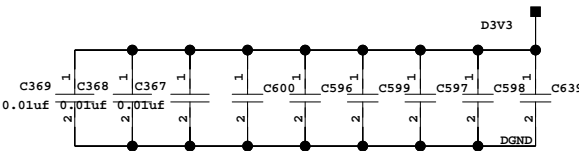
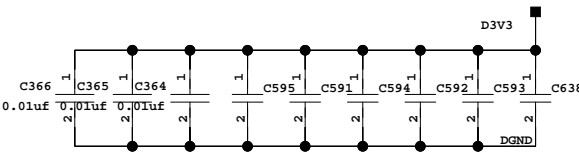
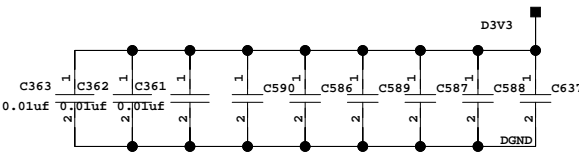
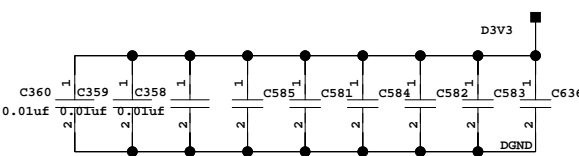
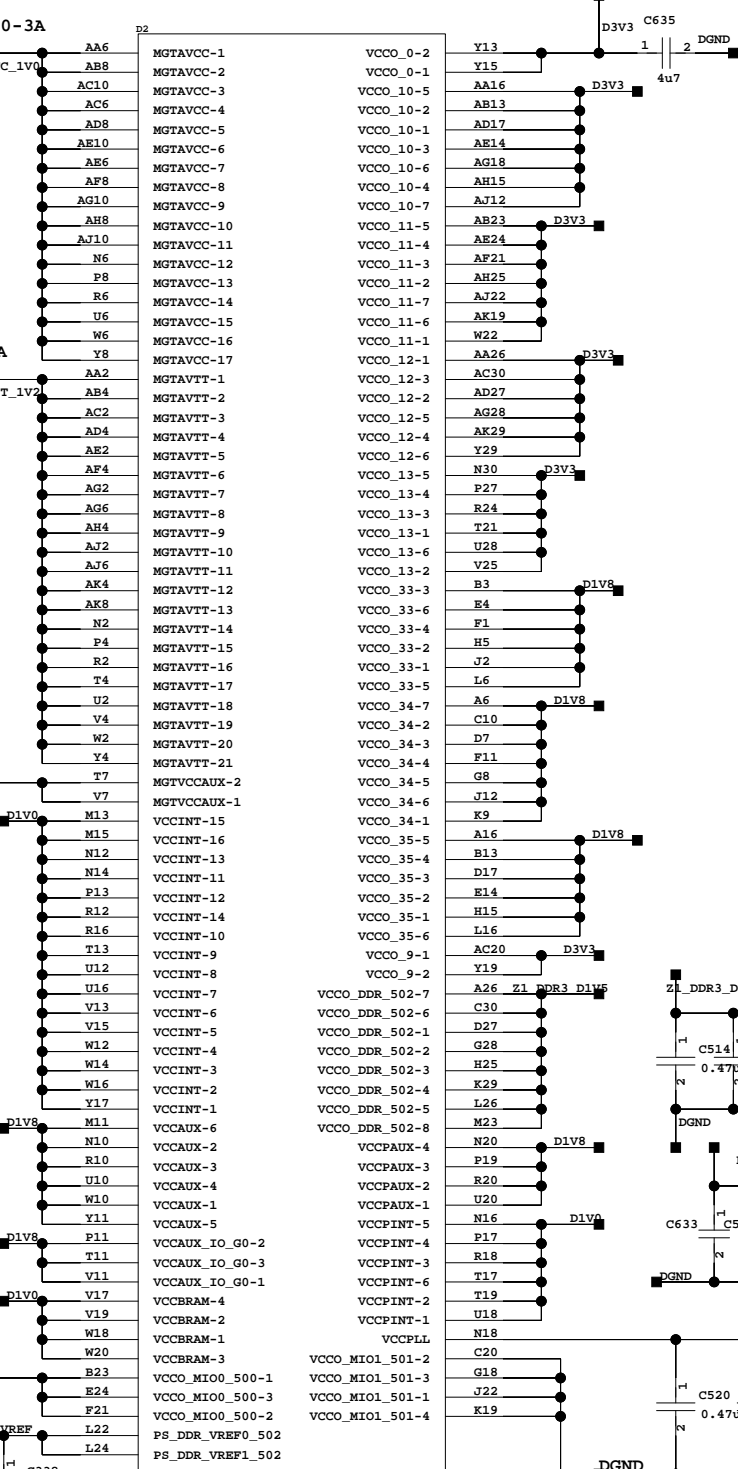
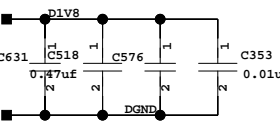
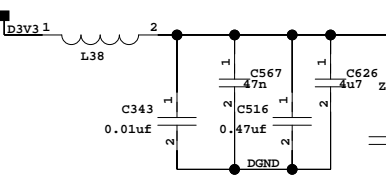
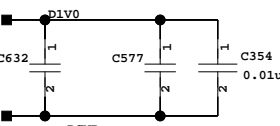
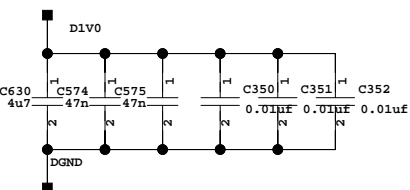
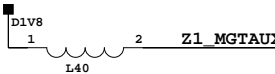
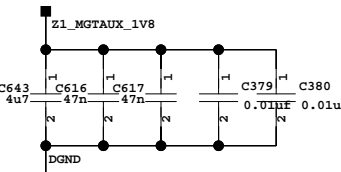
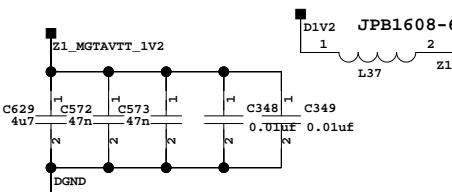
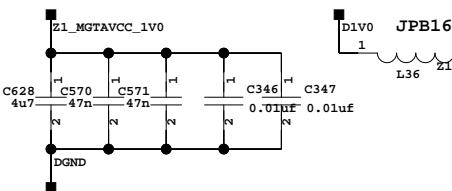
The diagram illustrates the electrical connection for a QSPI Flash. On the left, a 10k resistor (R233) is connected between a D3V3 supply and the CS# pin (pin 1) of the flash chip. The chip's pins are numbered 1 through 9. Pins 2, 3, 4, 5, 6, and 7 are connected to D3V3. Pin 8 is connected to GND. Pin 9 is also connected to GND. The flash chip is labeled N25Q256A13EF840E. The pins are labeled as follows: 1 CS#, 2 DQ0/BMODE3, 3 DQ2/BMODE2, 4 VSS, 5 DQ0/BMODE3, 6 CLK/BMODE4, 7 DQ3/BMODE0, 8 DQ1, 9 GND. The chip is also labeled with VCC, GND, and TP.

[illegible]

Z0\_PWR\_GND



**Z1\_PWR\_GND**



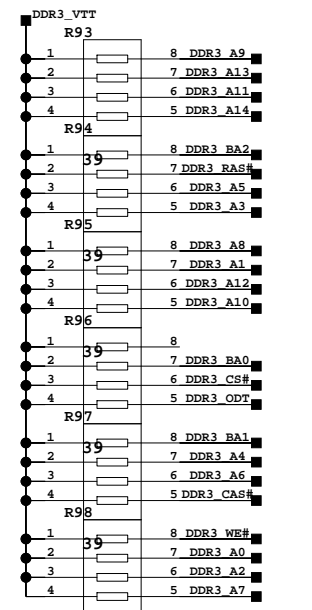
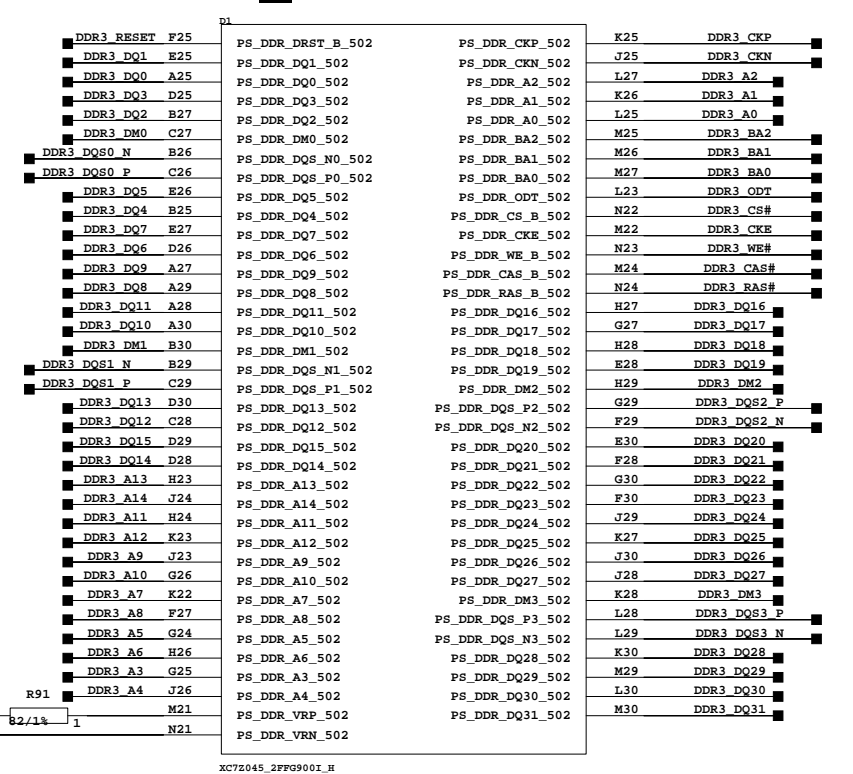
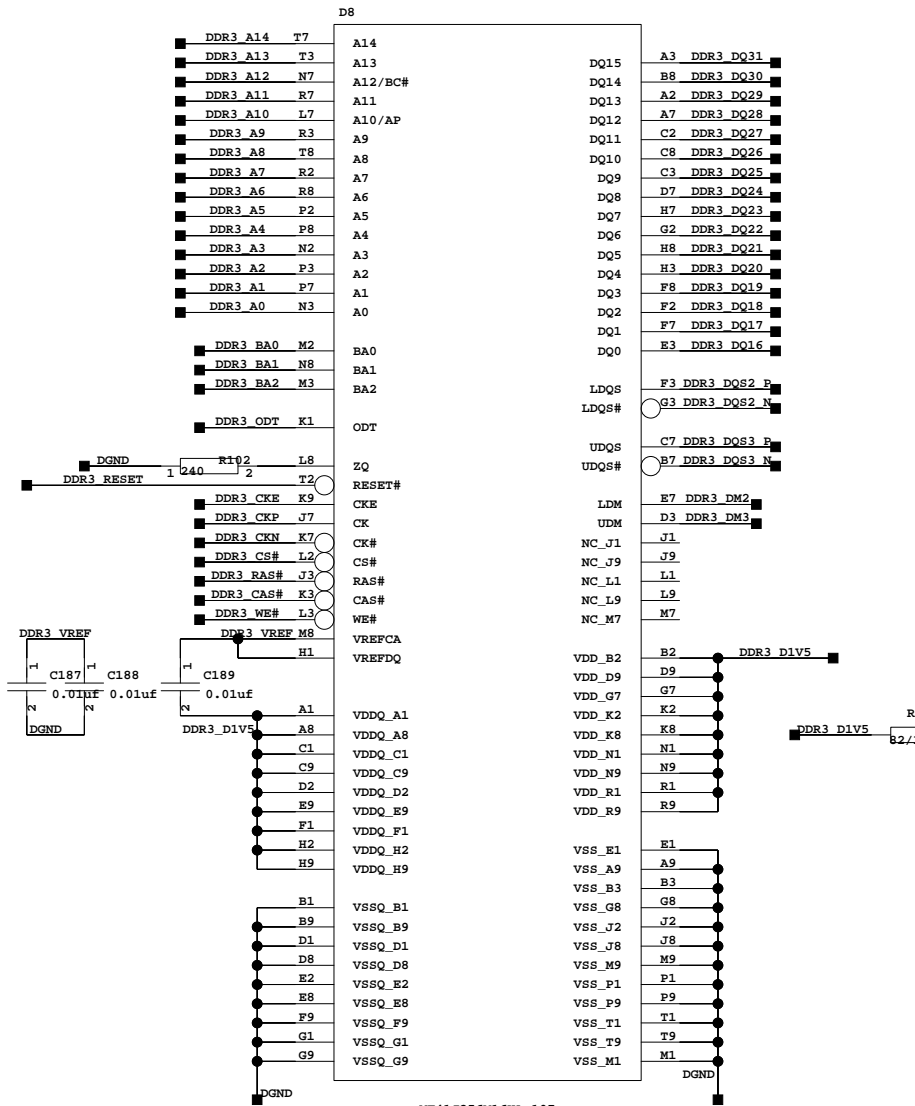
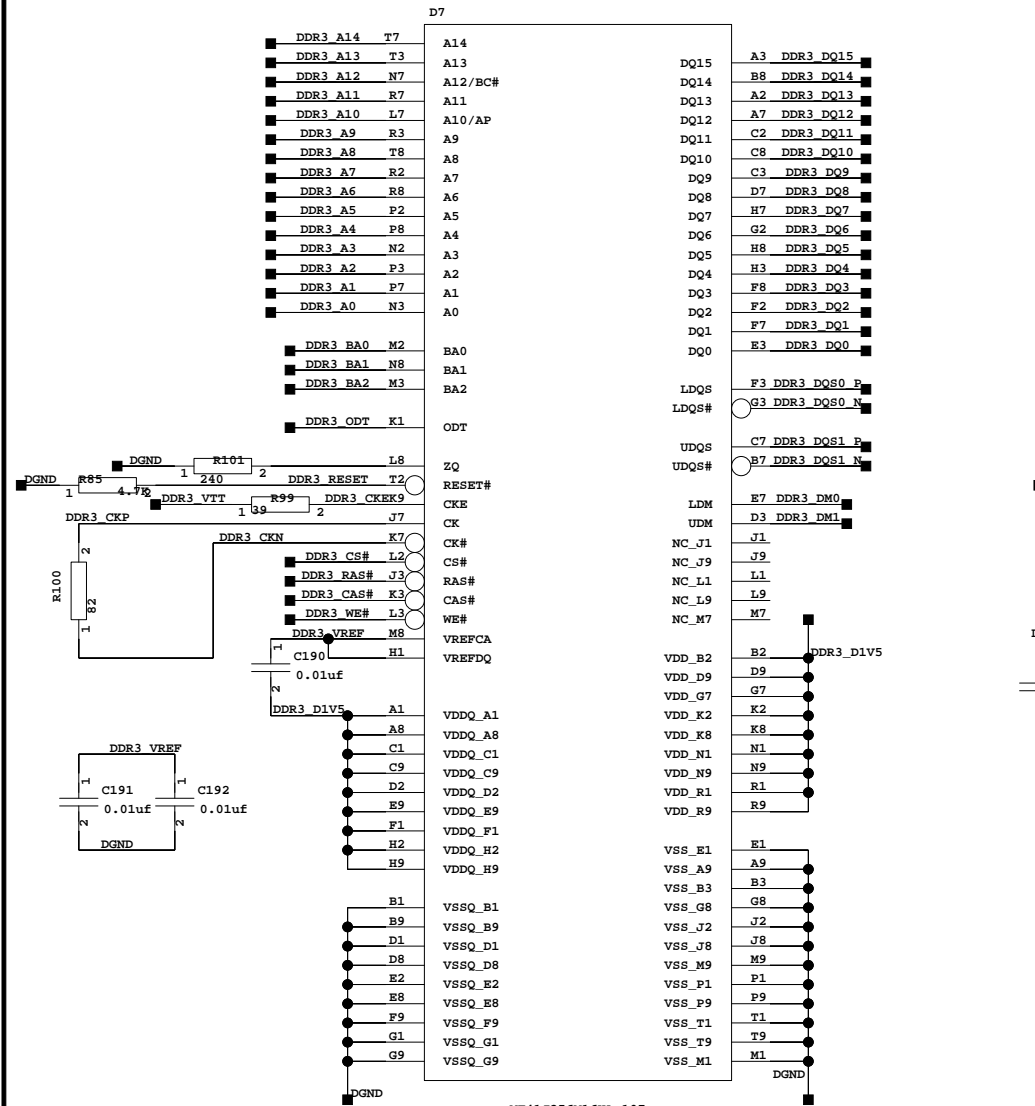
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						<div>µŮ    ŌǺ</div> <div>1 2              ŌǺ</div>

D2			
A1	GND-1	GND-80	J27
A11	GND-2	GND-81	J7
A21	GND-3	GND-82	K14
AA1	GND-4	GND-83	K24
AA10	GND-5	GND-84	K4
AA11	GND-6	GND-85	L11
AA21	GND-7	GND-86	L21
AA5	GND-8	GND-87	M1
AB11	GND-9	GND-88	M14
AB18	GND-10	GND-89	M18
AB28	GND-11	GND-90	M2
AB3	GND-12	GND-91	M28
AB7	GND-13	GND-92	M3
AC1	GND-14	GND-93	M4
AC11	GND-15	GND-94	M5
AC15	GND-16	GND-95	M6
AC25	GND-17	GND-96	M7
AC5	GND-18	GND-97	M8
AC9	GND-19	GND-98	M9
AD11	GND-20	GND-99	N1
AD12	GND-21GND-100		N11
AD22	GND-22GND-101		N13
AD3	GND-23GND-102		N15
AD7	GND-24GND-103		N17
AE1	GND-25GND-104		N19
AE11	GND-26GND-105		N25
AE19	GND-27GND-106		N5
AE29	GND-28GND-107		N9
AE5	GND-29GND-108		F12
AE9	GND-30GND-109		F16
AF11	GND-31GND-110		P18
AF16	GND-32GND-111		P20
AF26	GND-33GND-112		P22
AF3	GND-34GND-113		P3
AF7	GND-35GND-114		P7
AG1	GND-36GND-115		R1
AG11	GND-37GND-116		R11
AG13	GND-38GND-117		R13
AG23	GND-39GND-118		R17
AG5	GND-40GND-119		R19
AG9	GND-41GND-120		R29
AH11	GND-42GND-121		R5
AH20	GND-43GND-122		T10
AH3	GND-44GND-123		T12
AH30	GND-45GND-124		T16
AH7	GND-46GND-125		T18
AJ71	GND-47GND-126		T20
AJ11	GND-48GND-127		T26
AJ17	GND-49GND-128		T3
AJ27	GND-50GND-129		T8
AJ5	GND-51GND-130		U1
AJ7	GND-52GND-131		U11
AK11	GND-53GND-132		U13
AK14	GND-54GND-133		U17
AK24	GND-55GND-134		U19
AK3	GND-56GND-135		U23
AK7	GND-57GND-136		U5
B18	GND-58GND-137		V12
B28	GND-59GND-138		V14
B8	GND-60GND-139		V16
C15	GND-61GND-140		V18
C25	GND-62GND-141		V20
C5	GND-63GND-142		V3
D12	GND-64GND-143		V30
D2	GND-65GND-144		V8
D22	GND-66GND-145		W1
E19	GND-67GND-146		W11
E29	GND-68GND-147		W13
E9	GND-69GND-148		W15
F16	GND-70GND-149		W17
F26	GND-71GND-150		W19
F6	GND-72GND-151		W27
G13	GND-73GND-152		W5
G23	GND-74GND-153		Y14
G3	GND-75GND-154		Y16
H10	GND-76GND-155		Y18
H20	GND-77GND-156		Y24
H30	GND-78GND-157		Y3
J17	GND-79GND-158		Y7

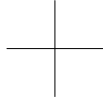
DGND
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DGND

Z0\_DDR3

# Z0\_BANK502

[illegible]

Z1\_DDR3



1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100

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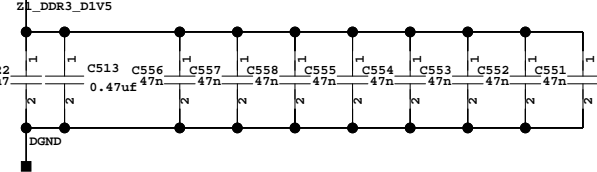
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$\frac{3}{4}\hat{E}_\mu \times \hat{I}^1_{\frac{1}{4}} \times \ddot{U} \circ \hat{A}$

$\mu \times \hat{I}^{\frac{1}{4}} \times \ddot{U} \circ \text{\AA}$

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3.  $\frac{1}{2} \times 3 = \frac{3}{2}$



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Êó	°Ë			
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ÄŮ	×¼			

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A3



Z0\_Z1\_HP

L5	IO_0_VRN_33	IO_0_VRN_34	M12
J4	IO_L1P_T0_33	IO_L1P_T0_34	B10
J3	IO_L1N_T0_33	IO_L1N_T0_34	A10
L1	IO_L2P_T0_33	IO_L2P_T0_34	B9
K1	IO_L2N_T0_33	IO_L2N_T0_34	A9
K3	IO_L3P_T0_DQS_33	IO_L3P_T0_DQS_PUDC_B_34	A8
K2	IO_L3N_T0_DQS_33	IO_L3N_T0_DQS_34	A7
L3	IO_L4P_T0_33	IO_L4P_T0_34	C7
L2	IO_L4N_T0_33	IO_L4N_T0_34	B7
K5	IO_L5P_T0_33	IO_L5P_T0_34	C6
J5	IO_L5N_T0_33	IO_L5N_T0_34	B6
K6	IO_L6P_T0_33	IO_L6P_T0_34	C9
J6	IO_L6N_T0_VREF_33	IO_L6N_T0_VREF_34	C8
G2	IO_L7P_T1_33	IO_L7P_T1_34	J11
F2	IO_L7N_T1_33	IO_L7N_T1_34	H11
H6	IO_L8P_T1_33	IO_L8P_T1_34	E11
G6	IO_L8N_T1_33	IO_L8N_T1_34	D11
J1	IO_L9P_T1_DQS_33	IO_L9P_T1_DQS_34	H12
H1	IO_L9N_T1_DQS_33	IO_L9N_T1_DQS_34	G11
H2	IO_L10P_T1_33	IO_L10P_T1_34	E10
G1	IO_L10N_T1_33	IO_L10N_T1_34	D10
H4	IO_L11P_T1_SRCC_33	IO_L11P_T1_SRCC_34	G10
H3	IO_L11N_T1_SRCC_33	IO_L11N_T1_SRCC_34	F10
G5	IO_L12P_T1_MRCC_33	IO_L12P_T1_MRCC_34	D9
G4	IO_L12N_T1_MRCC_33	IO_L12N_T1_MRCC_34	D8
F5	IO_L13P_T2_MRCC_33	IO_L13P_T2_MRCC_34	H9
E5	IO_L13N_T2_MRCC_33	IO_L13N_T2_MRCC_34	G9
F4	IO_L14P_T2_SRCC_33	IO_L14P_T2_SRCC_34	F9
F3	IO_L14N_T2_SRCC_33	IO_L14N_T2_SRCC_34	E8
E6	IO_L15P_T2_DQS_33	IO_L15P_T2_DQS_34	J8
D5	IO_L15N_T2_DQS_33	IO_L15N_T2_DQS_34	H8
D4	IO_L16P_T2_33	IO_L16P_T2_34	F8
D3	IO_L16N_T2_33	IO_L16N_T2_34	F7
E3	IO_L17P_T2_33	IO_L17P_T2_34	E7
E2	IO_L17N_T2_33	IO_L17N_T2_34	D6
E1	IO_L18P_T2_33	IO_L18P_T2_34	H7
D1	IO_L18N_T2_33	IO_L18N_T2_34	G7
C4	IO_L19P_T3_33	IO_L19P_T3_34	L7
C3	IO_L19N_T3_VREF_33	IO_L19N_T3_VREF_34	K7
B5	IO_L20P_T3_33	IO_L20P_T3_34	J10
B4	IO_L20N_T3_33	IO_L20N_T3_34	J9
A5	IO_L21P_T3_DQS_33	IO_L21P_T3_DQS_34	L8
A4	IO_L21N_T3_DQS_33	IO_L21N_T3_DQS_34	K8
C2	IO_L22P_T3_33	IO_L22P_T3_34	K11
C1	IO_L22N_T3_33	IO_L22N_T3_34	K10
B2	IO_L23P_T3_33	IO_L23P_T3_34	L10
B1	IO_L23N_T3_33	IO_L23N_T3_34	L9
A3	IO_L24P_T3_33	IO_L24P_T3_34	L12
A2	IO_L24N_T3_33	IO_L24N_T3_34	K12
L4	IO_25_VRP_33	IO_25_VRP_34	M10

XC7Z045\_2FFG9001\_D

K16	IO_0_VRN_35	
L15	IO_L1P_T0_AD0P_35	
L14	IO_L1N_T0_AD0N_35	
J13	IO_L2P_T0_AD8P_35	
H13	IO_L2N_T0_AD8N_35	
L13	IO_L3P_T0_DQS_AD1P_35	
K13	IO_L3N_T0_DQS_AD1N_35	
J14	IO_L4P_T0_35	
H14	IO_L4N_T0_35	
K15	IO_L5P_T0_AD9P_35	
J15	IO_L5N_T0_AD9N_35	
J16	IO_L6P_T0_35	
H16	IO_L6N_T0_VREF_35	
G17	IO_L7P_T1_AD2P_35	
G16	IO_L7N_T1_AD2N_35	
G15	IO_L8P_T1_AD10P_35	
G14	IO_L8N_T1_AD10N_35	
G12	IO_L9P_T1_DQS_AD3P_35	
F12	IO_L9N_T1_DQS_AD3N_35	
F13	IO_L10P_T1_AD11P_35	
E12	IO_L10N_T1_AD11N_35	
E13	IO_L11P_T1_SRCC_35	
D13	IO_L11N_T1_SRCC_35	
F15	IO_L12P_T1_MRCC_35	
F14	IO_L12N_T1_MRCC_35	
E16	IO_L13P_T2_MRCC_35	
E15	IO_L13N_T2_MRCC_35	
D15	IO_L14P_T2_AD4P_SRCC_35	
D14	IO_L14N_T2_AD4N_SRCC_35	
F17	IO_L15P_T2_DQS_AD12P_35	
H8	IO_L15N_T2_DQS_AD12N_35	
D16	IO_L16P_T2_35	
C16	IO_L16N_T2_35	
C17	IO_L17P_T2_AD5P_35	
B16	IO_L17N_T2_AD5N_35	
B17	IO_L18P_T2_AD13P_35	
A17	IO_L18N_T2_AD13N_35	
C14	IO_L19P_T3_35	
C13	IO_L19N_T3_VREF_35	
C12	IO_L20P_T3_AD6P_35	
B12	IO_L20N_T3_AD6N_35	
B15	IO_L21P_T3_DQS_AD14P_35	
A15	IO_L21N_T3_DQS_AD14N_35	
C11	IO_L22P_T3_AD7P_35	
B11	IO_L22N_T3_AD7N_35	
B14	IO_L23P_T3_35	
A14	IO_L23N_T3_35	
A13	IO_L24P_T3_AD15P_35	
A12	IO_L24N_T3_AD15N_35	
M16	IO_25_VRP_35	

XC7Z045\_2FFG9001\_E

L5	IO_0_VRN_33	IO_0_VRN_34	M12
J4	IO_L1P_T0_33	IO_L1P_T0_34	B10
J3	IO_L1N_T0_33	IO_L1N_T0_34	A10
L1	IO_L2P_T0_33	IO_L2P_T0_34	B9
K1	IO_L2N_T0_33	IO_L2N_T0_34	A9
K3	IO_L3P_T0_DQS_33	IO_L3P_T0_DQS_PUDC_B_34	A8
K2	IO_L3N_T0_DQS_33	IO_L3N_T0_DQS_34	A7
L3	IO_L4P_T0_33	IO_L4P_T0_34	C7
L2	IO_L4N_T0_33	IO_L4N_T0_34	B7
K5	IO_L5P_T0_33	IO_L5P_T0_34	C6
J5	IO_L5N_T0_33	IO_L5N_T0_34	B6
K6	IO_L6P_T0_33	IO_L6P_T0_34	C9
J6	IO_L6N_T0_VREF_33	IO_L6N_T0_VREF_34	C8
G2	IO_L7P_T1_33	IO_L7P_T1_34	J11
F2	IO_L7N_T1_33	IO_L7N_T1_34	H11
H6	IO_L8P_T1_33	IO_L8P_T1_34	E11
G6	IO_L8N_T1_33	IO_L8N_T1_34	D11
J1	IO_L9P_T1_DQS_33	IO_L9P_T1_DQS_34	H12
H1	IO_L9N_T1_DQS_33	IO_L9N_T1_DQS_34	G11
H2	IO_L10P_T1_33	IO_L10P_T1_34	E10
G1	IO_L10N_T1_33	IO_L10N_T1_34	D10
H4	IO_L11P_T1_SRCC_33	IO_L11P_T1_SRCC_34	G10
H3	IO_L11N_T1_SRCC_33	IO_L11N_T1_SRCC_34	F10
G5	IO_L12P_T1_MRCC_33	IO_L12P_T1_MRCC_34	D9
G4	IO_L12N_T1_MRCC_33	IO_L12N_T1_MRCC_34	D8
F5	IO_L13P_T2_MRCC_33	IO_L13P_T2_MRCC_34	H9
E5	IO_L13N_T2_MRCC_33	IO_L13N_T2_MRCC_34	G9
F4	IO_L14P_T2_SRCC_33	IO_L14P_T2_SRCC_34	F9
F3	IO_L14N_T2_SRCC_33	IO_L14N_T2_SRCC_34	E8
E6	IO_L15P_T2_DQS_33	IO_L15P_T2_DQS_34	J8
D5	IO_L15N_T2_DQS_33	IO_L15N_T2_DQS_34	H8
D4	IO_L16P_T2_33	IO_L16P_T2_34	F8
D3	IO_L16N_T2_33	IO_L16N_T2_34	F7
E3	IO_L17P_T2_33	IO_L17P_T2_34	E7
E2	IO_L17N_T2_33	IO_L17N_T2_34	D6
E1	IO_L18P_T2_33	IO_L18P_T2_34	H7
D1	IO_L18N_T2_33	IO_L18N_T2_34	G7
C4	IO_L19P_T3_33	IO_L19P_T3_34	L7
C3	IO_L19N_T3_VREF_33	IO_L19N_T3_VREF_34	K7
B5	IO_L20P_T3_33	IO_L20P_T3_34	J10
B4	IO_L20N_T3_33	IO_L20N_T3_34	J9
A5	IO_L21P_T3_DQS_33	IO_L21P_T3_DQS_34	L8
A4	IO_L21N_T3_DQS_33	IO_L21N_T3_DQS_34	K8
C2	IO_L22P_T3_33	IO_L22P_T3_34	K11
C1	IO_L22N_T3_33	IO_L22N_T3_34	K10
B2	IO_L23P_T3_33	IO_L23P_T3_34	L10
B1	IO_L23N_T3_33	IO_L23N_T3_34	L9
A3	IO_L24P_T3_33	IO_L24P_T3_34	L12
A2	IO_L24N_T3_33	IO_L24N_T3_34	K12
L4	IO_25_VRP_33	IO_25_VRP_34	M10

XC7Z045\_2FFG9001\_D

K16	IO_0_VRN_35	
L15	IO_L1P_T0_AD0P_35	
L14	IO_L1N_T0_AD0N_35	
J13	IO_L2P_T0_AD8P_35	
H13	IO_L2N_T0_AD8N_35	
L13	IO_L3P_T0_DQS_AD1P_35	
K13	IO_L3N_T0_DQS_AD1N_35	
J14	IO_L4P_T0_35	
H14	IO_L4N_T0_35	
K15	IO_L5P_T0_AD9P_35	
J15	IO_L5N_T0_AD9N_35	
J16	IO_L6P_T0_35	
H16	IO_L6N_T0_VREF_35	
G17	IO_L7P_T1_AD2P_35	
G16	IO_L7N_T1_AD2N_35	
G15	IO_L8P_T1_AD10P_35	
G14	IO_L8N_T1_AD10N_35	
G12	IO_L9P_T1_DQS_AD3P_35	
F12	IO_L9N_T1_DQS_AD3N_35	
F13	IO_L10P_T1_AD11P_35	
E12	IO_L10N_T1_AD11N_35	
E13	IO_L11P_T1_SRCC_35	
D13	IO_L11N_T1_SRCC_35	
F15	IO_L12P_T1_MRCC_35	
F14	IO_L12N_T1_MRCC_35	
E16	IO_L13P_T2_MRCC_35	
E15	IO_L13N_T2_MRCC_35	
D15	IO_L14P_T2_AD4P_SRCC_35	
D14	IO_L14N_T2_AD4N_SRCC_35	
F17	IO_L15P_T2_DQS_AD12P_35	
E17	IO_L15N_T2_DQS_AD12N_35	
D16	IO_L16P_T2_35	
C16	IO_L16N_T2_35	
C17	IO_L17P_T2_AD5P_35	
B16	IO_L17N_T2_AD5N_35	
B17	IO_L18P_T2_AD13P_35	
A17	IO_L18N_T2_AD13N_35	
C14	IO_L19P_T3_35	
C13	IO_L19N_T3_VREF_35	
C12	IO_L20P_T3_AD6P_35	
B12	IO_L20N_T3_AD6N_35	
B15	IO_L21P_T3_DQS_AD14P_35	
A15	IO_L21N_T3_DQS_AD14N_35	
C11	IO_L22P_T3_AD7P_35	
B11	IO_L22N_T3_AD7N_35	
B14	IO_L23P_T3_35	
A14	IO_L23N_T3_35	
A13	IO_L24P_T3_AD15P_35	
A12	IO_L24N_T3_AD15N_35	
M16	IO_25_VRP_35	

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