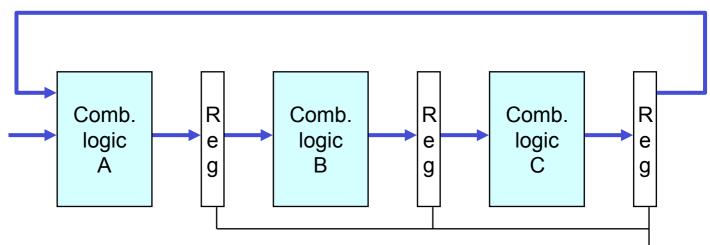


(a) Hardware: Unpipelined with feedback

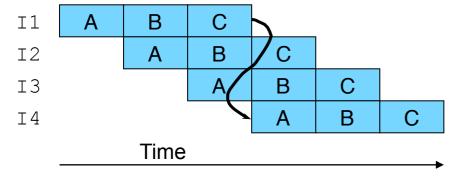


(b) Pipeline diagram



Clock

(c) Hardware: Three-stage pipeline with feedback



(d) Pipeline diagram