

IO

The IO diagram shows the connection of the ATmega328P microcontroller pins to the breadboard. The microcontroller is represented by two 50-pin connectors, JP1 and JP2.

JP1 Connections:

Pin	Signal
1	RESET
3	PA1
5	PB11
7	PA3
9	PA5
11	PA7
13	PC5
15	PB2
17	PF10
19	PF8
21	PD11
23	PD13
25	PF4
27	PE11
29	PE12
31	PF1
33	PE14
35	PE15
37	PD9
39	PD15
41	PD15
43	PF15
45	PF13
47	PF13
49	PB0

JP2 Connections:

Pin	Signal
3	VCC3.3
5	VREF
7	PA2
9	PB10
11	PA4
13	PA6
15	PC4
17	PB1
19	PF11
21	PF9
23	PF7
25	PD12
27	PF5
29	PF3
31	PE10
33	PE13
35	PE9
37	PE7
39	PD8
41	PD10
43	PD0
45	PD14
47	PF14
49	PF12

JP6 Connections:

Pin	Signal
1	SWDIO
2	SWDCLK
3	
4	

BOOT

The BOOT diagram shows the connection of the ATmega328P microcontroller pins to the breadboard. The microcontroller is represented by two 50-pin connectors, JP1 and JP2.

JP1 Connections:

Pin	Signal
1	RESET
3	PA1
5	PB11
7	PA3
9	PA5
11	PA7
13	PC5
15	PB2
17	PF10
19	PF8
21	PD11
23	PD13
25	PF4
27	PE11
29	PE12
31	PF1
33	PE14
35	PE15
37	PD9
39	PD15
41	PD15
43	PF15
45	PF13
47	PF13
49	PB0

JP2 Connections:

Pin	Signal
3	VCC3.3
5	VREF
7	PA2
9	PB10
11	PA4
13	PA6
15	PC4
17	PB1
19	PF11
21	PF9
23	PF7
25	PD12
27	PF5
29	PF3
31	PE10
33	PE13
35	PE9
37	PE7
39	PD8
41	PD10
43	PD0
45	PD14
47	PF14
49	PF12

JP6 Connections:

Pin	Signal
1	SWDIO
2	SWDCLK
3	
4	

BOOT

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Pin	Signal
1	RESET
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27	PE11
29	PE12
31	PF1
33	PE14
35	PE15
37	PD9
39	PD15
41	PD15
43	PF15
45	PF13
47	PF13
49	PB0

JP2 Connections:

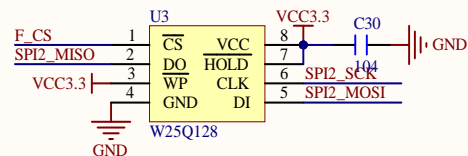
Pin	Signal
3	VCC3.3
5	VREF
7	PA2
9	PB10
11	PA4
13	PA6
15	PC4
17	PB1
19	PF11
21	PF9
23	PF7
25	PD12
27	PF5
29	PF3
31	PE10
33	PE13
35	PE9
37	PE7
39	PD8
41	PD10
43	PD0
45	PD14
47	PF14
49	PF12

JP6 Connections:

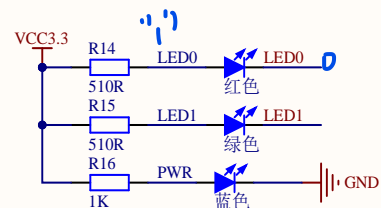
Pin	Signal
1	SWDIO
2	SWDCLK
3	
4	

[illegible][illegible]

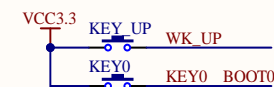
FLASH



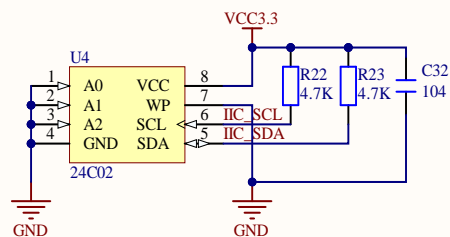
LED



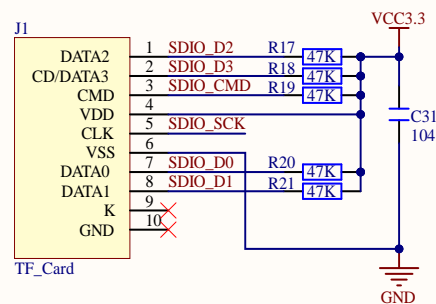
KEY



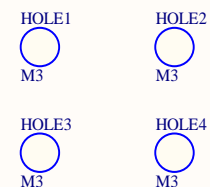
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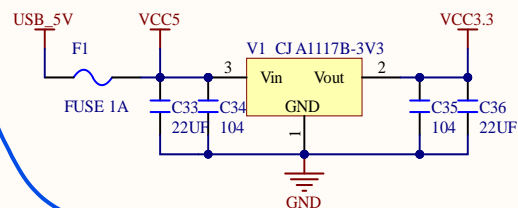
SD CARD



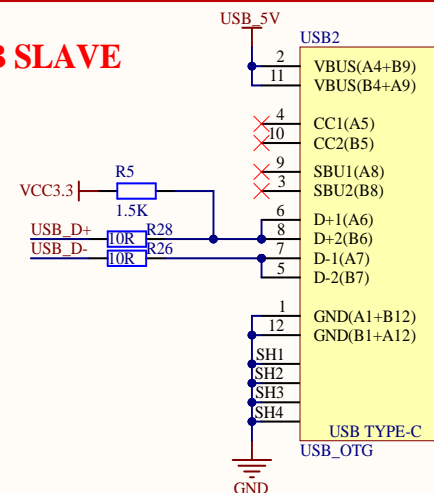
M3过孔



3.3V POWER



USB SLAVE



USB USART

