

3DY4 Project Contributions (Group 04)

Group Member	Mono	Stereo	RDS	Multithreading
Ryan Mancera	Mode 0, Mode 2 and Mode 3, resampler/fast convolution w/ state saving.	Mode 2, 3.	Plotting on Gnuplot.	Debugged some white noise
Japman Nagra	Mode 0, Mode 2. Improved run time for blocks in mono to attempt to remove underrun issues.	Mode 2. Bandpass filter.	Refactored root-raised cosine to C++.	RF thread and audio thread. Debugged multi-threading locking. Created RDS thread template.
Jeremy Cherian	Changed Lab3 python code into c++ for Mode 0, mode 1.	Setup working Mode 0, Mode 1 . Made PLL with state saving. Stereo Combiner.	Channel extraction, Carrier Recovery, Demodulation, partially working CDR algorithm implementation , Gnuplot. Not working fully, but everything up to Root Raised Cosine is correct.	
Shaiyaan Ishtiaq	Mode 1 Mode 2 Converted code for from python to c++ and worked on upsampling/downsampling	Mode 2	Debugged and plotting gnuplot	Debugged