

memory allocation
structs
Template meta programming
classes
object slicing
V table & virtual functions
Inlining Prevention
CRTP -> for optimizing the usage of virtual keyword
TYPE ERASURE
MOVE SEMANTICS
CPU architecture for page table per process and TLB(TRANSLATION LOOK ASIDE BUFFER)
PAGE FAULT
SWAP memory
CPU Arch for writing the data
False sharing
MESI PROTOCOL
MUTEX & SPIN LOCK
ATOMIC