BHIMAVARAPU SRINU
Electronics & Communication Engineer

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CAREER OBJECTIVE

Motivated and detail-oriented engineering graduate seeking a VLSI Frontend / RTL Engineer role to apply strong SystemVerilog, RTL design, and digital logic skills in developing reliable hardware solutions while continuously learning and growing in a dynamic semiconductor environment.

EDUCATION

B.Tech in Electronics & communication Engineering
Andhra University College of Engineering

2024 - Present
CGPA: 7.66

Intermediate - SSC
Sri Prathibha junior college

2022 - 2024
Percentage : 95%

EXPERIENCE & PROJECTS

Frontend Developer Intern – CodSoft, Virtual   **Sep 2024 - Oct 2024**

- Developed responsive web projects including a portfolio website, landing page, and calculator using HTML, CSS, and JavaScript.
- Implemented interactive features and optimized UI for usability and performance.
- Utilized Git/GitHub for version control and followed structured development workflows.

AXI-Lite To APB Bridge

- Designed an AXI-Lite to APB bridge in SystemVerilog to translate AXI-Lite read/write transactions into APB protocol-compliant transfers.
- Verified functionality using simulation, ensuring correct handshaking, address decoding, and response generation.

AXI-Lite Slave (SystemVerilog)

- Developed a simplified AXI-Lite slave interface using SystemVerilog, supporting register read and write transactions.
- Implemented address, data, and response channels following AXI-Lite protocol handshaking and verified using simulation testbenches.

SKILLS

- **Coding & HDL:** Verilog, System Verilog
- **Core Concepts:** Digital Logic Design, RTL Design
- **Protocols:** UART, AXI-Lite (Slave), AXI4, SPI, APB
- **Tools & Platforms:** Xilinx ISE, EDA Playground, FPGA, Xilinx Vivado
- **Version Control:** Git, GitHub

ACHIEVEMENTS & ACTIVITIES

- Winner – SIH Hackathon 2025 (University Level): Developed a real-time tracking solution designed for 2-tier towns and cities.
- Volunteer for CODIAM Ideathon & Startup Event, Spark Nation Hackathon, and an International Hackathon conducted at the university, contributing to coordination and participant management.