



# S32G Linux PFE Driver User's Manual

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# Chapter 1

## Revision History

Revision	Change Description
preEAR 0.4.0	Initial version for preEAR (FPGA/x86 platform) [JPet]
preEAR 0.4.1	Added VDK info [JPet]

Table 1.1: Revision History

## Chapter 2

# Introduction

The Linux PFE driver is, in general, an OS-specific SW component responsible for the complex PFE management. It consists of three main functional blocks:

- The platform driver  
This part covers the initial, low-level PFE HW bring-up, configuration, firmware upload, and the SW representation of the PFE HW components. This part can be described as a low-level PFE driver providing the interface to the hardware (including the firmware).
- The data-path driver  
Data-path driver covers tasks related to the Ethernet data traffic in terms of passing the packets between the PFE and the networking stack. This includes the implementation of the Host Interface (HIF) driver and connection with the host OS-provided the networking stack.
- Control path driver  
The block in charge of the functionality related to runtime PFE engine configuration and monitoring, implementing the Fast Control Interface (FCI) endpoint which is accessible from the user-space through related FCI library.

The driver runs within the target host OS environment and connects the PFE with networking stack. It provides access to physical Ethernet interfaces via exposing logical interfaces to the OS, and the OS together with user's applications can use the Ethernet connectivity via standard OS-provided interfaces (i.e. network sockets).

## Chapter 3

# Building Procedure

### 3.1 Building the driver


The PFE driver consists of number of smaller software modules. The main module producing the final driver is called linux-pfeng and depends on all the others. Successful build gives the final driver library *pfeng.ko* which is an Ethernet driver for particular Linux kernel. Its location within the project tree is in *.../linux-pfeng/*. There are two ways to build the driver: standalone and embedded within the Linux kernel sources.

#### 3.1.1 Building standalone

1. As prerequisite check all necessary development requirements:
  - (a) Host development GNU toolchain, including GNU-cc, GNU-make
  - (b) Linux kernel development files, usually installed by host package manager as linux-kernel-devel or linux-dev
2. Go to *.../linux-pfeng/*.
3. Make sure that the following environment variables are set and points to the right directories:
  - (a) KERNELDIR  
The directory where Linux kernel header files are located. Usually it is somewhere under */usr/src/...*
  - (b) optionally also PLATFORM  
The name of the GNU toolchain platform. For example it is "x86\_64-linux-gnu" for Ubuntu or "x86\_64-redhat-linux" for Redhat/Fedora. In Linux driver Makefile, in the directory *sw/linux-pfeng* there are already included some default values for different hardware targets. One for FPGA/x86 and second for VDK/arm64.
4. Run 'make drv-clean' to clean working directories
5. Build the driver:
  - (a) On FPGA/x86 run:
 

```
make all
```
  - (b) on VDK/arm64: run:
 

```
make GLOBAL_CFG_RUN_ON_VDK=1 GLOBAL_CFG_HIF_SEQNUM_CHECK=1 all
```

 *Note that additional compile options are necessary because of not fully simulated PFE hardware inside VDK TLM*

#### 3.1.2 Building within Linux kernel source tree

1. (TBD)

# Chapter 4

## Usage

### 4.1 Prerequisites

#### 4.1.1 Pre-silicon platform FPGA/x86

For the usage of PFE in FPGA on PCIe card, following shall be fulfilled:

- An x86 platform with PCIe support and one PCI-e x8 slot
- VIRTEX UltraScale FPGA connected with the x86 via PCIe.
- FPGA up and running with the bitfile loaded.
- The Linux kernel 4.14 or newer running on the target x86 platform.
- The pfeng.ko available on the target (see [Building the driver](#)).
- The firmware binary called *pfe-s32g-class.fw* available on the target and served by the kernel-firmware subsystem

 Note that PFE firmware is being delivered as a standalone product package.

#### 4.1.2 Pre-silicon platform VDK S32G TLM

For usage the software virtualizer of S32G, the following shall be fulfilled:

- Synopsys VDK for NXP S32G274 VP installed on supported host system. For more info read “VDK for NXP S32G274A VP Installation Guide”
- Auto Linux BSP version 22 images for target s32g274:
  1. Linux kernel
  2. Linux device tree database
  3. Root filesystem
  4. Bootloader
- Classifier config classifier\_firmware\_mmap.txt which is a part of driver release package
- VDK's vpconfig containing locations to the above files.

	Filename	Source	VDK vpconfig mapping
Linux kernel	Image--4.19-r0-s32g274sim.bin	ALB	ECU.MCU.Base.Peripherals.Memory_IF.DDR_0. #EXTRA_PROPERTIES#/ImageInfo/initial_image
Linux device tree database	Image--4.19-r0-s32g274-simulator.dtb	ALB	--
Root filesystem	fsl-image-sim-s32g274sim.rootfs.cpio.gz	ALB	--
Bootloader	u-boot-cpio.bin	ALB	ECU.MCU.Base.Peripherals.Memory_IF.SRAM. #EXTRA_PROPERTIES#/ImageInfo/initial_image
PFE classifier mmap	classifier_firmware_mmap.txt	PFE	ECU.MCU.PFE.PFE_0. #SCML_PROPERTIES#CLASSIFIER_FIRMWARE_MMMap_FILE

Table 4.1: VDK supporting files

- U-boot bootargs requires additional parameter: "coherent\_pool=64M"
- VDK VHub in case of possibility to connect the driver to the "real networks". What means possibility to use host's ethernet driver to act as proxy for pfeng.ko running in the guest Linux. For more info how to configurer VHub check "Synopsys VDK VHub User Guide".

## 4.2 Running the driver


1. Once all prerequisites are met, one can start the driver by executing:

```
insmod pfeng.ko
```

Command

```
ifconfig -a
```

can then be used to obtain a list of available interfaces. The three new interfaces should be available.

 The exact names of the interfaces vary. Usually, it depends on the OS underlayer subsystems like udev/systemd

1. Configure the IP addresses and bring the interfaces up by executing:


```
ifconfig pfe<0-2> <interface_ip_address>/<mask>
```

2. The network interfaces usage

- (a) FPGA/x86: Connect the Ethernet cable or cables to the connector(s) on the HW platform board. The assignment of interfaces to the connectors on the FPGA/x86 platform board is as follows:

Interface	"ETHERNETFMC" Connector
pfe0	PORT0
pfe1	PORT2
pfe2	N/A

Table 4.2: FPGA/x86 Interface Assignment

 In case of FPGA/x86 platform it shall be ensured that the physical connection will **only** be established using 100Mbps/full duplex.

- (b) VDK/arm64: For use of host's network interfaces refer to the VDK VHub documentation
3. The "ping" utility can be used to test the connection:

```
ping <remote_ip_address>
```