	1	1 0010														
							01	01	1							
	1	1 0000					01	01	1							
	1	1 0011					01	01	1							
	1	1 0001							1							
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	1	1 1101					01	01	1							
	1	1 1100					01	01	1							
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	1	1 0010					01	01	1 1	1		01				
	1	1 0000					01	01	1 1	1		01				
	1	1 0100					01	01	1 1	1		01				
	1	1 0101					01	01	1 1	1		01				
	1	1 0110					01	01	1 1	1		01				
	1	1 100x					01	01	1 1	1		01				
	1	1 0010	1	1			01	00	1 1	1		01				
	1	0010	1		-	1	01	01	1 1	1 1						
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		1 10000							1							
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	1	1 0010	1	1			01	00	1 1	1		01		100		
	1	1 0000	1	1			01	00	1 1	1		01	(	011		
	1	1 0010	1		-	1	01	00	1 1	1						
	1	1 0010	1		-	1	01	00	1 1	1						
	1	1					01	01					1			
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	1	1					01	00					1			
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							01	00		1				01000	)	1
		1   1 <td>1         1         0100           1         1         0101           1         1         0110           1         1         0111           1         1         1011           1         1         1010           1         1         1101           1         1         1100           1         1         1100           1         1         1100           1         1         0010           1         1         0010           1         1         0010           1         1         0010           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1000</td> <td>1         1         0100           1         1         0101           1         1         0110           1         1         0111           1         1         1011           1         1         1010           1         1         1100           1         1         1100           1         1         1100           1         1         1100           1         1         1100           1         1         1000           1         1         0010           1         1         0010           1         1         0010           1         1         0010           1         1         0010           1         1         0010           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001</td> <td>  1</td> <td>  1</td> <td>                                     </td> <td>  1</td> <td>                                     </td> <td>  1</td> <td>                                     </td> <td>  1</td> <td>  1</td> <td>  1</td> <td>  1</td> <td>                                     </td>	1         1         0100           1         1         0101           1         1         0110           1         1         0111           1         1         1011           1         1         1010           1         1         1101           1         1         1100           1         1         1100           1         1         1100           1         1         0010           1         1         0010           1         1         0010           1         1         0010           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1000	1         1         0100           1         1         0101           1         1         0110           1         1         0111           1         1         1011           1         1         1010           1         1         1100           1         1         1100           1         1         1100           1         1         1100           1         1         1100           1         1         1000           1         1         0010           1         1         0010           1         1         0010           1         1         0010           1         1         0010           1         1         0010           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001           1         1         1001	1	1		1		1		1	1	1	1	

						- h:(((-h)	aludana (alua da)					la a dista d								
ADD	zero	negtive	wreg	regrt	m2reg	shift(aluc_a)	aluimm(aluc_b)	sext	aluc	storekind	wmem	loadkind	pcsource	wlohi	rlohi	mudiv	exception	r_cp0	w_cp0	
ADD	X		1	0	00	0	0	X	0010		0		00	X	X	X	X			
ADDU	X		1	0	00	0	0	X	0000		0		00	X	X	X	X			
SUB	X		1	0	00	0	0	X	0011		0		00	X	X	X	X			
SUBU	X		1	0	00	0	0	X	0001		0		00	X	X	X	X			
AND	X		1	0	00	0	0	X	0100		0		00	X	X	X	X			
OR	X		1	0	00	0	0	X	0101		0		00	X	X	X	X			
XOR	X		1	0	00	0	0	X	0110		0		00	X	X	X	X			
NOR	X		1	0	00	0	0	X	0111		0		00	X	X	X	X			
SLT	X		1	0	00	0	0	X	1011		0		00	X	X	X	X			
SLTU	X		1	0	00	1	0	X	1010		0		00	X	X	X	X			
SLL	X		1	0	00	1	0	X	111x		0		00	X	X	X	X			
SRL	X		1	0	00	1	0	x	1101		0		00	X	X	X	X			
SLLV	X X		1	0	00	0	0	X V	111x		0		00	×	X	×	X			
SRLV	×		1	0	00	0	0	× ×	1101		0		00	× ×	× ×	^	X			
SRAV	x		1	0	00	0	0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	1100		0		00	^ v	^	^				
JR	x		0	v	y	v	v	Y	X		v		10	Y	Y	^ 	X			
ADDI	×		1	1	00	0	1	1(ext16)	0010		0		00	×	Y Y	У	X			
ADDIU	×		1	1	00	0	1	0(ext16)	0000		0		00	Y	x	x	X			
ANDI	x		1	1	00	0	1	0(ext16)	0100		0		00	x	x	x	X			
ORI	x		1	1	00	0	1	0(ext16)	0101		0		00	x	x	x	X			
XORI	x		1	1	00	0	1	0(ext16)	0110		0		00	X	x	x	X			
LUI	×		1	1	00	x	1	x	100x		0		00	X	x	x	x			
LW	x		1	1	01	0	1	1(ext16)	0010		0	00	00	x	x	x	x			
sw	x		0	0	x	0	1	1(ext16)	0010	00	1		00	x	x	x	x			
BEQ(=)	1		0	0	x	0	0	1(ext16)(<<2)	0011		0		01	х	x	x	x			
BEQ(!=)	0		0	0	х	0	0		0011		0		00	х	х	x	х			
BNE(=)	1		0	0	x	0	0		0011		0		00	х	х	x	х			
BNE(!=)	0		0	0	x	0	0		0011		0		01	х	х	х	х			
SLTI	х		1	1	00	0	1	1(ext16)	1101		0		00	x	х	х	х			
SLTIU	x		1	1	00	0	1	0(ext16)	1101		0		00	x	x	x	x			
J	х		x	x	x	x	x	x	x		0		11	x	x	x	x			
JAL	х		1	\$31	10	x	x	x	x		0		11	x	x	x	x			
CLZ	х		1	0	00	0	x	x	10000		0		00	x	x	x	x			
JALR	х		1	0	10	x	х	x	x		0		10	х	x	x	x			
LB	Х		1	1	01	0	1	1(ext16)	0010		0	10	00	х	X	X	X			
LBU	Х		1	1	01	0	1	0(ext16)	0010		0	10	00	х	X	X	X			
LH	х		1	1	01	0	1	1(ext16)	0010		0	01	00	х	X	X	X			
LHU	X		1	1	01	0	1	0(ext16)	0010		0	01	00	X	X	X	X			
SB	X		0	X	X	0	1	X	0010	10	1		00	X	X	X	X			
SH	X		0	X	X	0	1	X	0010	01	1		00	X	X	X	X			
MFLO	X		1	U	11	X	X	X	X		0		00	X 4/20\	01	X	X			
MTLO MFHI	X 		U	X	X	X	X	X	X		0		00	1(rs)	10	X	X			
MTHI	x x		0	X	X	X	X	X	X		0		00	1(rs)	IU V	X	X			
DIV	X X		0	×	X	0	0	^ y	X		0		00	1 (15)	X	11	X			
DIVU	X		0	Y	X	0	0	Y	x		0		00	1	X	10	x			
MUL	X		1	0	00	0	0	x	10100		0		00	X	X	X	X			
MULTU	x		0	x	x	0	0	x	X		0		00	1	0	00	x			
BEGZ(>=)		0	X	x	X	x	x	x	0011		0		01	x	x	X	X			
BGEZ(<)		1	X	x	X	x	X	x	0011		0		00	X	x	x	X			
MFC0	x	×	1	1	4	x	x	x	X		0		00	X	x	x	x	1	0	
MTC0	x		0										00					0	1	
BREAK	x												00				1			
TEQ(rs!=rt)	x					0	0		0011				00				0			
TEQ(rs==rt)	1					0	0		0011				00				1			
ERET	х												00				1			
SYSCALL	х												00				1			
	<u> </u>												1							