

A

A

B

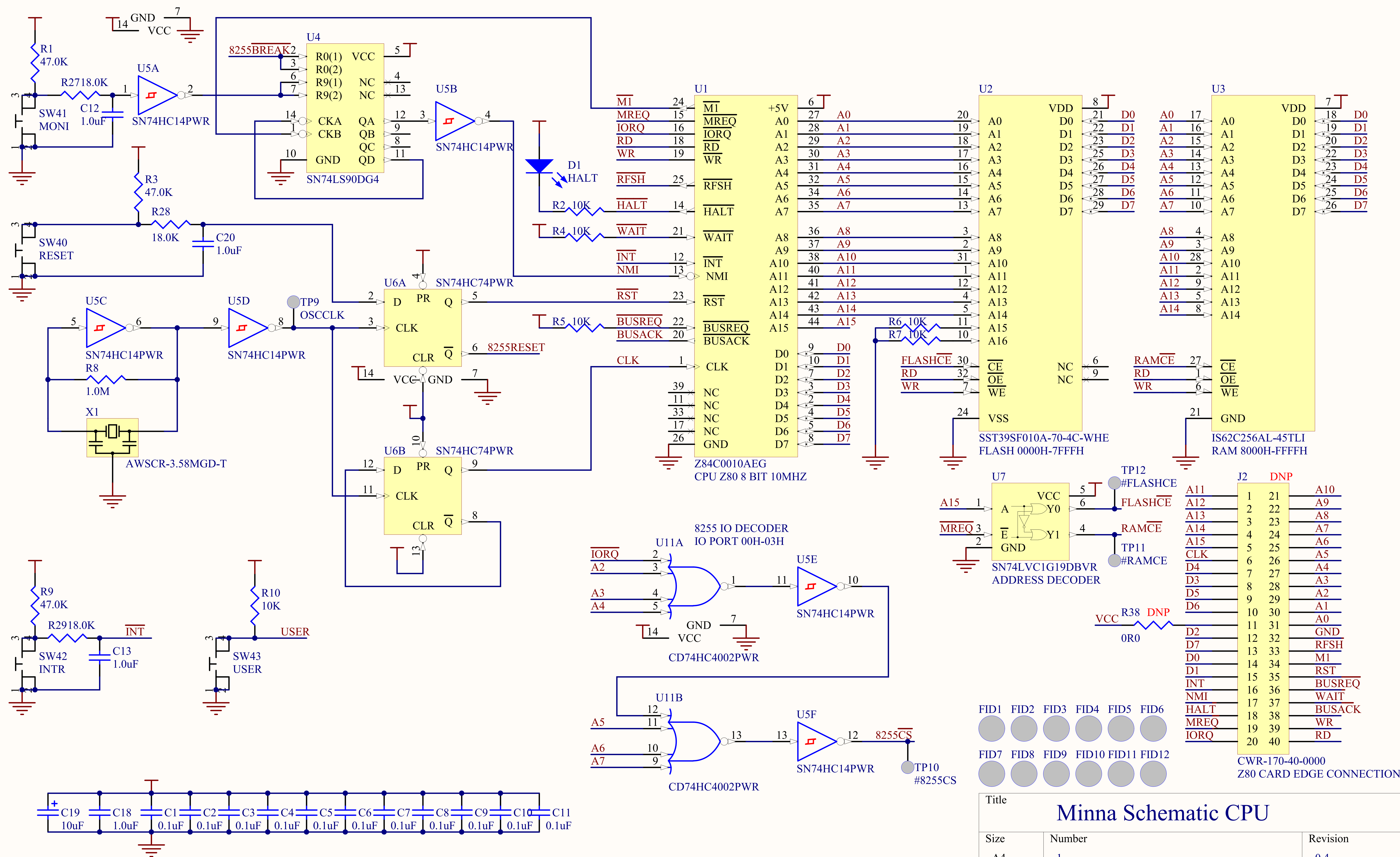
B

C

C

D

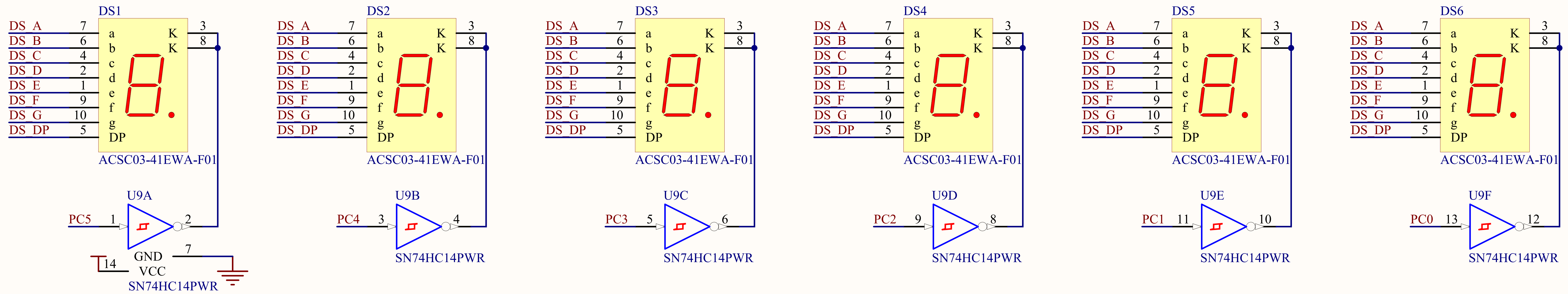
D



Title		
Minna Schematic CPU		
Size	Number	Revision
A4	1	0.4
Date:	2012-08-21	Sheet 1 of 2
File:	C:\DEV\...\minna_schematic_cpu.SchDoc	Drawn By: Chris Hettrick

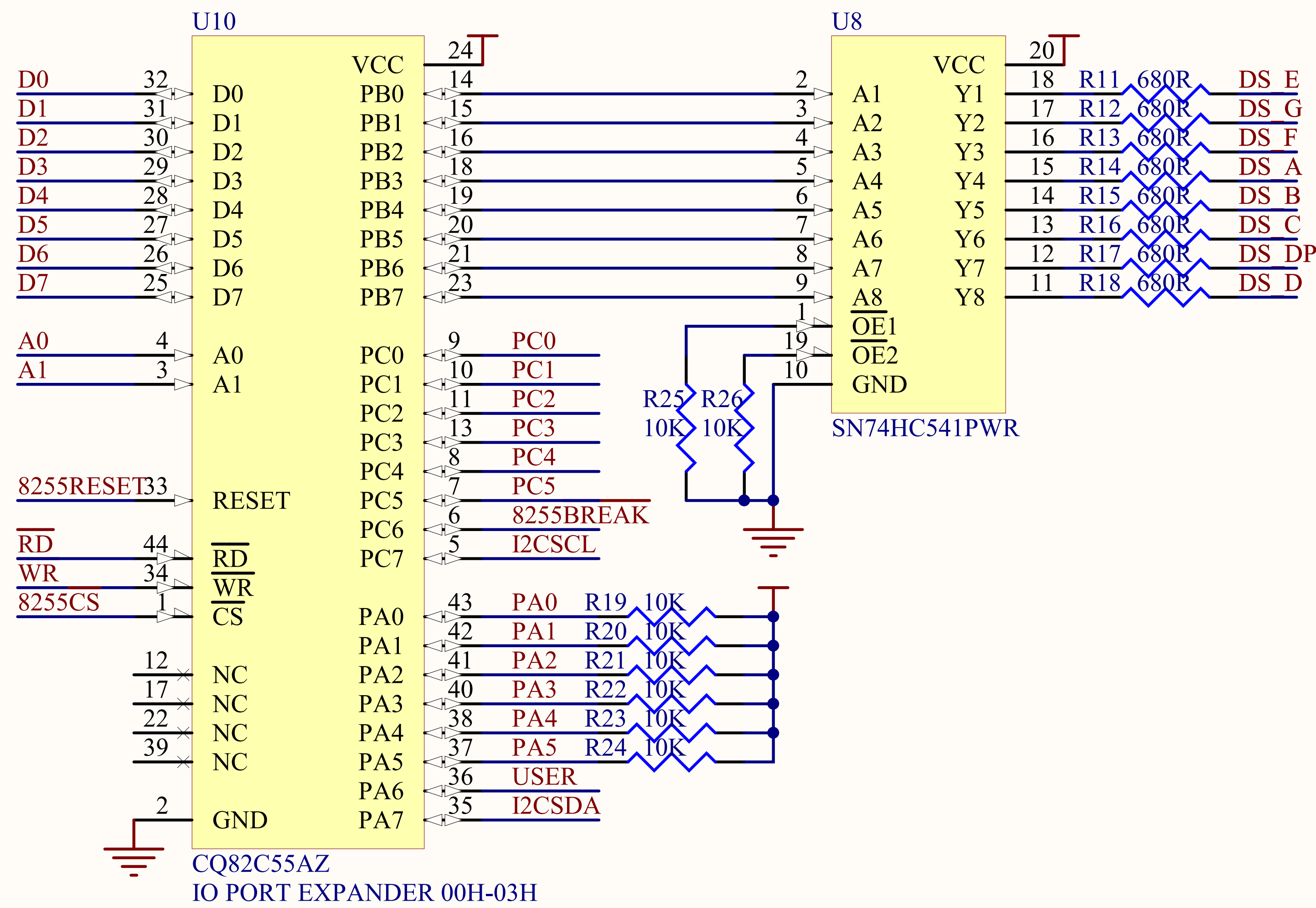


A

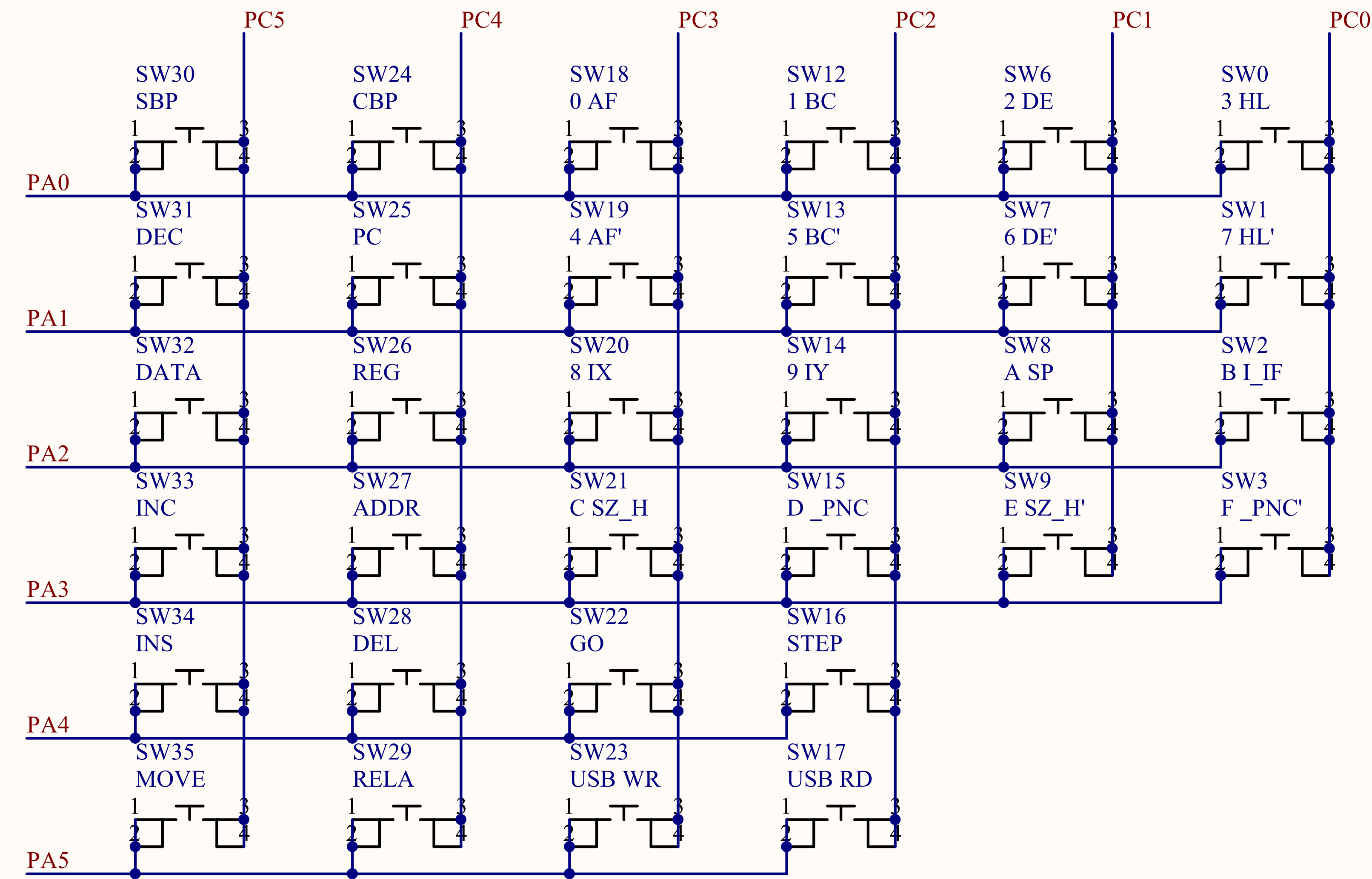


A

B

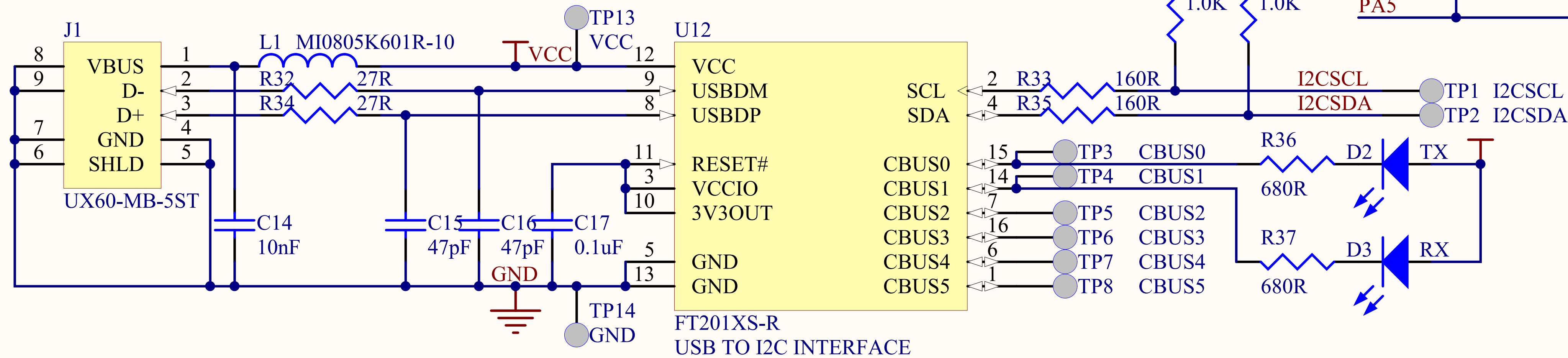


B



C

D



D

Title		
Minna Schematic Input Output		
Size	Number	Revision
A4	2	0.4
Date:	2012-08-21	Sheet 2 of 2
File:	C:\DEV\..minna_schematic_io.SchDoc	Drawn By: Chris Hettrick