

31 TRIBHUVAN UNIVERSITY  
INSTITUTE OF ENGINEERING  
**Examination Control Division**  
2072 Ashwin

Exam.	Regular		
Level	BE	Full Marks	80
Programme	BEL, BEX, BCT	Pass Marks	32
Year / Part	II / II	Time	3 hrs.

**Subject: - Microprocessor (EX551)**

- ✓ Candidates are required to give their answers in their own words as far as practicable.
- ✓ Attempt All questions.
- ✓ The figures in the margin indicate Full Marks.
- ✓ Assume suitable data if necessary.

1. What is Bus? Explain Bus organization of microprocessor with diagram? Calculate the memory handling capacity of the processor having address bus of 24 lines and data bus of 16 lines. [1+5+2]
2. Explain the instruction format and data format of 8085 microprocessor. Explain different addressing modes of 8085 microprocessor. [3+5]
3. Write a program in 8085 to calculate the number of ones in the upper nibble of ten 8-bit numbers stored in a table. Store the count of ones in a location just after the table. [8]
4. What is statement in an assembly language program? Explain frequently used directives with suitable example. [2+6]
5. Write an assembly program to read a string from the user and display vowels and consonants separately. [8]
6. What do you understand by address decoding? Explain with example how PROM decoder is used in memory address decoding. [2+6]
7. Explain different modes of parallel data transfer. Explain briefly, what is PPI. [4+4]
8. What is interrupt? What is its importance in microprocessor? How interrupts from different peripherals can be handled with single INTR pin in 8086 microprocessor? Explain. [1+2+5]
9. What is register based and accumulator based architecture? Differentiate between CISC and RISC architecture? [2+6]
10. Write short notes on: [2×4]
  - a) DMA
  - b) INX, XTHL, MUL, JG

\*\*\*

31 TRIBHUVAN UNIVERSITY  
INSTITUTE OF ENGINEERING  
**Examination Control Division**  
2072 Magh

Exam.	New Back (2066 & Later Batch)		
Level	BE	Full Marks	80
Programme	BEL, BEX, BCT	Pass Marks	32
Year / Part	II / II	Time	3 hrs.

**Subject: - Microprocessor (EX551)**

- ✓ Candidates are required to give their answers in their own words as far as practicable.
- ✓ Attempt All questions.
- ✓ The figures in the margin indicate Full Marks.
- ✓ Assume suitable data if necessary.

1. Draw and explain the block diagram of a computer. Explain stored program concept. [4+4]
2. What is flag? Discuss about 8085 associated flags. Along with suitable examples show how these flags are affected by arithmetic and logical group of instructions. [1+2+5]
3. Write a program for 8085 to generate multiplication table of a number stored at 8230H and store the generated table starting at 8231H. For example, if location 8230H has number 05H then store 05H at 8231 H, 0AH at 8232H and so on. [8]
4. Draw the internal architecture of 8086 microprocessor. Explain the function of each register accessible to programmer. [8]
5. Write a program in 8086 to read a string and display each word in a separate line in the center of the screen. [8]
6. Draw the timing diagram of STA instruction? Calculate the time taken to execute the following program if  $T = 1$  micro second. [5+3]  
 MVI A,05H  
 ADI 20H  
 OUT 80H  
 HLT
7. Design an interfacing circuit to interface one 4 KB EPROM and two 2 KB R/W memory for 8085 microprocessor. [8]
8. What is IVT? How is it used to handle software and hardware interrupts? Explain. [2+6]
9. What is deadlock? What are the conditions for deadlock to occur? Write down the features of DSP chip. [1+3+4]
10. Write short notes on: [4×2]
  - a) RS 232
  - b) Two pass assembler

\*\*\*

31 TRIBHUVAN UNIVERSITY  
INSTITUTE OF ENGINEERING  
**Examination Control Division**  
2071 Bhadra

Exam.	Regular / Back		
Level	BE	Full Marks	80
Programme	BEL, BEX, BCT	Pass Marks	32
Year / Part	II / II	Time	3 hrs.

**Subject: - Microprocessor (EX551)**

- ✓ Candidates are required to give their answers in their own words as far as practicable.
- ✓ Attempt All questions.
- ✓ The figures in the margin indicate Full Marks.
- ✓ Assume suitable data if necessary.

1. Explain the microprocessor based system showing the bus organization. Explain the processing cycle of a stored program computer. [4+4]
2. Explain briefly the programmer's model of 8085 microprocessor. [8]
3. Write a program for 8085 to add the upper and lower nibble of ten 8 bit words stored in a table that starts from location 8B20H. Store the separate results in locations just after the table. [8]
4. What do you mean by addressing mode? Explain the addressing modes of 8086 with example. [3+5]
5. Write an assembly language program to read a text from keyboard, convert the text into uppercase and display on the cleared screen. [8]
6. Draw and explain the bus timing for OUT 42H instruction of 8085 microprocessor. [8]
7. Draw the address decoding circuit to interface two RAM memory block each of 8 KB at address C000H. [8]
8. Explain how hardware interrupt is processed in 8086 microprocessor. [8]
9. What do you mean by parallelism? Write and explain the features of a typical operating system. [8]
10. Write short notes on: [4×2]
  - a) RS 232 Standard
  - b) Programmable Peripheral Interface

\*\*\*

10/17 M.

36 TRIBHUVAN UNIVERSITY  
INSTITUTE OF ENGINEERING  
**Examination Control Division**  
2071 Magh

Exam.	New Back (2066 & Later Batch)		
Level	BE	Full Marks	80
Programme	BEL, BEX, BCT	Pass Marks	32
Year / Part	II / II	Time	3 hrs.

**Subject: - Microprocessors (EX351)**

- ✓ Candidates are required to give their answers in their own words as far as practicable.
- ✓ Attempt All questions.
- ✓ The figures in the margin indicate Full Marks.
- ✓ Assume suitable data if necessary.

1. Define instruction cycle and machine cycle. Explain briefly the instruction processing cycle of Von Neumann machine. [4+4]
2. Draw the internal architecture of 8085 microprocessor and explain each part. [8]
3. Write an assembly language program for 8085. Table1 contains 16 no. of 8 bit data, transfer data which have number of 1s greater than 3, from table1 to table2, otherwise store FFH in table2. [8]
4. What is statement in assembly language programming? Explain commonly used directives with examples. [8]
5. Write a program to read a string and display only the alphabetic characters from the string in a clear screen. [8]
6. Draw timing diagram of instruction LDA 2080H. Calculate the time required to execute this instruction if the crystal frequency is 6 MHz. [6+2]
7. Determine the capacity of devices in the following memory range. [2+6]

Range	Device
0000-1FFF	ROM
2000-3FFF	RAM

Design an interfacing circuit to interface above memory devices with 8085 microprocessor.

8. Define Interrupt Service Routine (ISR) and Interrupt Vector Table (IVT). Explain type 0 to type 4 interrupt of 8086. [2+6]
9. What are the features of digital signal processors? Describe instruction level, thread level and process level parallelism. [2+6]
10. Write short notes on: [4×2]
  - a) I/O mapped and memory mapped I/O
  - b) DMA

\*\*\*

21 TRIBHUVAN UNIVERSITY  
INSTITUTE OF ENGINEERING  
**Examination Control Division**  
2070 Bhadra

Exam.	Regular		
Level	BE	Full Marks	80
Programme	BEL, BEX, BCT	Pass Marks	32
Year / Part	II / II	Time	3 hrs.

**Subject: - Microprocessor (EX551)**

- ✓ Candidates are required to give their answers in their own words as far as practicable.
- ✓ Attempt All questions.
- ✓ The figures in the margin indicate Full Marks.
- ✓ Assume suitable data if necessary.

1. Define bus? Explain different types of bus. Define RTL. Write down the RTL for MOV  $r_1, r_2$  in 8085 microprocessor. [4+4]
2. In how many ways 8085 instructions can be classified? Explain with examples. What is the purpose of the branching instructions? [6+2]
3. Write an assembly language program for 8085 to exchange the bits D6 and D2 of every byte of a program. Suppose there are 200 bytes in the program starting from memory location 8090H. [8]
4. Draw internal block diagram of 8086. Explain Bus Interface Unit. [5+3]
5. Write an assembly language program for 8086 to read a string. Display each word in separate lines in a cleared screen, count how many words are there and display the count. [8]
6. Write the various machine cycle involved in LDA C030 stored at C050. Write the use of following pins of 8085 microprocessor. ALE, IO/M, READY, RD,  $AD_0-AD_7$ . [3+5]
7. Explain the execution of instruction LDA 8B7FH with the help of timing diagram. [8]
8. How interrupt vector table is used in microprocessors to manage the interrupt? Explain how software and hardware interrupts are used in 8086 microprocessor in detail. [3+5]
9. Write the conditions that may cause deadlock to arise. Explain Flynn's classification. [4+4]
10. Write short notes on: [4x2]
  - a) Hardwired and micro program control unit
  - b) EXE and COM programs

\*\*\*

21      TRIBHUVAN UNIVERSITY  
 INSTITUTE OF ENGINEERING  
**Examination Control Division**  
 2070 Magh

Exam.	New Batch (2066 & Later Batch)		
Level	BE	Full Marks	80
Programme	BEL, BEX, BCT	Pass Marks	32
Year / Part	I / II	Time	3 hrs.

**Subject: - Microprocessor (EX351)**

- ✓ Candidates are required to give their answers in their own words as far as practicable.
- ✓ Attempt All questions.
- ✓ The figures in the margin indicate Full Marks.
- ✓ Assume suitable data if necessary.

1. Differentiate between Hardwired and Microprogrammed Control Unit used in microprocessors? Write the fetch and execution cycle for LXI D,9050H instruction in RTL specifications. Explain each step. [4+4]
2. Write any three features of 8085 microprocessor. Explain the addressing modes of 8085 with example. [3+5]
3. Write an assembly language program in 8085 to divide a byte stored in memory location 9070 H by byte stored in 9071 H and store the remainder and quotient at 9072 H and 9073 H respectively. [8]
4. Draw the functional block diagram of 8086 microprocessor and explain each block. [8]
5. Write a program for 8086 to find the largest and smallest value from a list of ten 16 bit data and store the result in maxval and minval variables. [8]
6. What do you mean by serial interface? Differentiate between synchronous and asynchronous serial interlacing. Describe how we can use RS-232 standard to transfer data from DTE to DCE and vice versa? [1+3+4]
7. Design the address decoding interface of an input port and output port for 8085 at 81 H and 82 H address. Use block decoder. [8]
8. Differentiate Maskable and Non-Maskable interrupt. Write the general sequence to be followed when interrupt occurs. [2+6]
9. Write down the difference of RISC and CISC computers. [8]
10. Write short notes on; [2×4]
  - a) Serial and parallel interface
  - b) Programmable peripheral interface

21 TRIBHUVAN UNIVERSITY  
INSTITUTE OF ENGINEERING  
**Examination Control Division**  
2069 Bhadra

Exam.	Regular (2066 & Later Batch)		
Level	BE	Full Marks	80
Programme	BEL, BEX, BCT	Pass Marks	32
Year / Part	II / II	Time	3 hrs.

**Subject: - Microprocessor (EX551)**

- ✓ Candidates are required to give their answers in their own words as far as practicable.
  - ✓ Attempt All questions.
  - ✓ The figures in the margin indicate Full Marks.
  - ✓ Assume suitable data if necessary.
1. Define microprocessor based system. Explain the various components of a microprocessor based system including the bus structure. [2+6]
  2. Compare CALL and RET instructions with PUSH and POP instructions for 8085. Illustrate their usages with appropriate examples. [4+4]
  3. Write a program to convert ten BCD numbers stored at 4350H to binary and store the result at 4360H. [8]
  4. Draw the internal architecture of the 8086 microprocessor. Differentiate between EXE and COM programs. [4+4]
  5. Write a program in 8086 to convert the vowels to uppercase from a string entered by the user and display the converted string in a new line. Also count the number of uppercase letters in the converted string and display the count in a new line. [8]
  6. Define synchronous and asynchronous bus. Draw and explain the timing diagram of LXIB 1234H. [3+5]
  7. Interface two 4K\*8 ROM with 3:8 decoder at the address of your choice. Show the address map for each ROM and explain your circuit. [8]
  8. Explain how the interrupt vector table is used in 8086 microprocessor. Show the complete instruction execution sequence when interrupt service routine is called. [3+5]
  9. What do you mean by parallelism? Explain the case of deadlock that may arise during inter process communication. What role does the operating system play in handling such deadlocks? [2+3+3]
  10. Write short notes on: (any two) [2×4]
    - a) Flags in 8086 microprocessor
    - b) Programmable interrupt controller (PIC)
    - c) Instruction and machine cycles

\*\*\*

21 TRIMULVYAN UNIVERSITY  
INSTITUTE OF ENGINEERING  
**Examination Control Division**  
2068 Bhadra

Exam.	Regular / Back		
Level	BE	Full Marks	80
Programme	BEL, BEX, BCT	Pass Marks	32
Year / Part	II / II	Time	3 hrs.

**Subject: - Microprocessors**

- ✓ Candidates are required to give their answers in their own words as far as practicable.
- ✓ Attempt All questions.
- ✓ The figures in the margin indicate Full Marks.
- ✓ Assume suitable data if necessary.

- ✓ 1. What do you mean by stored program concept? Explain the processing cycle of stored program computer. [2+6]
- ✓ 2. Draw the block diagram of 8085 microprocessor and explain each part briefly. [8]
- ✓ 3. Write a program in 8085 to transfer 8-bit number from one table to other by setting bit D<sub>5</sub> if the number is less than 80H else transfer the number by resetting bit D<sub>6</sub>. [8]
- ✓ 4. What do you mean by directives and instructions? Explain the frequently used directives in assembly language programming. [2+6]
- ✓ 5. Write a program in 8086 to read a string and count the number of vowels, consonants, numerals and other characters and display the count. [8]
- ✓ 6. What do you mean by synchronous and asynchronous bus? Draw the bus timing diagram for LXI B, 7492H which is stored in memory location 8200H. [2+6]
- ✓ 7. What do you mean by unique and non-unique address decoding? Explain memory mapped I/O along with an example. [2+6]
- ✓ 8. How is interrupt processing different than polling? Explain how interrupt processing is done with 8085. [2+6]
- ✓ 9. What do you mean by real and pseudo-parallelism? Explain Flynn's classification. [3+5]
10. Write short notes on [4×2=8]
  - a) Stack memory
  - b) USART

\*\*\*



23 TRIBHUVAN UNIVERSITY  
INSTITUTE OF ENGINEERING  
Examination Control Division  
2067 Mangsir

Exam. Level	Regular / Back	Full Marks	80
Programme	BEL, BEX, BCT	Pass Marks	32
Year / Part	II / II	Time	3 hrs.

*Subject: - Microprocessors*

- ✓ Candidates are required to give their answers in their own words as far as practicable.
- ✓ Attempt All questions.
- ✓ The figures in the margin indicate Full Marks.
- ✓ Assume suitable data if necessary.

1. What is a system bus? Explain different types of system buses. Compare 8035 and 8086 microprocessors system bus. [2-6+2]
2. What do you mean by an instruction? Explain different types of instructions in 8085 on the basis of function and instruction size with suitable examples. [2+8]
3. What is a flag? Describe status and control flags of 8085 in brief. Explain how 20 bit physical address is calculated from 16 bit logical address. Give suitable examples. [1+5+4]
4. A set of three readings is stored in memory starting at 9000H. Write an assembly language program to sort the readings in ascending order. Store the smallest value in address 9054H and so on in higher addresses. [10]
5. Write an assembly language program to read a string from memory in data segment. Change all the upper case letters to lower case and vice versa. Display the result on the screen. [Note: ASCII code for A = 65..... Z = 90, a = 97..... z = 122] [1-9]
6. In 8085, memory and I/O read/write instructions use extra machine cycle for memory and I/O read/write operation. Use the bus time diagram for MOV R,M and out instructions to illustrate the statement. [5-5]
7. What is unique and non unique address decoding, explain with suitable examples. Design an unique address decoding circuit using memory mapped I/O interface to read input from port address F5F9H and output to port address F5F8H. [4+6]
8. Write short notes on [2x5]
  - a) RIM and SIM instructions
  - b) RS 232

\*\*\*

23  
 TRIUNIVAN UNIVERSITY  
 INSTITUTE OF ENGINEERING  
 Examination Control Division  
 2067 Shrawan

Examination	Back
Level	BEL
Programme	BEL, BEX, BCT
Year / Part	II / II
Full Marks	80
Pass Marks	32
Time	3 hrs.

Subject: - Microprocessors

- ✓ Candidates are required to give their answers in their own words as far as practicable.
- ✓ Attempt All questions.
- ✓ The figures in the margin indicate Full Marks
- ✓ Assume suitable data if necessary.

1. Explain how instructions and opcodes are designed in microprocessor? What do you mean by register transfer language? What could be the register transfer statements for ADD B. [3+2+5]
2. Explain the following instructions in 8085 in detail with example  
 SBB B, DAD B, CPO 8285, XTHL, RST 5 [10]
3. Write a program in 8085 to add all the numbers from a table of 8-bit numbers whose higher nibble value is greater than 6 and store the 16 bit result just after the table. [10]
4. What do you mean by addressing mode? Explain different addressing modes of 8086 with example. [2+8]
5. Write a program in 8085 to read a single digit number and display the multiplication table of that number as  
 1 2 6 8 10 12 14 16 18 20 if user enters digit 2. [10]
6. What do you understand by synchronous bus and asynchronous bus? Draw and explain the bus timing diagram when the instruction ADI 04H is executed. [3+7]
7. Write down different ways of synchronizing peripherals with computer. Explain the interrupt processing mechanism vectored and polled interrupts. [4+6]
8. Write short notes on [5x2=10]
  - a) Structured program concept
  - b) Dynamic Memory Allocation

23 TRIBHUVAN UNIVERSITY  
INSTITUTE OF ENGINEERING  
**Examination Control Division**  
2066 Magh

Exam.	Regular/Back		
Level	BE	Full Marks	80
Programme	BEL, BEX, BCI	Pass Marks	32
Year / Part	II / II	Time	3 hrs.

**Subject: - Microprocessor**

- ✓ Candidates are required to give their answers in their own words as far as practicable.
- ✓ Attempt All questions.
- ✓ The figures in the margin indicate Full Marks.
- ✓ Assume suitable data if necessary.

1. Explain the features of automated calculator. Differentiate von-Neumann and Harvard architecture. [3+7]
2. What do you mean by instructions? How are instructions designed? Explain different addressing modes of 8085 along with the example. [2+2+6]
3. Write a program for 8085 to add corresponding data from two table if the data from the first table is smaller than the second table else subtract data of second table from the first table. Store the result of each operation in the corresponding location of the third table. Assume each table has ten eight bit data. [10]
4. Draw the internal block diagram of the 8086 microprocessor. Explain the function of each component. [10]
5. Write an assembly language program to calculate sum of the series  $1^2 + 2^2 + 3^2 + 4^2 + \dots$  up to ten terms and display the result. [10]
6. What do you understand by machine cycle? What are the operations that are performed by microprocessor? Draw and explain the timing diagram of STA 2050 instruction. [1+2+7]
7. What is interrupt vector table? Explain polled and vectored interrupts. [4+6]
8. Write short notes on: [2x5=10]
  - a) RS232 Standard and Handshaking signals
  - b) RISC

\*\*\*

23 TRIBHUVAN UNIVERSITY  
INSTITUTE OF ENGINEERING  
**Examination Control Division**  
2065 Chaitra

Exam.	Regular/Back		
Level	BE	Full Marks	80
Programme	BEL, BEX, BCT	Pass Marks	32
Year / Part	II / II	Time	3 hrs.

**Subject: - Microprocessors**

- ✓ Candidates are required to give their answers in their own words as far as practicable.
- ✓ Attempt All questions.
- ✓ The figures in the margin indicate Full Marks.
- ✓ Assume suitable data if necessary.

1. Differentiate microprocessors and microcontrollers. Compare 8085 and 8086 microprocessors in terms of programming models and addressing techniques. [2+6]

2. Write an assembly language program to count no. of -ve element in a data block containing 16 bytes of data; store the count at the end of the block if the count is greater than 8 otherwise store 0. [8]

3. What is flag? Describe the condition flags and control flags of 8086 briefly. Explain how 20 bit physical address is calculated from 16 bit logical addresses? [1+4+3]

4. Write an assembly language program to get a string input; count no. of vowels and display message 'even vowels' on the screen on the screen if the count is even otherwise display 'odd vowels'. [8]

5. An instruction is stored at memory location as follows: [8]

Memory Location	Hex Code
2050	5A (opcode)
2051	80
2052	20

This instruction loads the content of memory location 2080 into accumulator. Draw timing diagram of this instruction.

6. Present a complete plan to use 2 RAM chips of 16 KB each with 8085 microprocessor. [8]

7. How processor handles interrupt? Describe briefly hardware and software interrupts with example of each. [3]

8. Draw a simple block diagram of RISC and CISC architectures and describe merits and demerits of RISC and CISC computers. [3]

9. What is Asynchronous Communication? Describe RS-232A for serial communication. [2+6]

10. Write short notes on:

- a) Alternatives of semiconductor computing
- b) Memory mapped I/O and I/O mapped I/O

\*\*\*

Exam.	Back		
Level	BE	Full Marks	80
Programme	BEI, BEX, BCI	Pass Marks	32
Year / Part	II / II	Time	3 hrs.

**Subject: - Microprocessors**

- ✓ Candidates are required to give their answers in their own words as far as practicable.
- ✓ Attempt All questions.
- ✓ The figures in the margin indicate Full Marks.
- ✓ Assume suitable data if necessary.

1. Draw the block diagram of 8085 microprocessor and explain different blocks. {10}
2. What is the purpose of the branching instructions? List out all the branching instructions in 8085 and explain each with example. [2+8]
3. What do you mean by statements in assembly language programming? Explain different directives used in assembly language programming. [2+8]
4. Write a program to transfer eight-bit numbers from 9080H to 9090H if bit D<sub>5</sub> is 1 and D<sub>5</sub> is 0. Otherwise transfer data by changing bit D<sub>2</sub> and D<sub>6</sub> from 1 to 0 or 0 to 1. Assume there are ten numbers. [10]
5. Write a program to generate multiplication table of five numbers stored in memory as array, store the result and display in following format. [10]
 

5 10 15 20 25 30 35 40 45 50  
 3 6 9 12 15 18 21 24 27 30  
 ... ..
6. What is unique and non unique address decoding, explain with suitable example. Design an address decoding circuit to interface an input device with eight input switches and a LED output device at 41H and 42H respectively. [4+6]
7. Explain how interrupt is processed in microprocessor systems. Differentiate between vectored and polled interrupts. [5+5]
8. Write short notes on: [2×5]
  - a) RS232 & RS423
  - b) RISC and CISC

13 TRIBHUVAN UNIVERSITY  
INSTITUTE OF ENGINEERING  
**Examination Control Division**  
2064 Boush

Exam. Level	Regular/Back	Full Marks	
BE		80	
Programme		Pass Marks	
BEL, SEX, BCT		32	
Year / Part		Time	
II / II		3 hrs.	

Subject: - Microprocessors

- ✓ Candidates are required to give their answers in their own words as far as practicable.
- ✓ Attempt All questions
- ✓ The figures in the margin indicate Full Marks.
- ✓ Assume suitable data if necessary.

1. Explain the instruction processing cycle of a microprocessor, instruction format and instruction sheet. Write down the history in the development of the microprocessor. [6+4]
2. What do you understand by looping? How can we perform looping in 8083 microprocessor, explain with example. Explain the mechanism of creating loops in creating delay loops of specified time. (2+2+6)
3. There are two tables T1, T2 in memory having ten eight bit data in each. Write a program for 8085 to find the difference of the corresponding element of these two tables. Store the result of each operation on the corresponding element of the third table. Remember that the result should not be negative; it should be  $|T1 - T2|$ . [10]
4. Write down the assembling linking and executing process. Explain macro assembling, one pass and two pass assemblers. [5+5]
5. Write down an assembly language program to read a string and count the no of vowels in the string. Display the no of vowels in the string and the string without the vowels in it in a clear screen with reverse attribute. [10]
6. What do you mean by address decoding? Differentiate between I/O mapped I/O and memory mapped I/O. Design an address decoding circuit to interface two RAM blocks and a ROM block each of 4 KB starting at address 4000H. [2+2+6]
7. Explain the interrupt processing operation for hardware and software in 8086 microprocessor. [10]
8. Write short notes on: [2×5]
  - a) Types of memory
  - b) Digital Signal Processor

Exam.	Regular/Back		
Level	BE	Full Marks	80
Programme	BEL, BEX, DCT	Pass Marks	32
Year / Part	II / II	Time	3 hrs.

**Subject: - Microprocessors**

- ✓ Candidates are required to give their answers in their own words as far as practicable.
- ✓ Attempt All questions.
- ✓ The figures in the margin indicate Full Marks.
- ✓ Assume suitable data if necessary.

1. Compare Harvard architecture with Von Neuman architecture. Explain which architecture 8085 microprocessor uses and how. [7+3]
2. What do you mean by stack and subroutine? What is the purpose of stack in subroutine call? Explain the concept of subroutine call and usage along with the changes in program execution sequence with a suitable example for 8085 microprocessor. [3+2+5]
3. What do you understand by addressing modes in microprocessor? Explain all the addressing modes of 8086 microprocessor with suitable example for each. [3+7]
4. Write a program for 8085 to convert and copy the lower case ASCII codes to upper case from memory location 9050H to 90A0H if any, otherwise copy as they are. Assume there are fifty codes in the source memory. [Note: ASCII Code for A=65...Z=90, a=97...z=122] [10]
5. Write an assembly language program for 8086 to read a string, count the number of vowels in the string and display the string and its vowels count in a clear screen. [10]
6. What are the different machine cycle operations of 8085 microprocessors? Write the bus timing cycle for IN and OUT instructions. [2+8]
7. What do you understand by address decoding? What are I/O mapped I/O and memory mapped I/O? Design an address decoding circuit for interfacing two RAM chips each of 256 bytes at address 5300H. [2+2+6]
8. Write short notes on: [2×5]
  - a) Vectored Interrupt
  - b) Dynamic Memory Allocation

\*\*\*