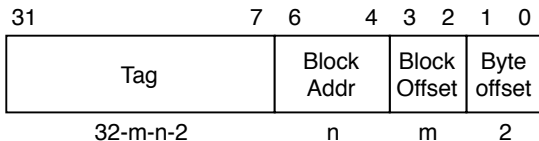
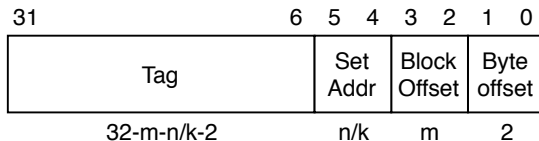


Address**Cache**

	1	32-m-n-2	2^{m+5}			
0	valid	tag	data	data	data	data
1	valid	tag	data	data	data	data
2	valid	tag	data	data	data	data
3	valid	tag	data	data	data	data
4	valid	tag	data	data	data	data
5	valid	tag	data	data	data	data
6	valid	tag	data	data	data	data
7	valid	tag	data	data	data	data

2^n	valid	tag	data	data	data	data	2^{mW}
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Cache size: $2^{m+n+2}B$ All: $2^n(31-m-n+2^{m+5})$ bits $1W=4B=32b$ **Direct mapped****Address****Cache**

	1	32-m-n/k-2	2^{m+5}			
0	0	valid	tag	data	data	data
	1	valid	tag	data	data	data
1	2	valid	tag	data	data	data
	3	valid	tag	data	data	data
2	4	valid	tag	data	data	data
	5	valid	tag	data	data	data
3	6	valid	tag	data	data	data
	7	valid	tag	data	data	data

2^n	valid	tag	data	data	data	data	2^{mW}
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Cache size: $2^{m+n+2}B$ All: $2^n(31-m-n/k+2^{m+5})$ bits $1W=4B=32b$ **k-way set associate**