

**Power Supply Decoupling Capacitors**

The diagram shows the MCP79410 module (PMOD-JA) connected to a power source. The module has two rows of pins. The top row (pins 1-5) is connected to a power source (represented by a red dot and a ground symbol). The bottom row (pins 6-11) is connected to ground. The module is labeled 'MCP79410' and has pins labeled X1, X2, MFP, SCL, and SDA. The power source is labeled 'P M'.

Pinout diagram of MCP7940 showing pins 1 (X1), 2 (X2), MFP, SCL, and SDA.

|    |    |    |    |
|----|----|----|----|
| 2  | 2  | 1  | 1  |
| 4  | 4  | 3  | 3  |
| 6  | 6  | 5  | 5  |
| 8  | 8  | 7  | 7  |
| 10 | 10 | 9  | 9  |
| 12 | 12 | 11 | 11 |

|   |                       |                         |
|---|-----------------------|-------------------------|
| TITLE: <b>FPGA DotMatrix Display</b>  |                       | REV: 1.0                |
|  | Company: Your Company | Sheet: 1/1              |
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