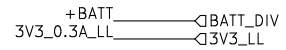
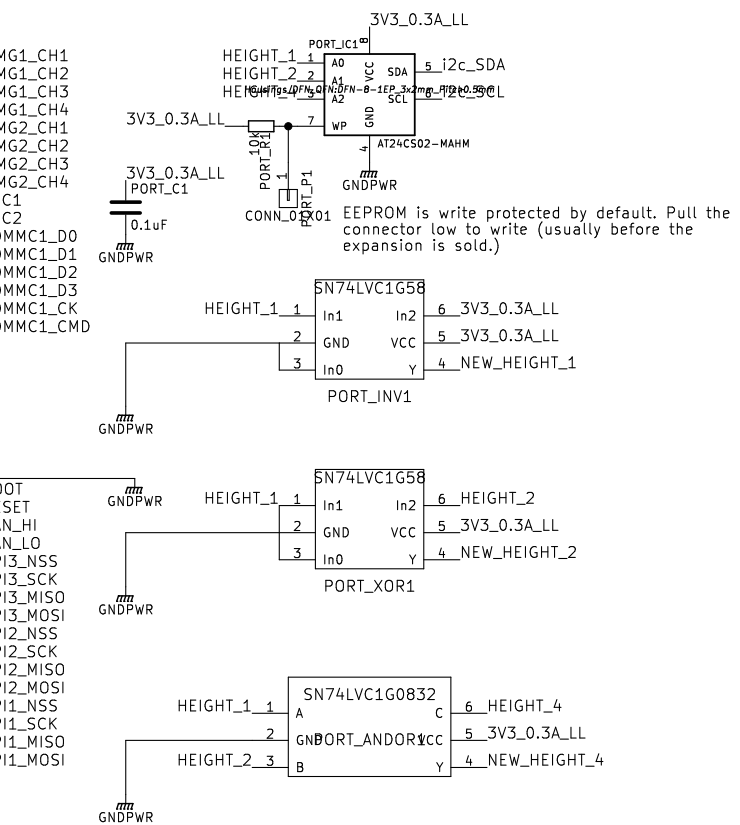
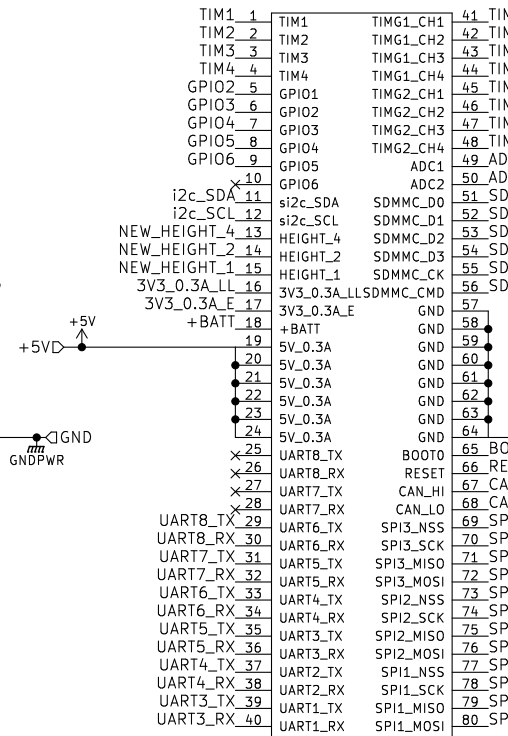
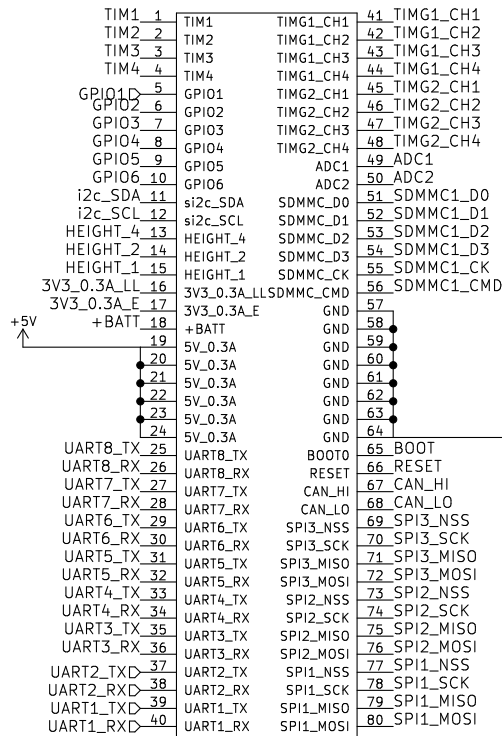


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Export 3v3 logic level for use
in the inverter. Power draw is
small.

Chickadee Expansion Port

Sheet: /ExpansionPort/	
File: ExpansionPort.sch	
Title:	
Size: A4	Date:
KiCad E.D.A. kicad 4.0.1-stable	
Rev:	
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