

Lecture Exercise: delay circuit switching & packet switching

[New Attempt](#)

Due Sep 18 by 11:59p.m. **Points** 5 **Submitting** a text entry box or a file upload

File of size $M=10^6$ bytes. If packet switching is used then the file will be segmented into $N_m=500$ packets, each packet adds 48 Bytes of header in addition to the data (part of the file). Let the bit rate be $B=1\text{Mbps}$.

–In the following assume that the total delay is approximated by the transmission delay only

1. What is the difference in the **transmission delay** between circuit switching and packet switching?
2. What is the throughput and efficiency of packet switching?
3. What is the throughput and efficiency of circuit switching?