# **Junior Design**

Introduction to PCB Design

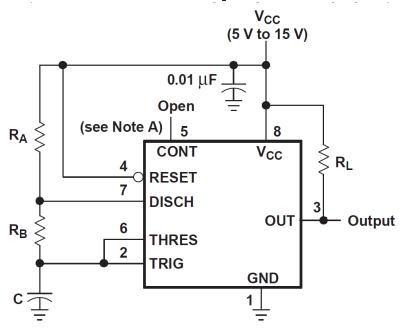
Samuel Dickerson, Ph.D. ECE Department
University of Pittsburgh





#### **Assignment #1 Reflection**

Not as simple as it seems!



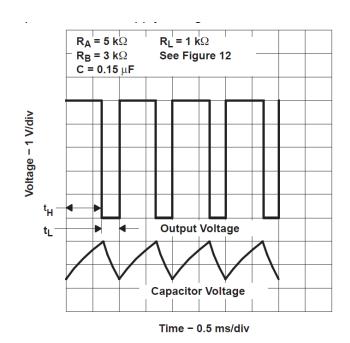


Figure 13. Typical Astable Waveforms



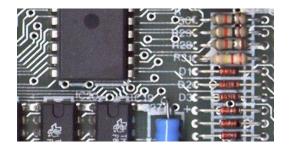
### **Assignment #1 Reflection**

What were some of the challenges faced with this first "Design" Exercise?



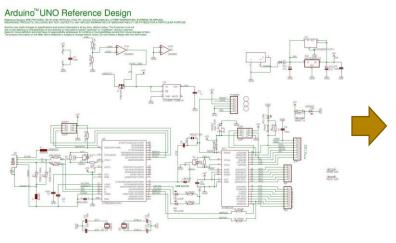
#### **Printed Circuit Boards**

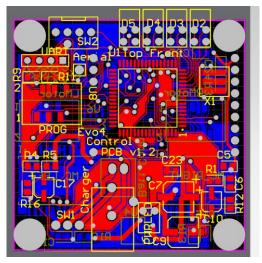




https://en.wikipedia.org/wiki/Printed\_circuit\_board











schematic

layout

Fabrication and assembly



# **PCB Design Tools**



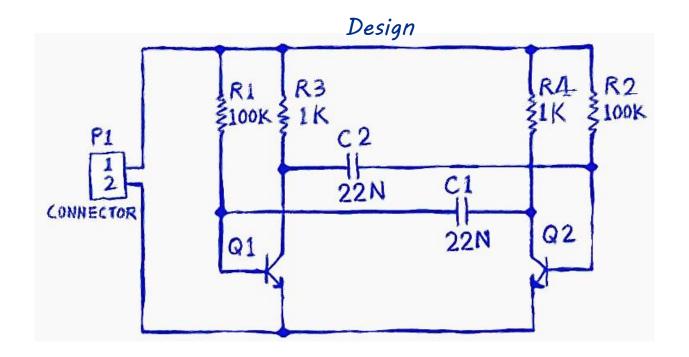






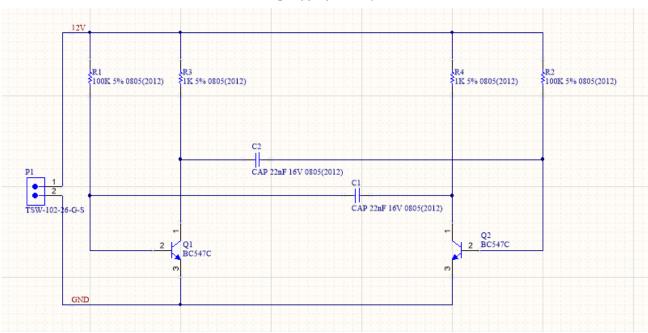




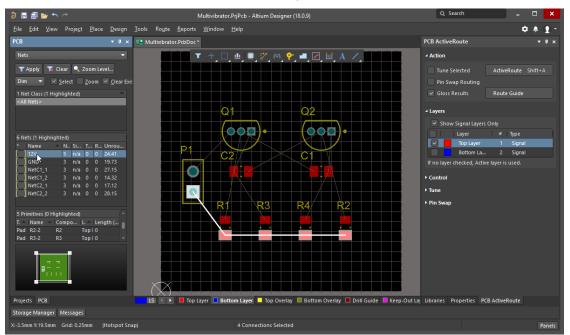




#### Schematic

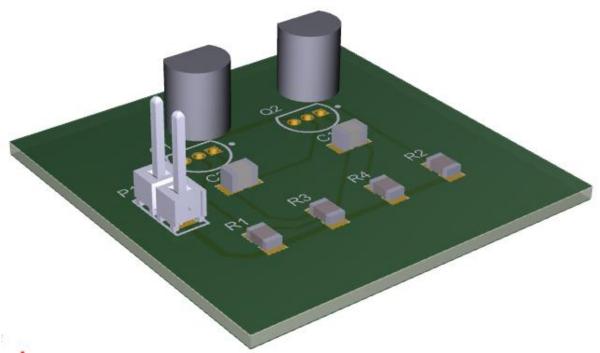


#### Layout

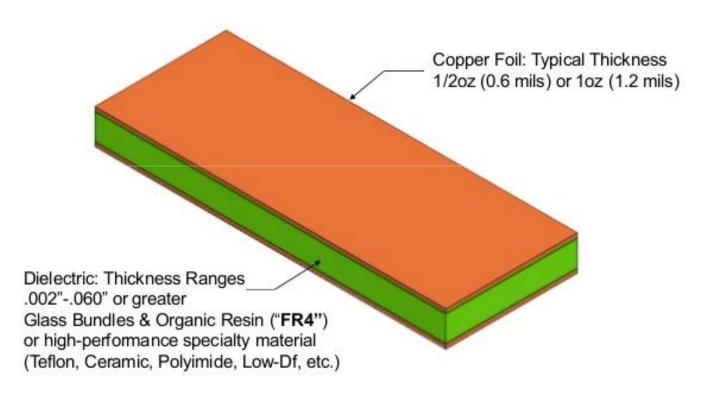




Fabrication and Assembly

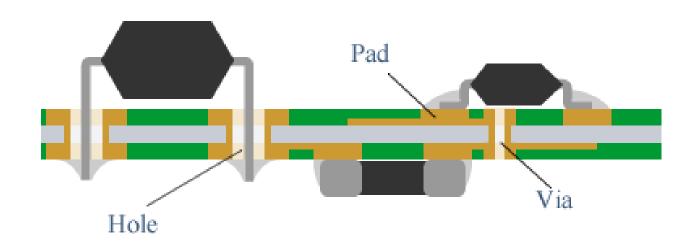


#### **PCB** Materials



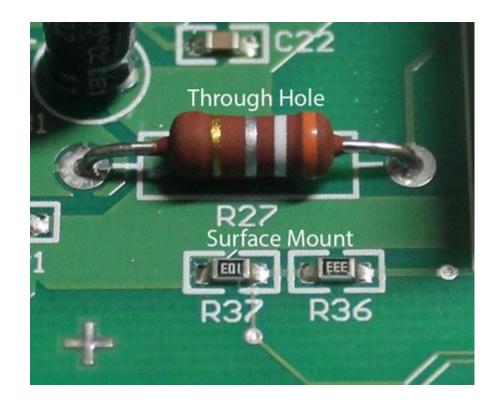


## **PCB Layer Stack**



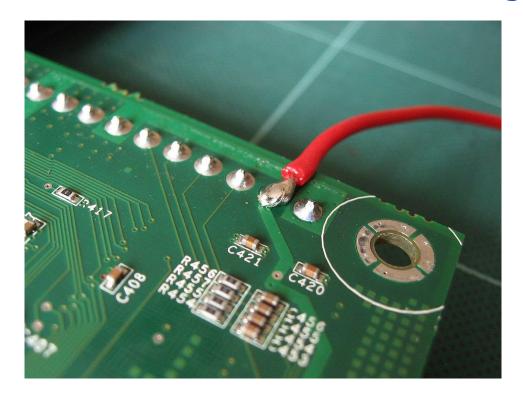


#### **Through Hole vs. Surface Mount Devices**



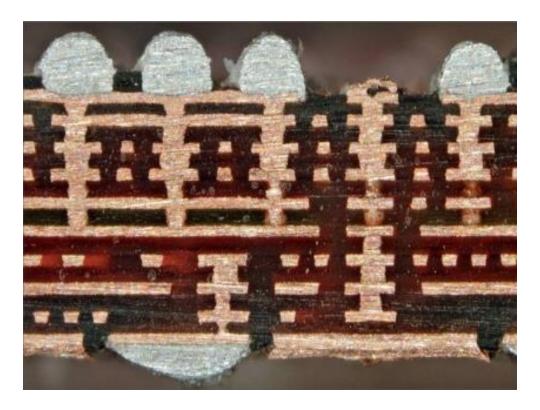


## **Solder Mask and Silkscreen Layers**





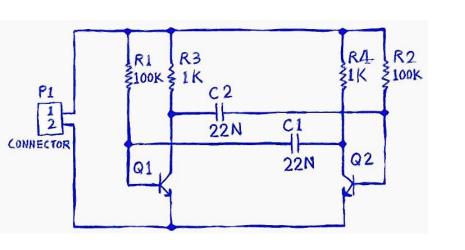
# **Multi-Layer PCB Cross Section**



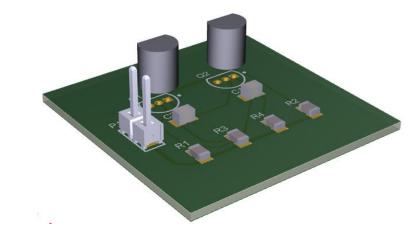


### **Design for Manufacturing (DFM)**

**Layout versus Schematic (LVS)** 



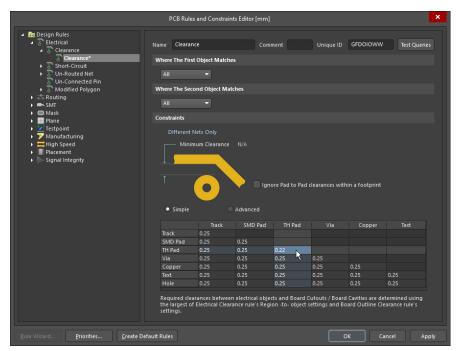


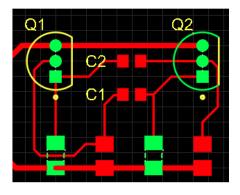


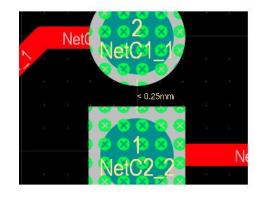


#### **Design for Manufacturing (DFM)**

**Design Rule Checking (DRC)** 



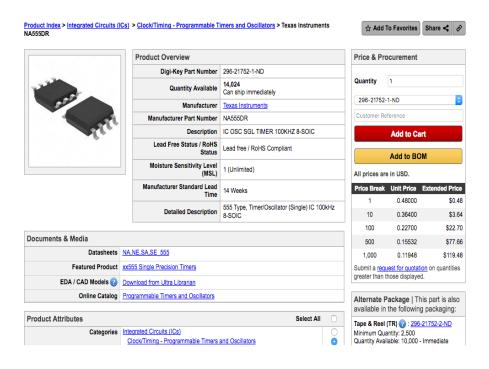






#### **Acquiring Components**







#### **ASSIGNMENT #2**

1. INSTALL THE DESIGN TOOLS

2. COMPLETE AN ALTIUM TUTORIAL

3. RE-DESIGN YOUR 555 TIMER OSCILLATOR CIRCUIT AS A PCB

