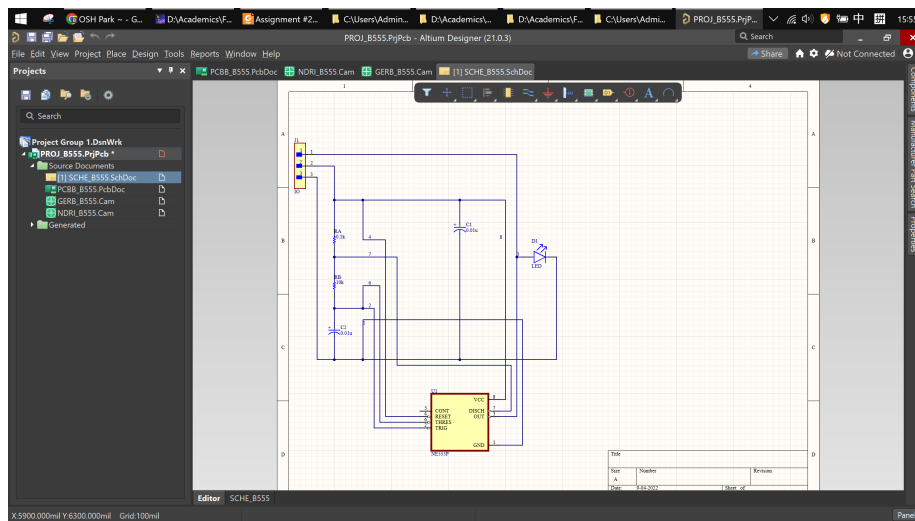


ECE 1895 - ASSIGNMENT 2 REPORT

Yinhao Qian

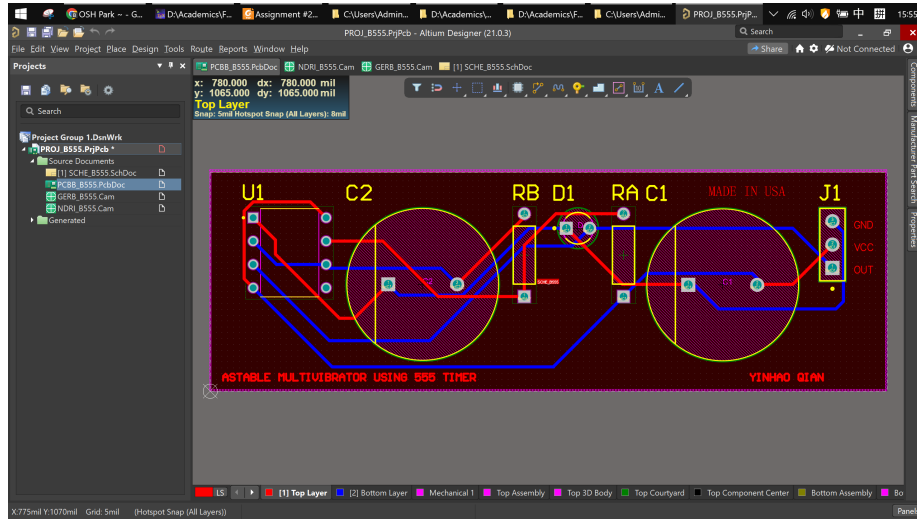
September 4, 2022

1 Screenshots of your schematic:



I'll use a three-pin header named J1 for voltage source, output and ground. Pin 1 provides the output voltage, **which provides a way for the user to access the output waveform of the 555-timer**. Pin 2 connects to the source voltage, **which provides a way for the user to supply power**. Pin 3 connects to ground, **which provides a way for the user to supply ground**.

2 Screenshots of your PCB:

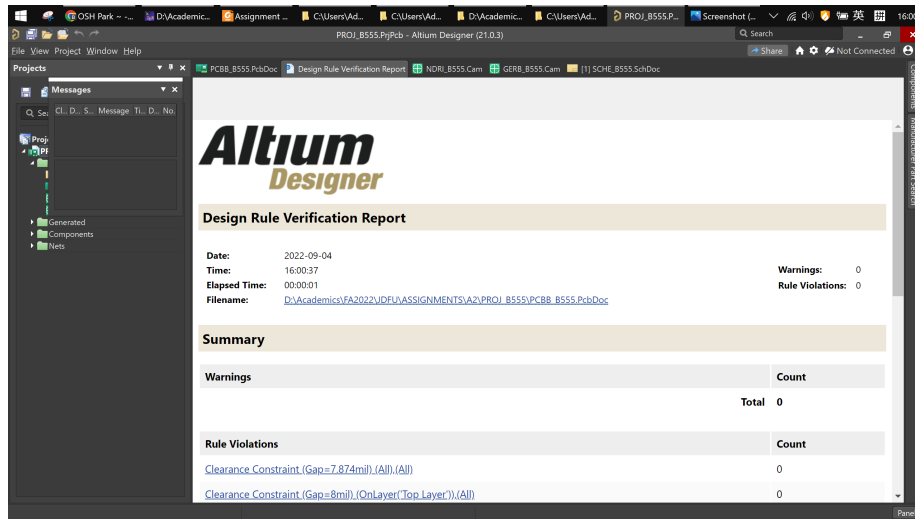


Each designated components map to unique ones from schematics, so it shows the PCB design have **components that match that of your schematic.**

2.0.1 A description of your PCB

The layout of each components remains unchanged since changing the layout manually does not necessary improve the wiring efficiency. I have also added some annotations on the bottom of the PCB board to make it "a bit more professional". The only parts that user should concern about is the three pins on the top right corner. I have added annotations to each pin to make sure they are connected correctly by the user.

3 Screenshots of your report logs showing no connectivity or design rule violations:



4 Screenshots of your BOM:

