

# Gabriele Russo Russo

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## Personal information

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**Birthdate:** January 17, 1992

**Birthplace:** Avellino, Italy

**Nationality:** Italian

**Gender:** Male

## Education

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**2017–today** **PhD student in Computer Science, Control and Geoinformation** at University of Rome Tor Vergata. Winner of a grant financed by the Italian Ministry for Education, University, and Scientific Research.

**2014–2017** **Master of Science in Engineering in Computer Science *cum laude*** at University of Rome Tor Vergata.

*Thesis:* “Optimal Deployment and Run-Time Reconfiguration for Data Stream Processing”.  
*Advisors:* Prof. Valeria Cardellini and Prof. Francesco Lo Presti.

**2011–2014** **Bachelor of Science in Engineering in Computer Science *cum laude*** at University of Rome Tor Vergata.

*Thesis:* “Analysis and Implementation of Energy-Aware Routing Algorithms for Ad-Hoc Wireless Networks”. *Advisor:* Prof. Francesco Lo Presti.

**2006–2011** **High school Diploma** at *Liceo Classico “Leoniano”*, Anagni, 100/100 cum laude.

## Summer schools

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**2017** Summer School on “Optimization, Big Data and Applications” (OBA), Veroli, Italy. July 2017. I also took part in the OBA Award session, presenting my current research activity.

**2017** International Summer School on “Latency Control for Internet of Services” organized by COST Action 1304 *Autonomous Control for a Reliable Internet of Services* (ACROSS), Karlstad, Sweden, June 2017. I won a student travel grant for attending the school, where I presented a poster entitled “Optimal Placement and Replication for Elastic Distributed Data Stream Processing”.

## Awards

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**2011** **Alfiere del Lavoro**, awarded by Italian President Giorgio Napolitano and *Federazione Nazionale Cavalieri del Lavoro*.

Included in the list of 25 Italian students completing high school with the highest grades during the previous five years.

## Tutoring activities

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**2017/2018 Tutoring for *Computer Architectures*** BSc course, taught by Prof. Salvatore Tucci at University of Rome Tor Vergata.

I hold 6 tutorials on logic circuits.

**2016/2017 Tutoring for *Computer Architectures*** BSc course, taught by Prof. Salvatore Tucci at University of Rome Tor Vergata.

I held 8 tutorials on C programming, MIPS assembly, and logic circuits.

## Publications

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### International Journals.....

**J2:** V. Cardellini, F. Lo Presti, M. Nardelli, G. Russo Russo, "Decentralized self-adaptation policies for elastic data stream processing", *Future Generation Computer Systems*, to appear in 2018.

**J1:** V. Cardellini, F. Lo Presti, M. Nardelli, G. Russo Russo, "Optimal operator deployment and replication for elastic distributed data stream processing", *Concurrency Computat: Pract Exper.* 2017;e4334. DOI: 10.1002/cpe.4334

### International Conferences and Workshops.....

**C3:** G. Russo Russo, "Towards Decentralized Auto-Scaling Policies for Data Stream Processing Applications", *Proceedings of 10th ZEUS Workshop (ZEUS 2018)*, Dresden, Germany, February 2018. CEUR-WS.org/Vol-2072.

**C2:** V. Cardellini, F. Lo Presti, M. Nardelli, G. Russo Russo, "Auto-scaling in Data Stream Processing: a Model Based Reinforcement Learning Approach", *Proceedings of InfQ 2017 - New Frontiers in Quantitative Methods in Informatics (in conjunction with ValueTools 2017)*, Communications in Computer and Information Science Vol. 825, Springer, to appear in 2018.

**C1:** V. Cardellini, F. Lo Presti, M. Nardelli, G. Russo Russo, "Towards Hierarchical Autonomous Control for Elastic Data Stream Processing in the Fog" *Proceedings of Autonomic Solutions for Parallel and Distributed Data Stream Processing (AutoDASP 2017)* (in conjunction with Euro-Par 2017), Santiago de Compostela, Spain, August 2017. [https://doi.org/10.1007/978-3-319-75178-8\\_9](https://doi.org/10.1007/978-3-319-75178-8_9)

Anagni, May 21, 2018