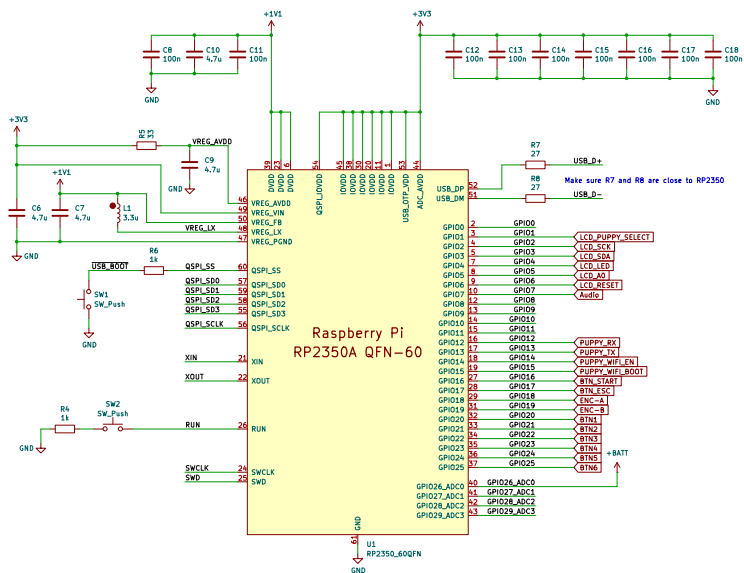
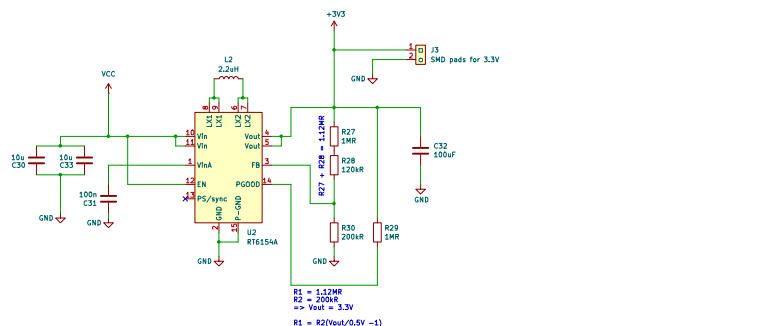
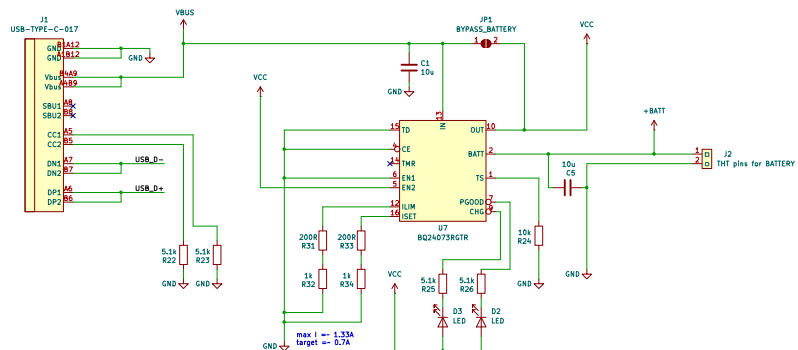
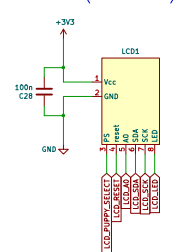


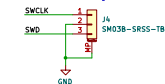
Power



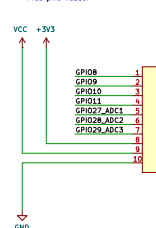
LCD (tht version)



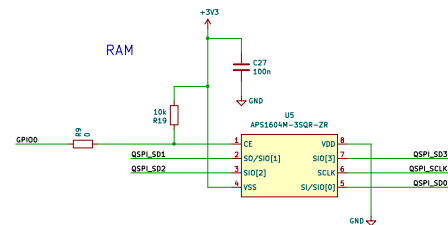
JST Debug Connector



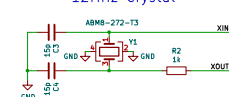
Free pins header



RAM



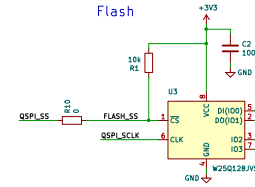
12MHz Crystal



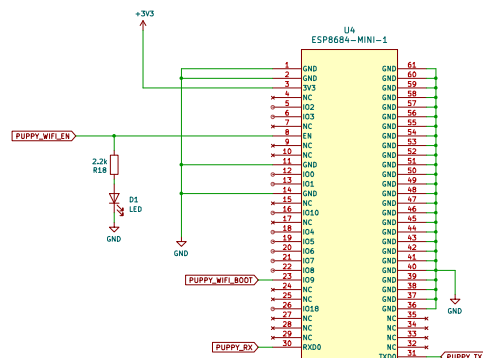
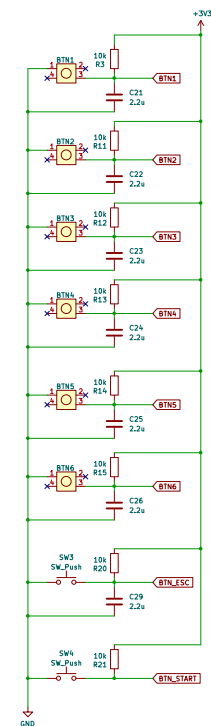
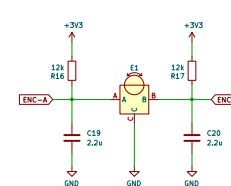
Mountin sys

- H1 MountingHole-M3
- H2 MountingHole-M3

Flash



Input System



TO DO:

- add 10 to 100nF caps

- add footprint to J1

meow

This is a prototype

Chipset

Sheet: /

File: VGC-0_rp1_v0.kicad.sch

Title: VGC-0-rpl

Size: A2

Kicad E.D.A. 8.0.0

Date: 2025-08-08

Rev 2

Id: 1/1