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	Clock generator & Reset	anout								
A .	File: clock_gen.kicad_sch									A
	Switched 4 x 1-lanes PC File: 4_1-lanes_PCle.kica:									
	RC 1—lane and 2—lane P									
	File: RC_4-lanes-PCIe.kid	ad_sch								
3	PCIe switch									В
	File: pcie_switch.kicad_scf Direct 4-lane PCIe									
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