

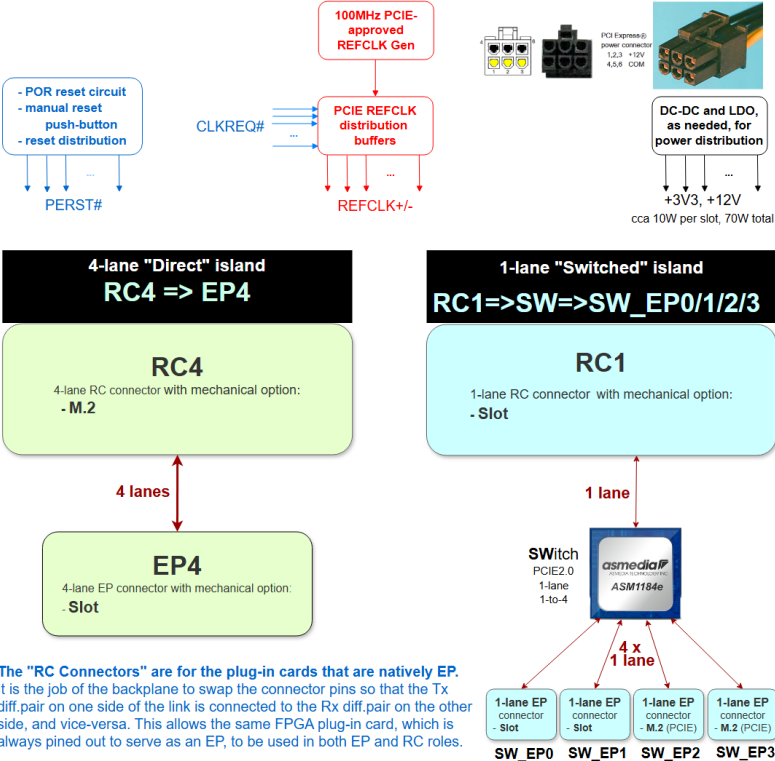
PCIE mini Backplane

Clock generator & Reset fanout  
File: clock\_gen.kicad\_sch

Direct 4-lane PCle  
File: direct\_PCle.kicad\_sch

PCle switch  
File: pcie\_switch.kicad\_sch

Switched 4 x 1-lanes PCle  
File: 4\_1-lanes\_PCle.kicad\_sch



The "RC Connectors" are for the plug-in cards that are natively EP. It is the job of the backplane to swap the connector pins so that the Tx diff.pair on one side of the link is connected to the Rx diff.pair on the other side, and vice-versa. This allows the same FPGA plug-in card, which is always pinned out to serve as an EP, to be used in both EP and RC roles.



Chili.CHIPS\*ba

Sheet: /  
File: openpci2-backplane.kicad\_sch

Title:

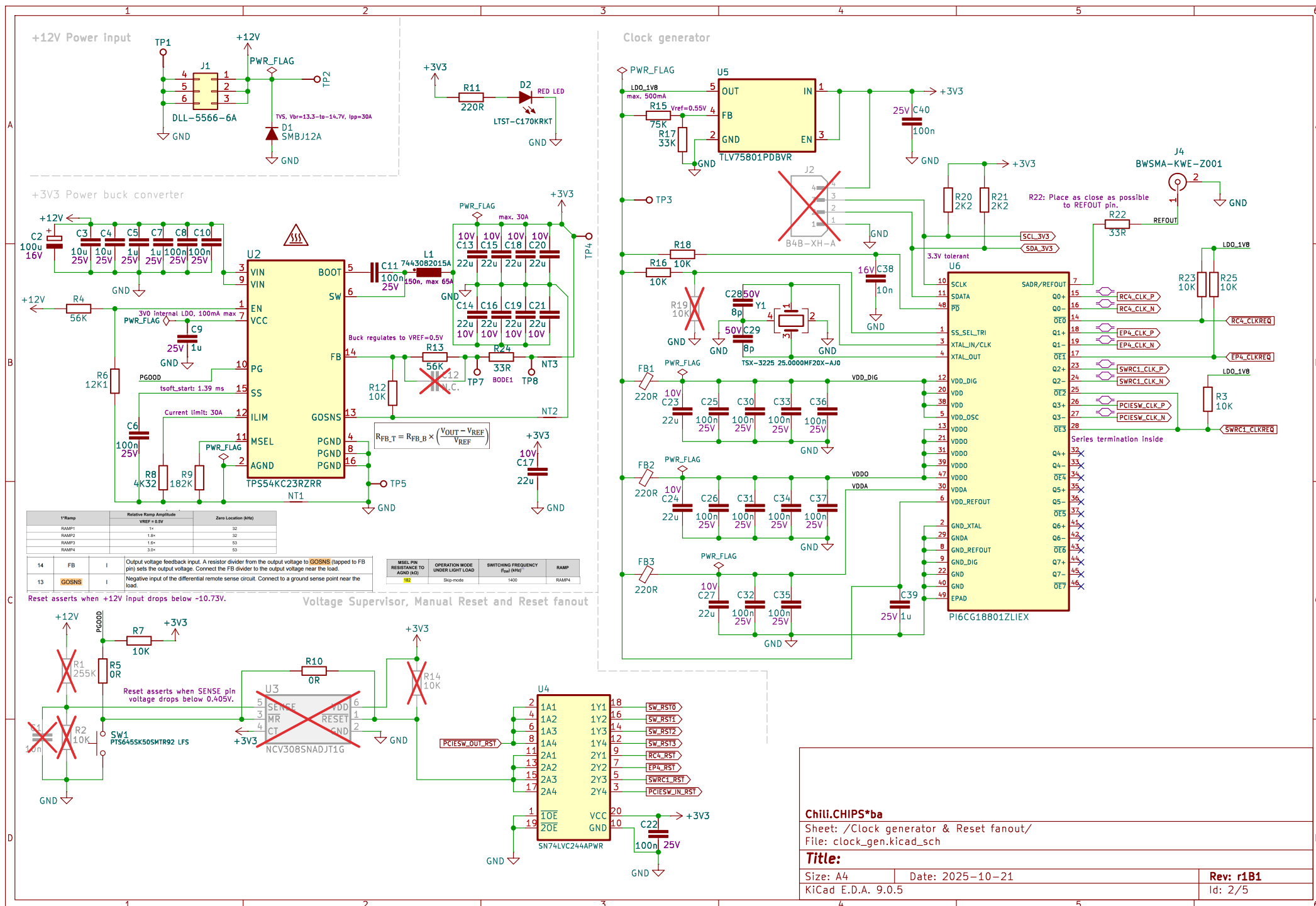
Size: A4

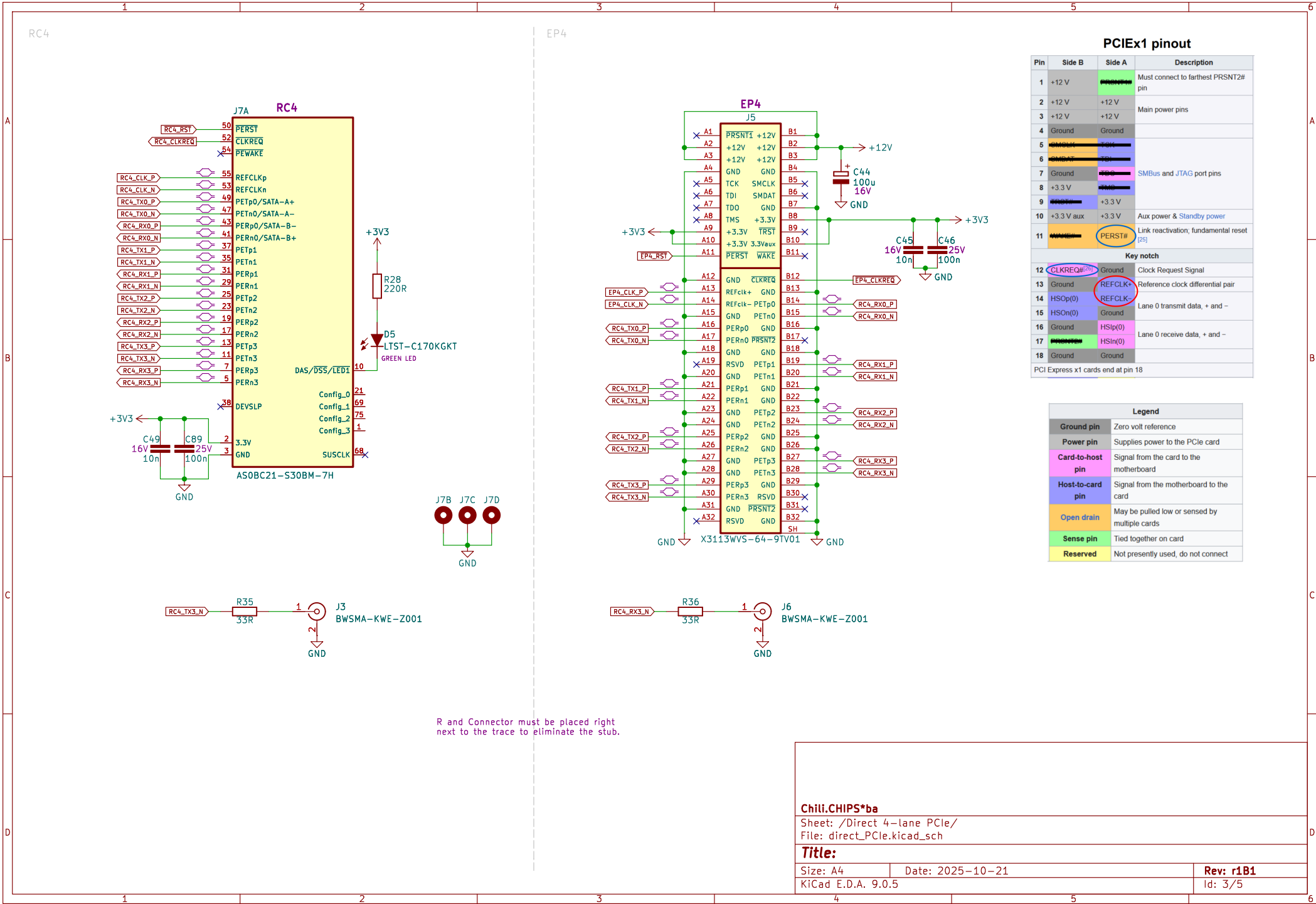
Date: 2025-10-21

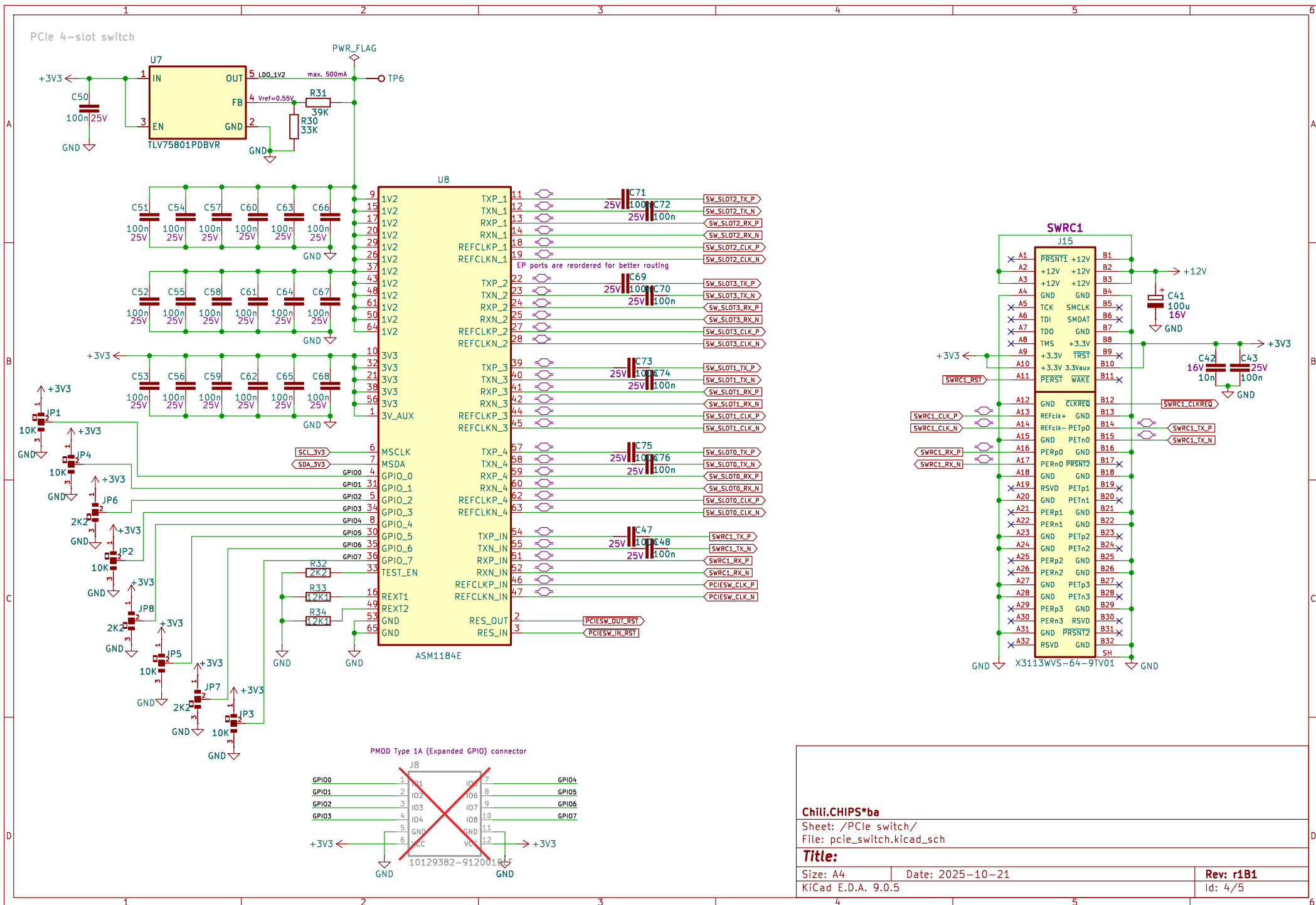
KiCad E.D.A. 9.0.5

Rev: r1B1

Id: 1/5







Chili.CHIPS\*ba

Sheet: /PCle switch/  
File: pcie\_switch.kicad\_sch

Title:

Size: A4

Date: 2025-10-21

Rev: r1B1

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Id: 4/5

