

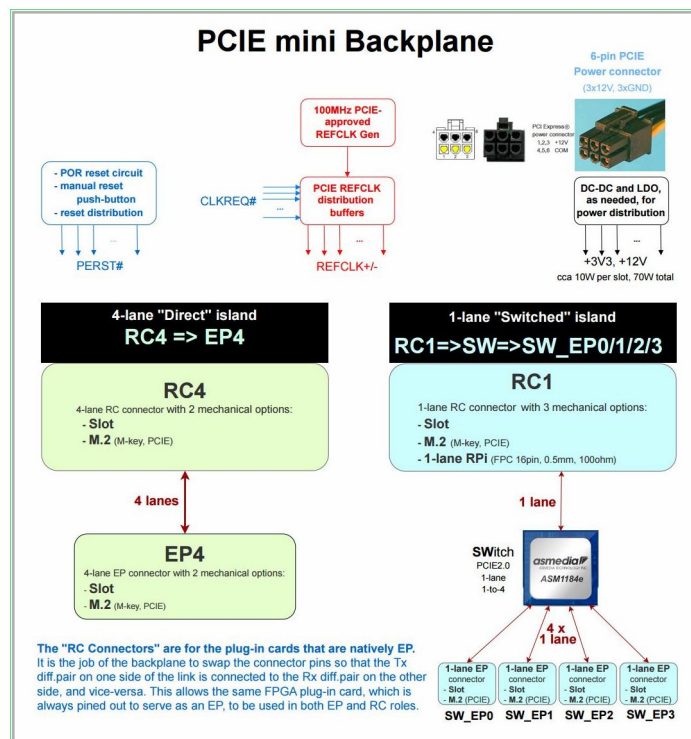
File: clock_gen.kicad_sch

File: direct_PClc.kicad_sch

File: RC_4-lanes-PCle.kicad_sch

File: pcie_switch.kicad_sch

File: 4_1-lanes_PCl.e.kicad_sch



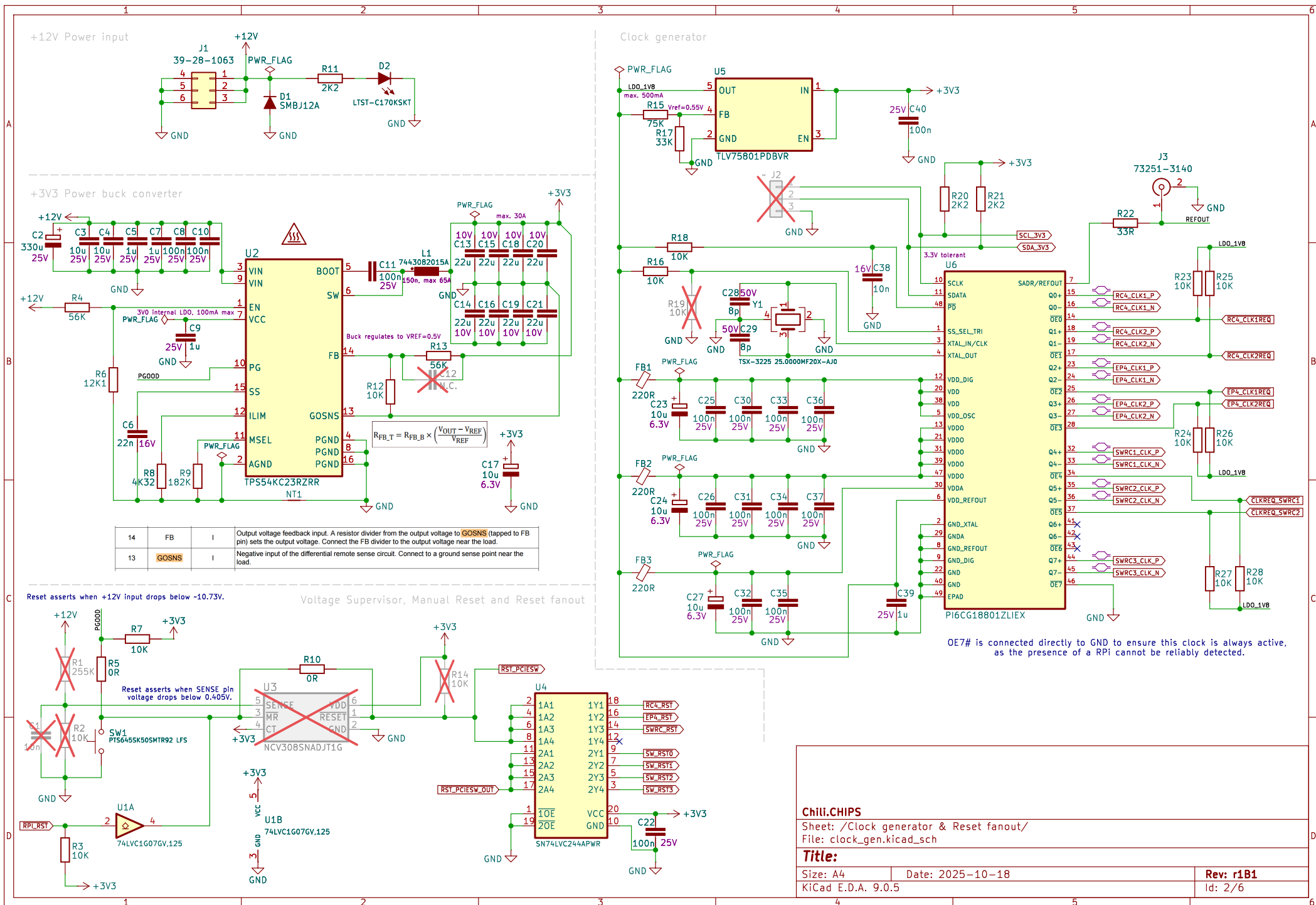
H1 H2 H3 H4 H5 H6

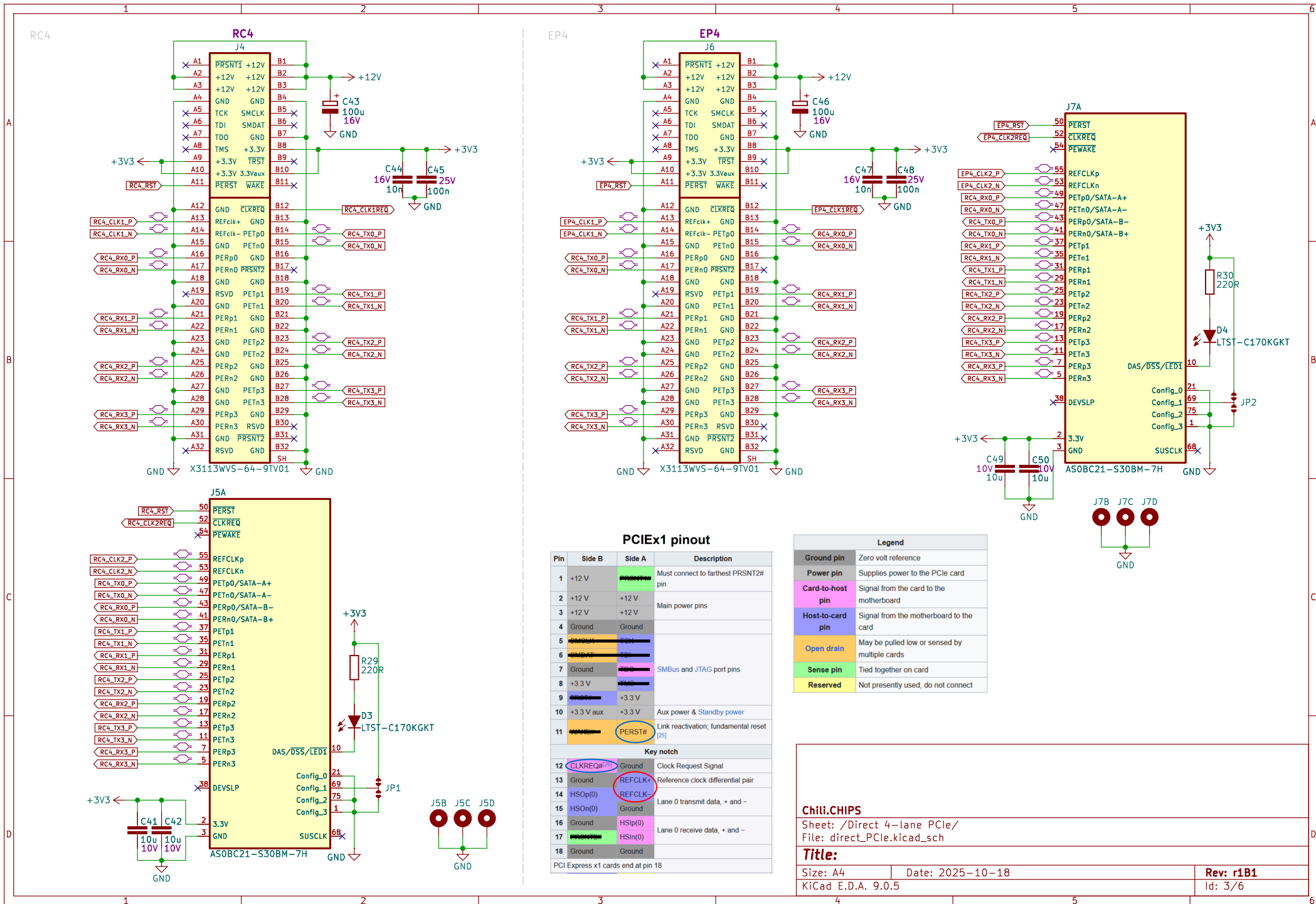
Sheet: /
File: openpci2-backplane.kicad_sch

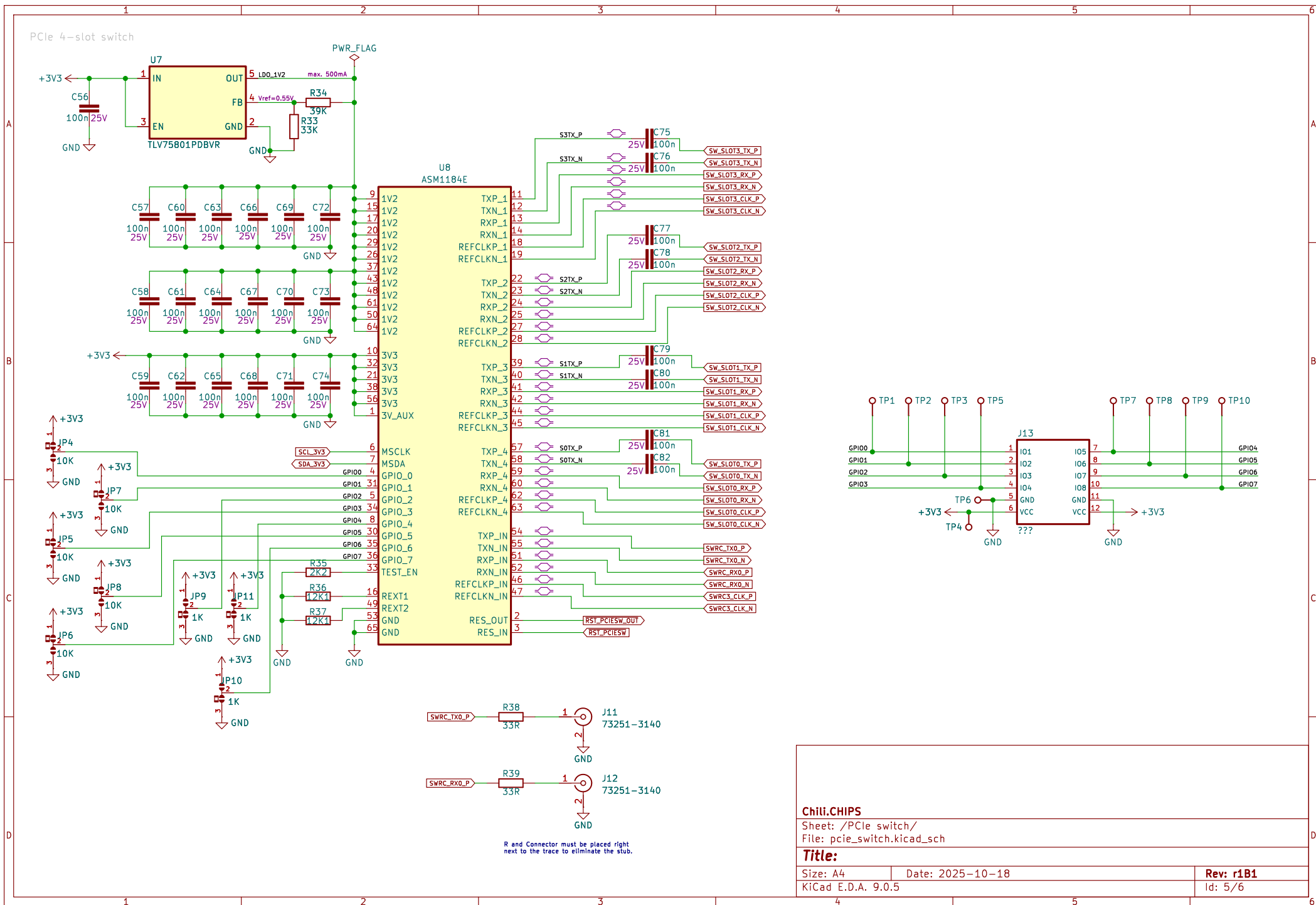
Size: A4	
KiCad E.D.A. 9.0.5	

Date: 2025-10-18

Rev: r1B1
Id: 1/6







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Sheet: /PCle switch/
File: pcie_switch.kicad_sch

Title:

Size: A4
KiCad E.D.A. 9.0.5

Date: 2025-10-18

Rev: r1B1

Id: 5/6

