Г	1 2 3	4 5
	Clock generator	
Α	Α	
П	File: clock_gen.kicad_sch	
П		
	PCle switch	
П		
	File: pcie_switch.kicad_sch	
Н	d	_
	RC 4-lines PCIe	
	File: RC_4-lines-PCIe.kicad_sch	
	/	
	4 x 1-lines PCle	
В	В	
	File: 4_1-lines_PCle.kicad_sch	
	FILE: 4_1=UNES_PUIE.KICAO_SCN	
	Physical Ref.	
	Direct PCIe	
	File: direct_PCle.kicad_sch	
Н	rite: ureccrote.kicad.scii	-
С	c c	l control of the cont
+	-	
		Chili.CHIPS
		Sheet: /
D		File: openpci2-backplane.kicad_sch
	D H1 O H2 O H3 O H4 O H5 O H6	Title:
		Size: A4 Date: 2025-01-29 Rev: r1B1
L		KiCad E.D.A. 8.0.8 Id: 1/6
	1 2 1 3	5









