

Clock generator & Reset fanout

File: clock_gen.kicad_sch

Direct 4-lane PCIe

File: direct_PClc.kicad_sch

RC 1-lane and 2-lane PCIe

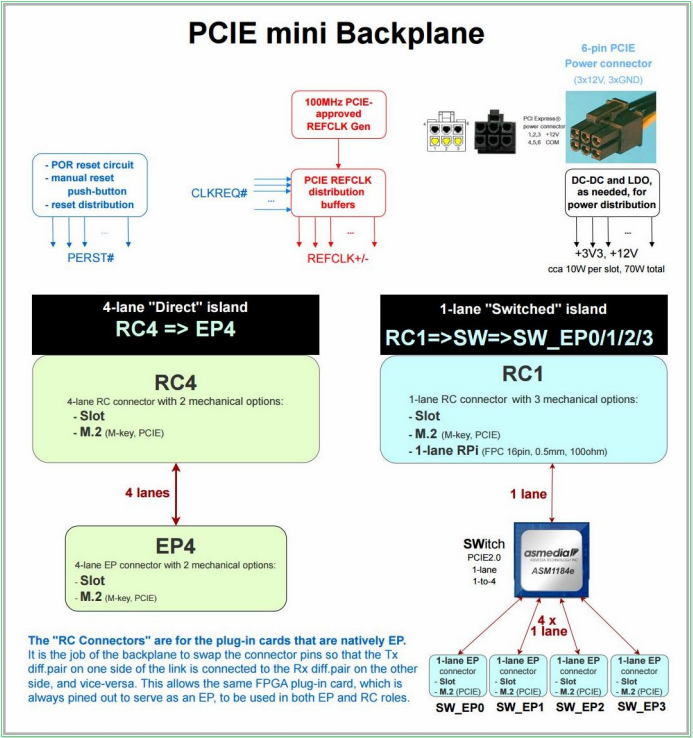
File: RC_4-lanes-PClc.kicad_sch

PClc switch

File: pcic_switch.kicad_sch

Switched 4 x 1-lanes PCIe

File: 4_1-lanes_PClc.kicad_sch



Chili.CHIPS

Sheet: /
File: openpci2-backplane.kicad_sch

Title:

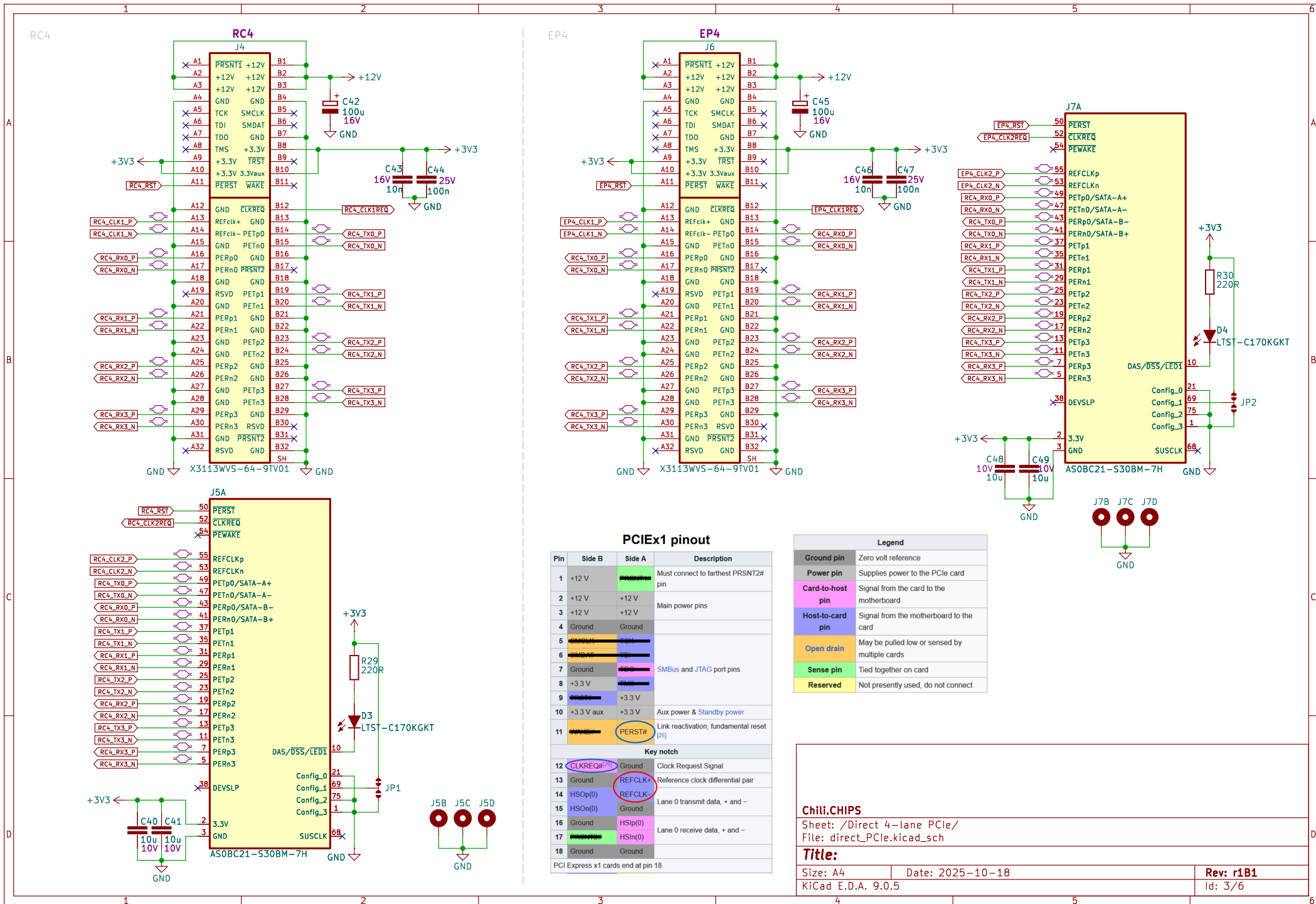
Size: A4

Date: 2025-10-18

KiCad E.D.A. 9.0.5

Rev: r1B1

Id: 1/6



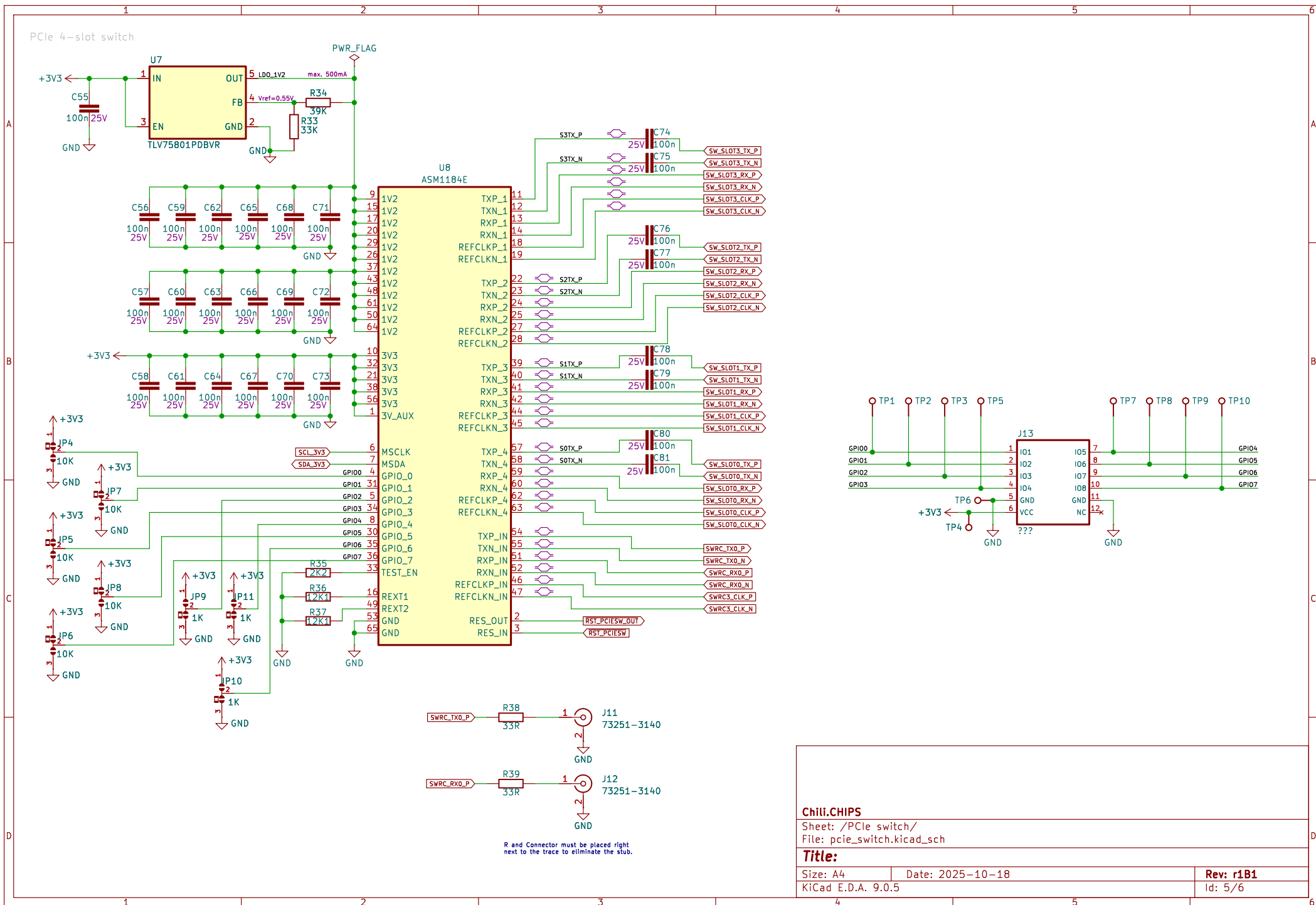
D



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Rev: r1B1
Id: 4/6



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Sheet: /PCle switch/
File: pcie_switch.kicad_sch

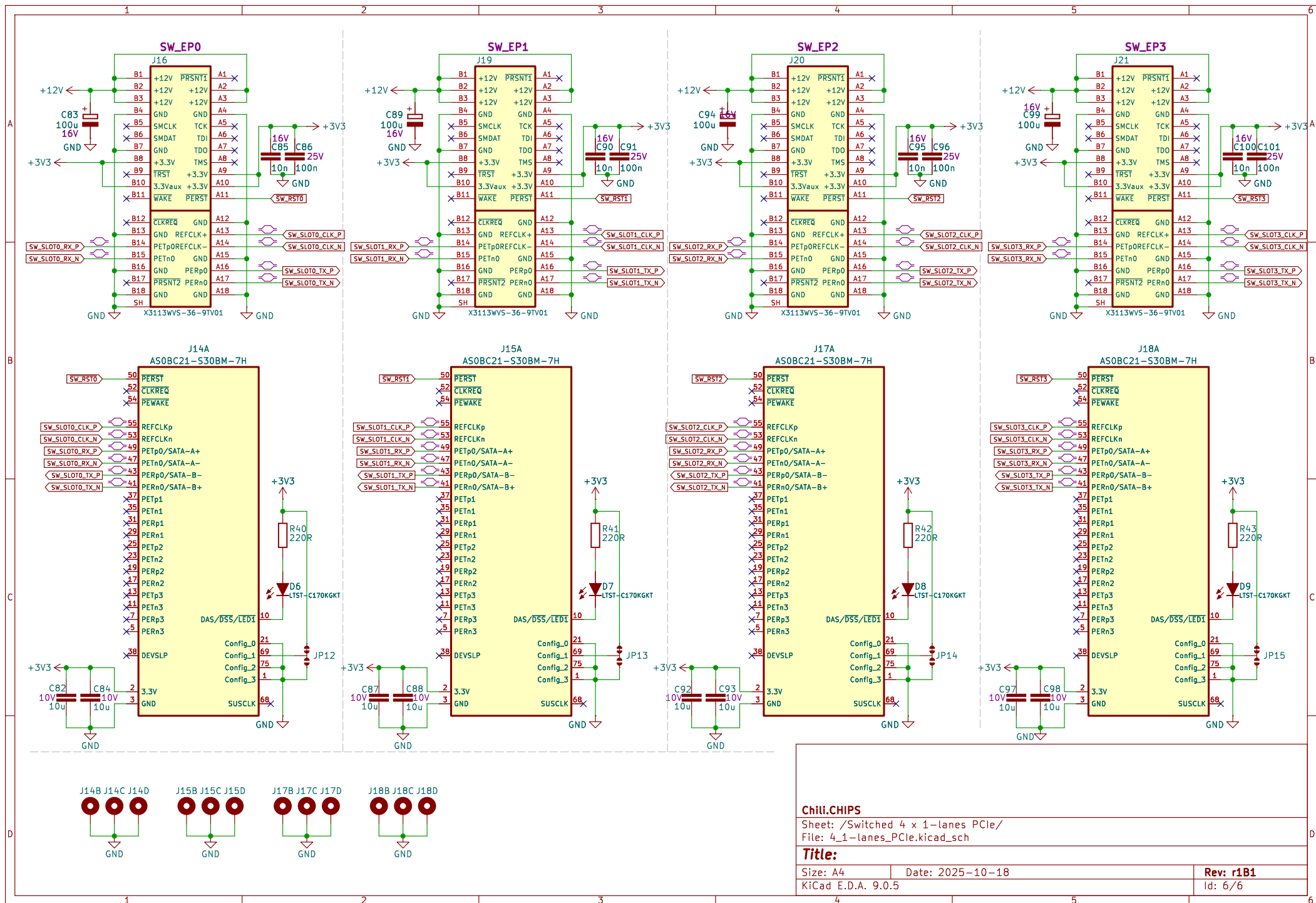
Title:

Size: A4
KiCad E.D.A. 9.0.5

Date: 2025-10-18

Rev: r1B1

Id: 5/6



Chili.CHIPS

Sheet: /Switched 4 x 1-lanes PCIe/
File: 4_1-lanes_PClc.kicad_sch

Title:

Size: A4 Date: 2025-10-18

KiCad E.D.A. 9.0.5

Rev: r1B1

Id: 6/6