

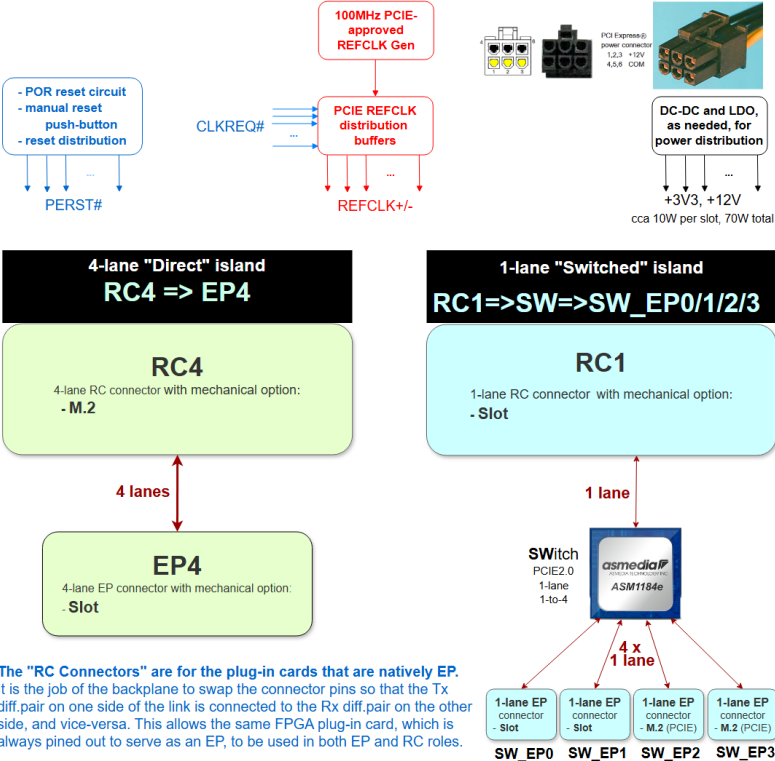
PCIE mini Backplane

Clock generator & Reset fanout
File: clock_gen.kicad_sch

Direct 4-lane PCIe
File: direct_PCIe.kicad_sch

PCIe switch
File: pcie_switch.kicad_sch

Switched 4 x 1-lanes PCIe
File: 4_1-lanes_PCIe.kicad_sch



Chili.CHIPS*ba

Sheet: /
File: openpci2-backplane.kicad_sch

Title:

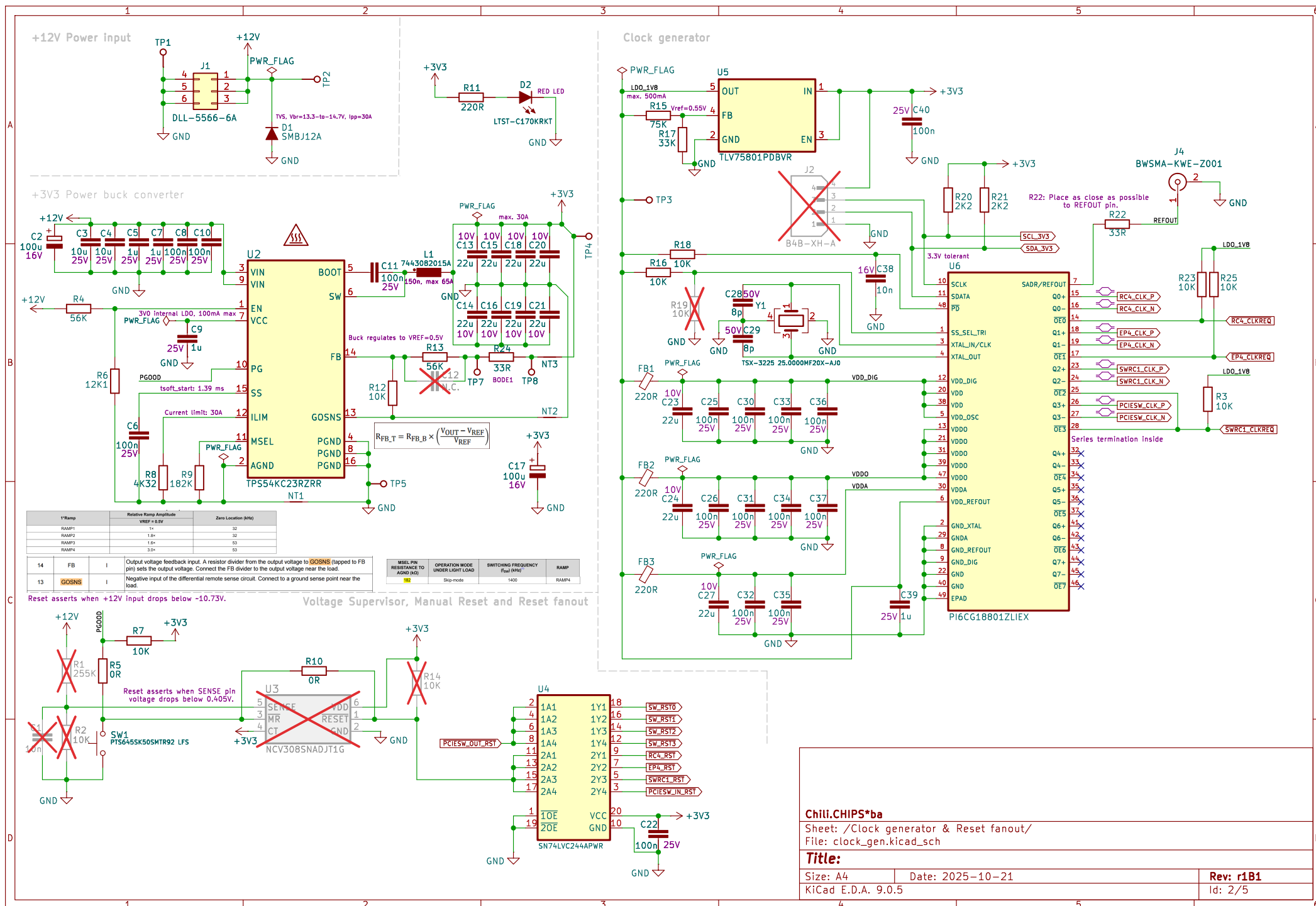
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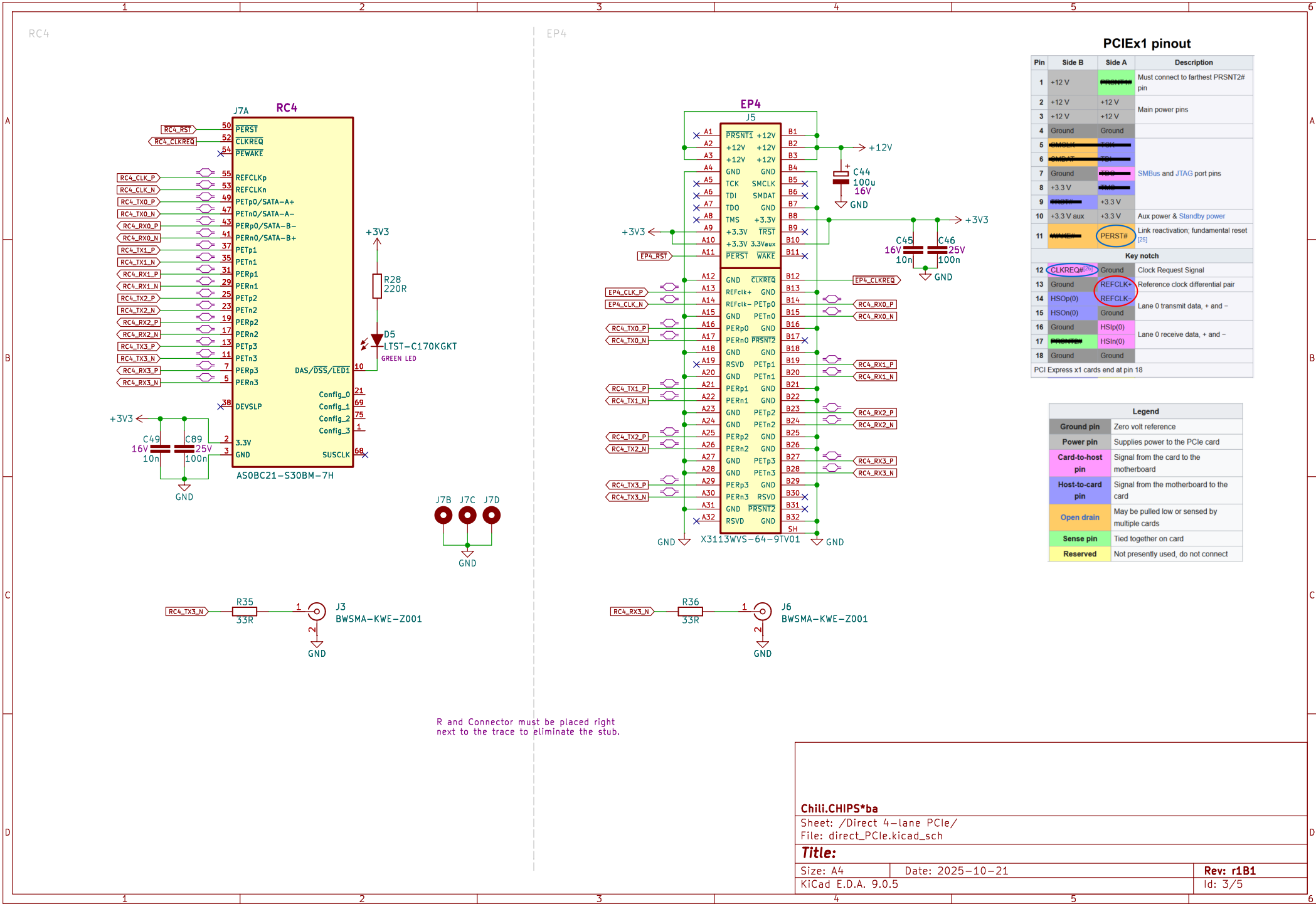
Date: 2025-10-21

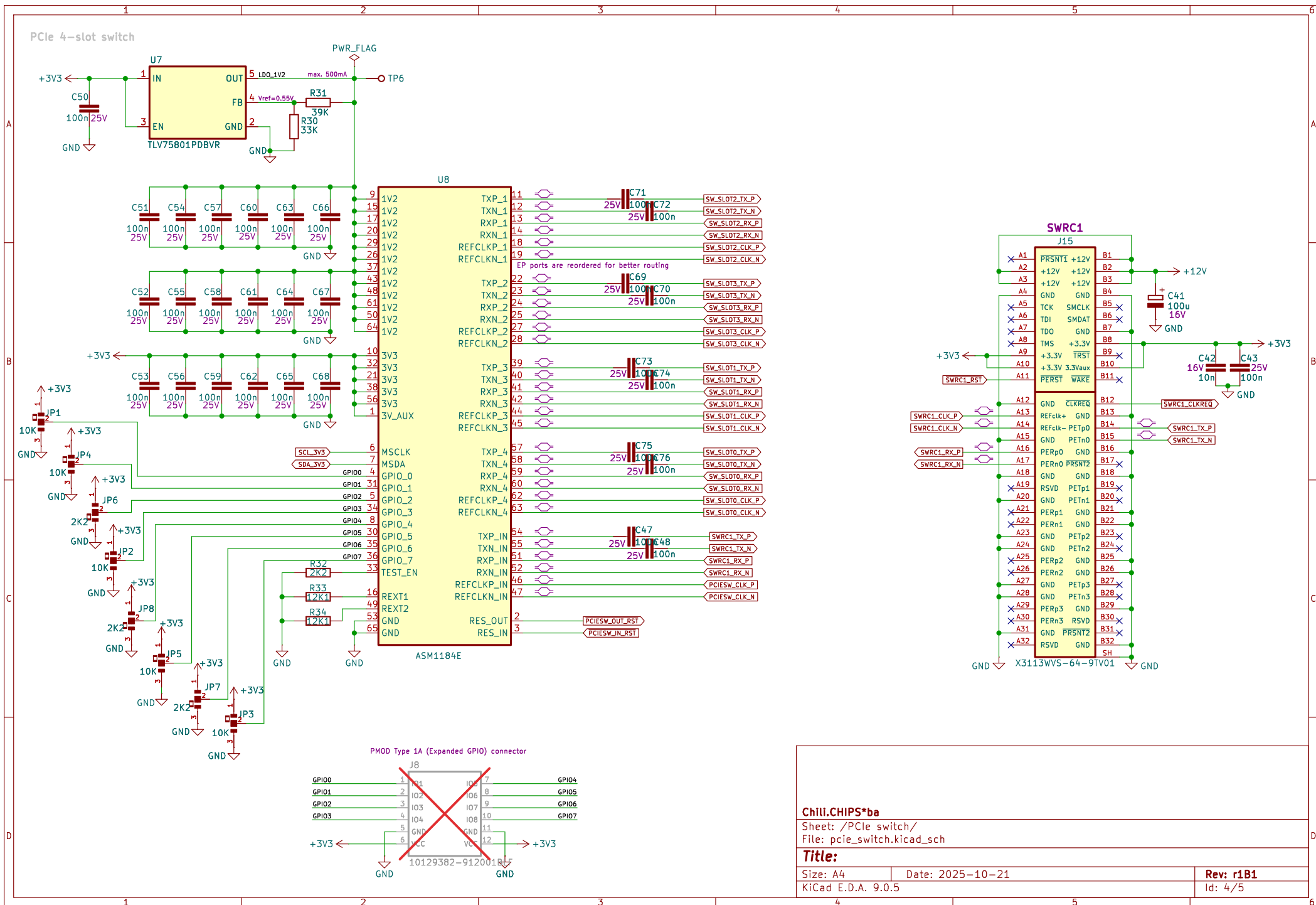
KiCad E.D.A. 9.0.5

Rev: r1B1

Id: 1/5







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Sheet: /PCle switch/
File: pcie_switch.kicad_sch

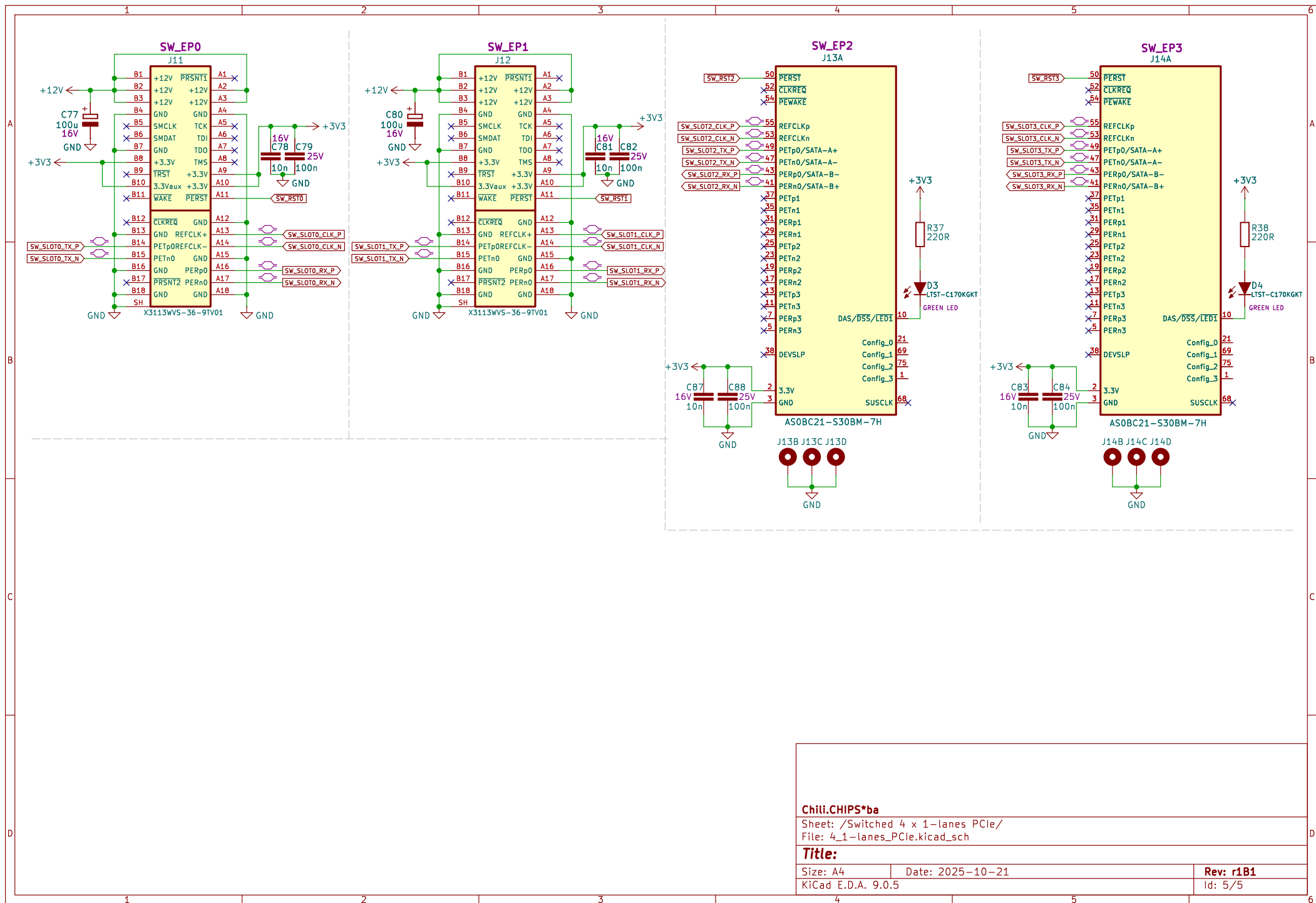
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Size: A4 Date: 2025-10-21

KiCad E.D.A. 9.0.5

Rev: r1B1

Id: 4/5



Chili.CHIPS*ba

Sheet: /Switched 4 x 1-lanes PCIe/
File: 4_1-lanes_PClc.kicad_sch

Title:

Size: A4

Date: 2025-10-21

Rev: r1B1

KiCad E.D.A. 9.0.5

Id: 5/5