	1	2	3		4		5	6]
	Clock generator & Reset fanout							
4	File: clock_gen.kicad_sch PCle switch File: pcie_switch.kicad_sch							A
	RC 4-lanes PCIe File: RC_4-lanes-PCIe.kicad_sch							
3	4 x 1-lanes PCle File: 4_1-lanes_PCle.kicad_sch Direct PCle							В
	File: direct_PCle.kicad_sch							
								С
_								
D	$\oplus \oplus \oplus \oplus \oplus$		OH6 * GONING		Chili.CHIPS Sheet: / File: openpci2-backplane.ki	cad_sch		D
	V V V V		THE SOUNDATION		Title: Size: A4 Date: 20 KiCad E.D.A. 9.0.2	25-07-01		Rev: r1B1









