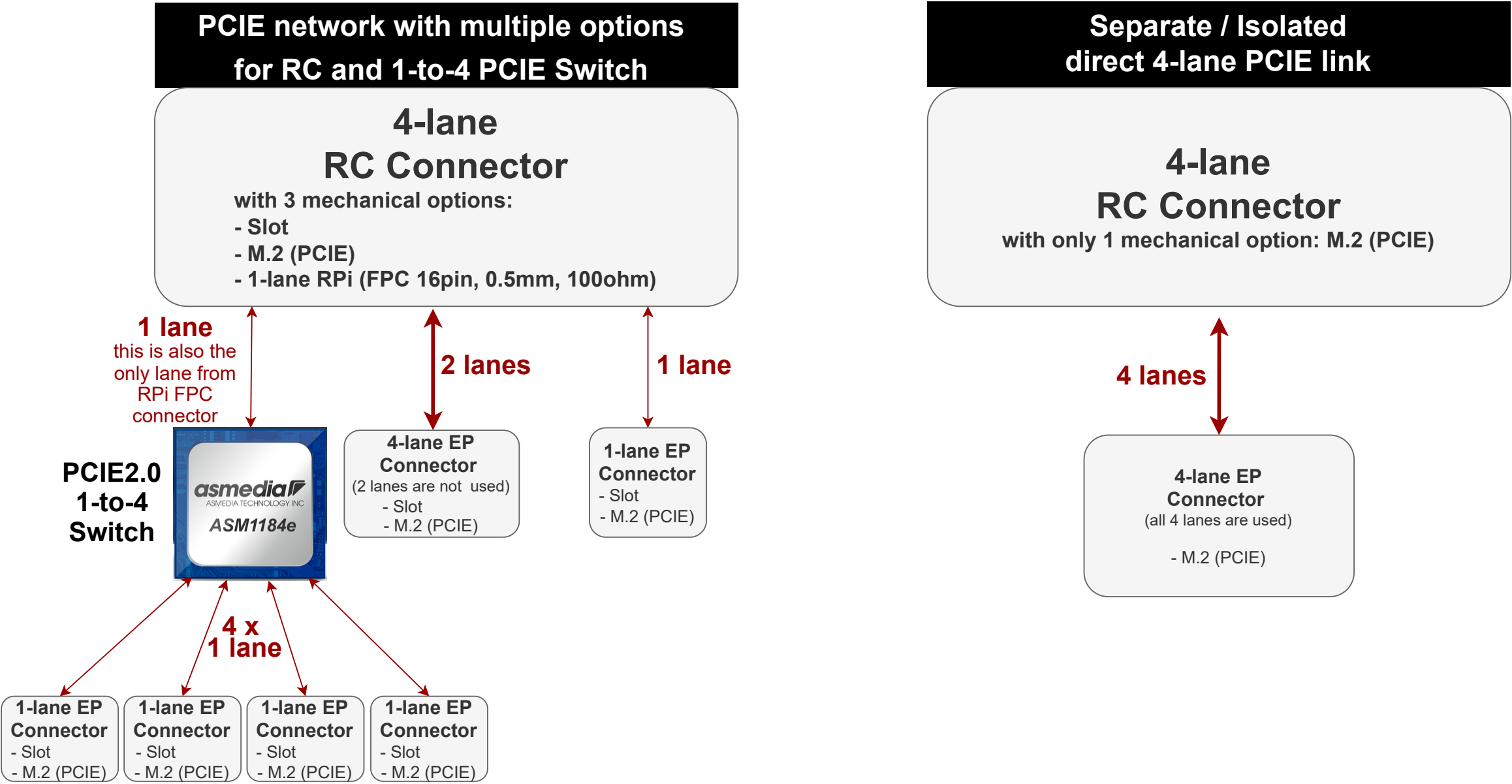
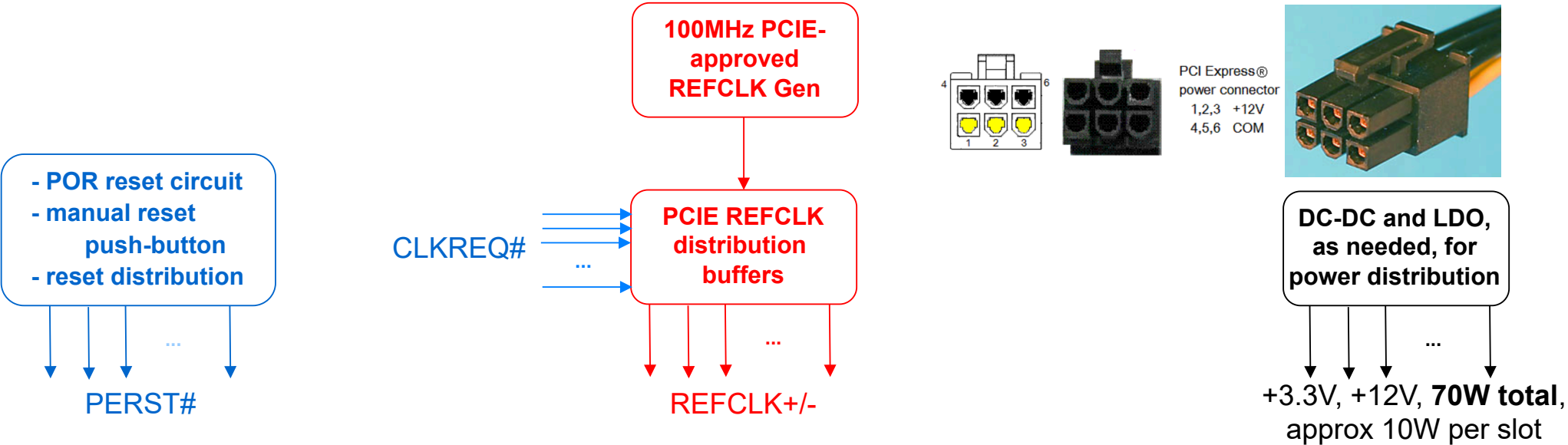
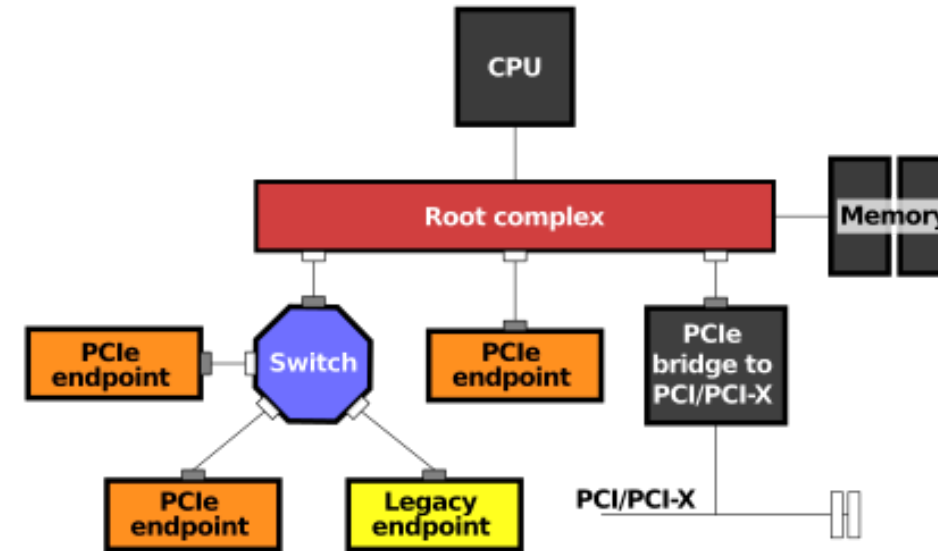
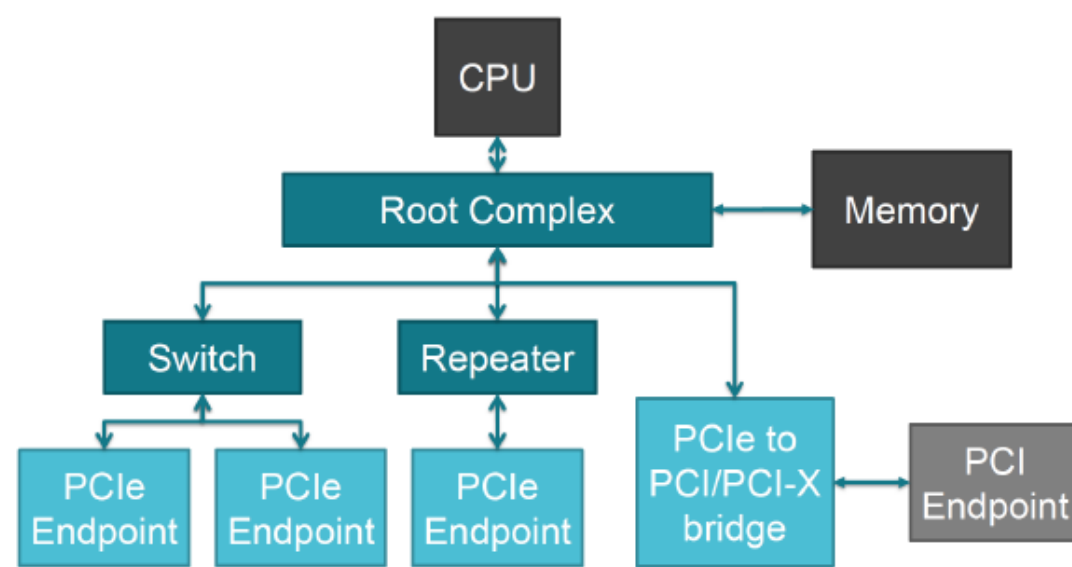


PCIE mini Backplane



General PCIE topology



PCIEx1 pinout

Pin	Side B	Side A	Description
1	+12 V	PRST#	Must connect to farthest PRSNT2# pin
2	+12 V	+12 V	Main power pins
3	+12 V	+12 V	
4	Ground	Ground	
5	SMCLK	TDK	SMBus and JTAG port pins
6	SMBAT	TDI	
7	Ground	TDO	
8	+3.3 V	TMS	
9	FRST#	+3.3 V	Aux power & Standby power
10	+3.3 V aux	+3.3 V	
11	WAKE#	PERST#	Link reactivation; fundamental reset [25]
Key notch			
12	CLKREQ#[26]	Ground	Clock Request Signal
13	Ground	REFCLK+	Reference clock differential pair
14	HSOp(0)	REFCLK-	
15	HSOn(0)	Ground	Lane 0 transmit data, + and -
16	Ground	HSIp(0)	
17	PRST2#	HSIn(0)	Lane 0 receive data, + and -
18	Ground	Ground	

PCI Express x1 cards end at pin 18

Legend	
Ground pin	Zero volt reference
Power pin	Supplies power to the PCIe card
Card-to-host pin	Signal from the card to the motherboard
Host-to-card pin	Signal from the motherboard to the card
Open drain	May be pulled low or sensed by multiple cards
Sense pin	Tied together on card
Reserved	Not presently used, do not connect