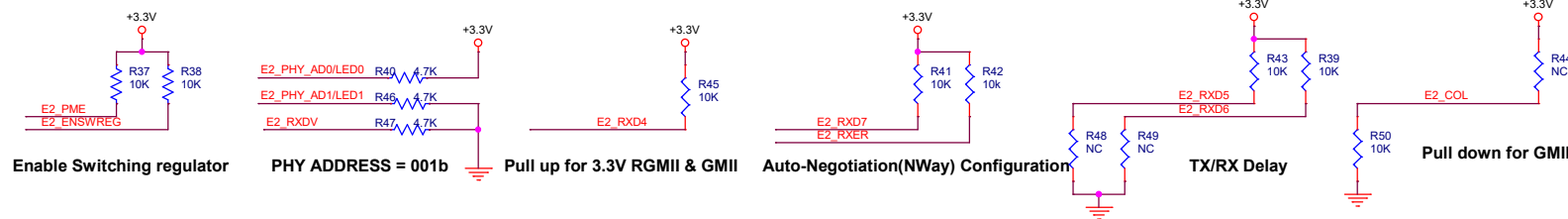
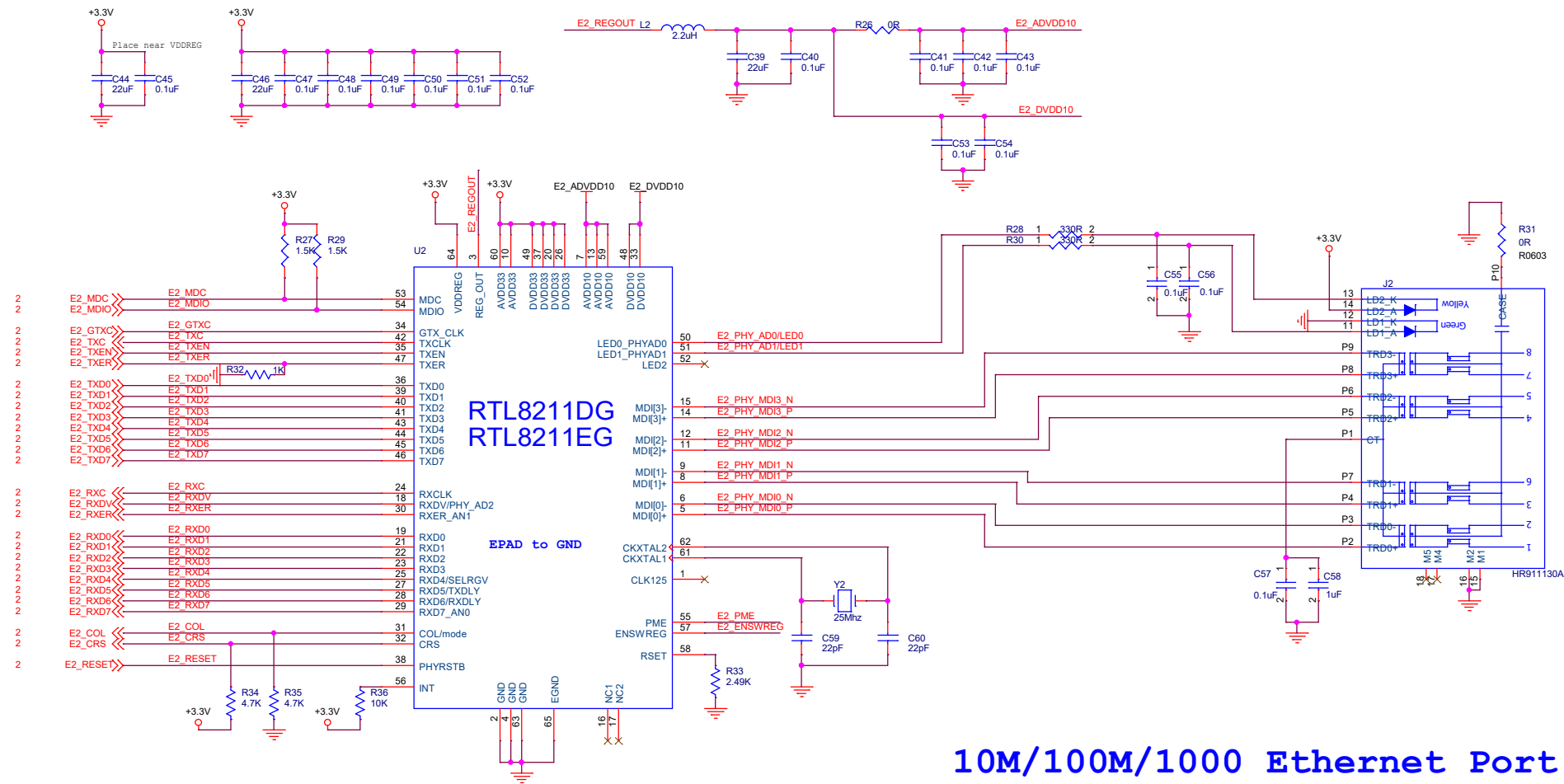
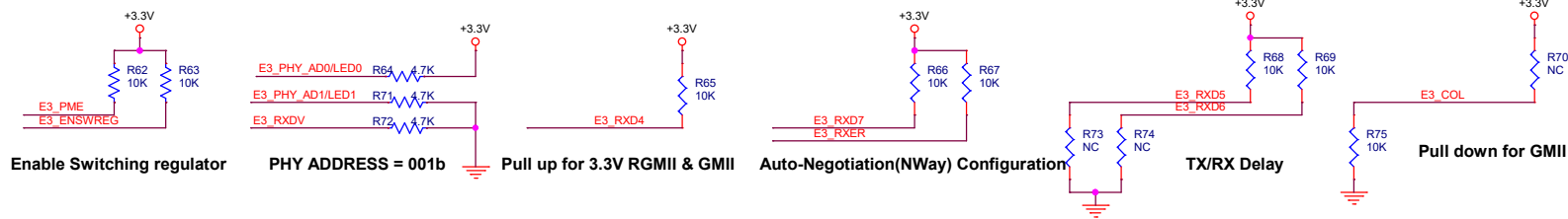
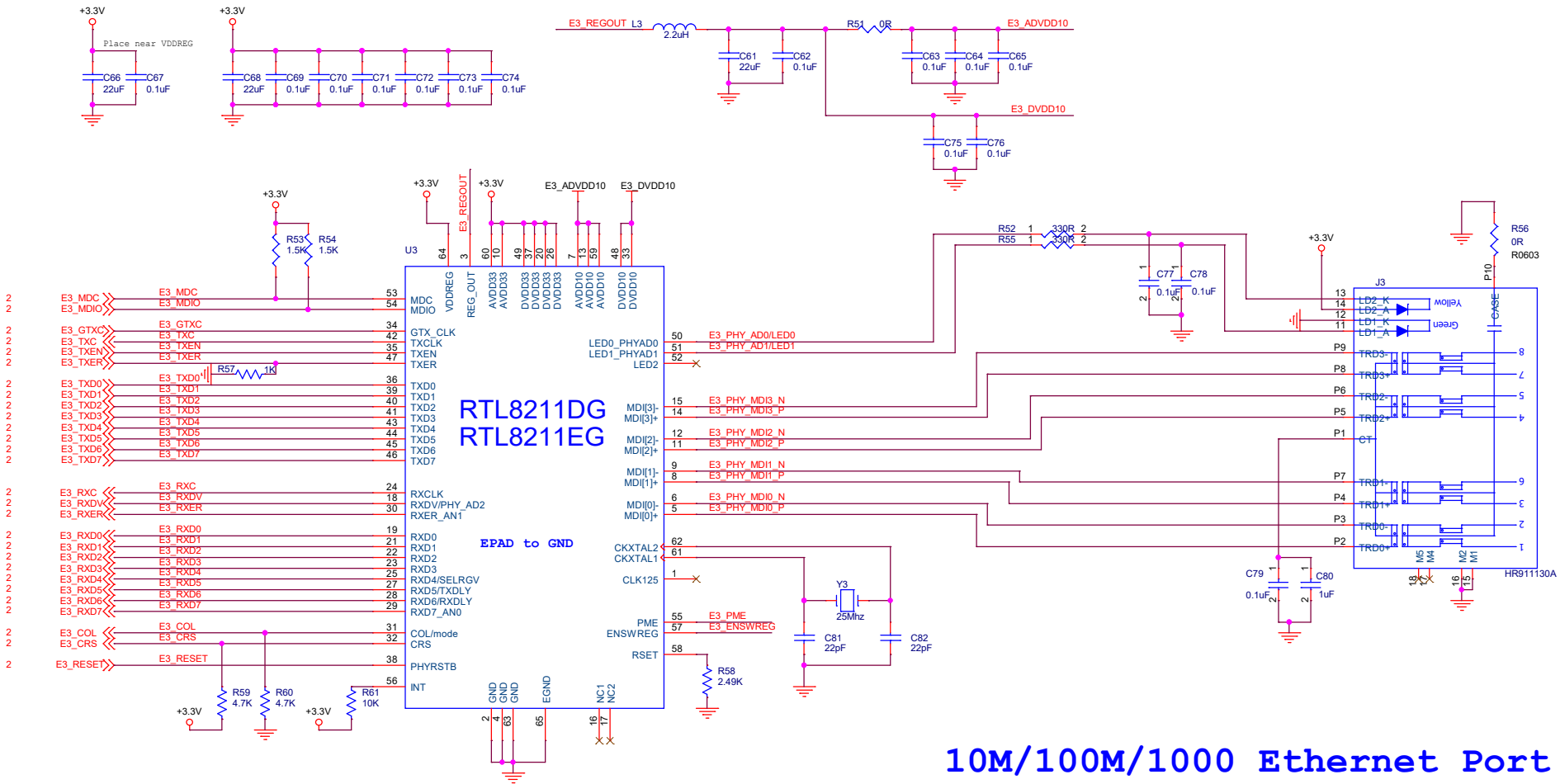


Pin-to-pin connection diagram for the AXKB80337YG module. The diagram shows two 80-pin connectors, CON1 and CON2, with pins numbered 1 to 80. Pin 1 is connected to pin 2, pin 3 to pin 4, pin 5 to pin 6, pin 7 to pin 8, pin 9 to pin 10, pin 11 to pin 12, pin 13 to pin 14, pin 15 to pin 16, pin 17 to pin 18, pin 19 to pin 20, pin 21 to pin 22, pin 23 to pin 24, pin 25 to pin 26, pin 27 to pin 28, pin 29 to pin 30, pin 31 to pin 32, pin 33 to pin 34, pin 35 to pin 36, pin 37 to pin 38, pin 39 to pin 40, pin 41 to pin 42, pin 43 to pin 44, pin 45 to pin 46, pin 47 to pin 48, pin 49 to pin 50, pin 51 to pin 52, pin 53 to pin 54, pin 55 to pin 56, pin 57 to pin 58, pin 59 to pin 60, pin 61 to pin 62, pin 63 to pin 64, pin 65 to pin 66, pin 67 to pin 68, pin 69 to pin 70, pin 71 to pin 72, pin 73 to pin 74, pin 75 to pin 76, pin 77 to pin 78, pin 79 to pin 80. The diagram also shows connections to external components: B13 L4 P, B13 L4 N, B13 L1 P, B13 L1 N, B13 L2 P, B13 L2 N, B13 L6 P, B13 L6 N, B34 L20 P, B34 L20 N, B34 L21 N, B34 L21 P, B34 L22 P, B34 L22 N, B34 L25, B34 L24 P, and B34 L24 N. The diagram is labeled with +5.0V at the top and bottom, and CON1 and CON2 on the left and right respectively. The bottom of the diagram is labeled AXKB80337YG.

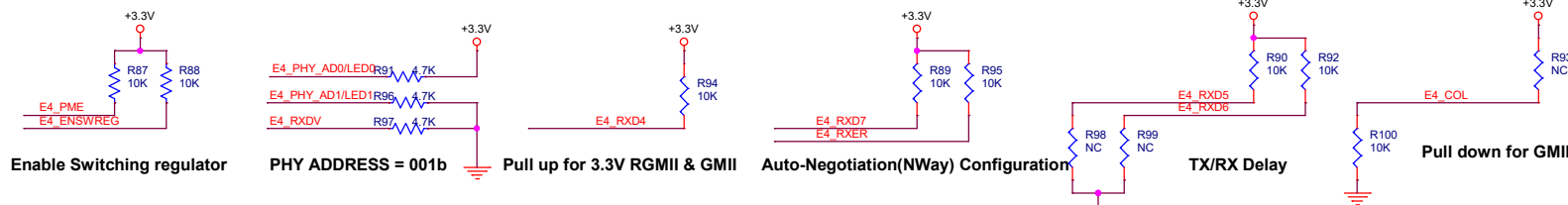
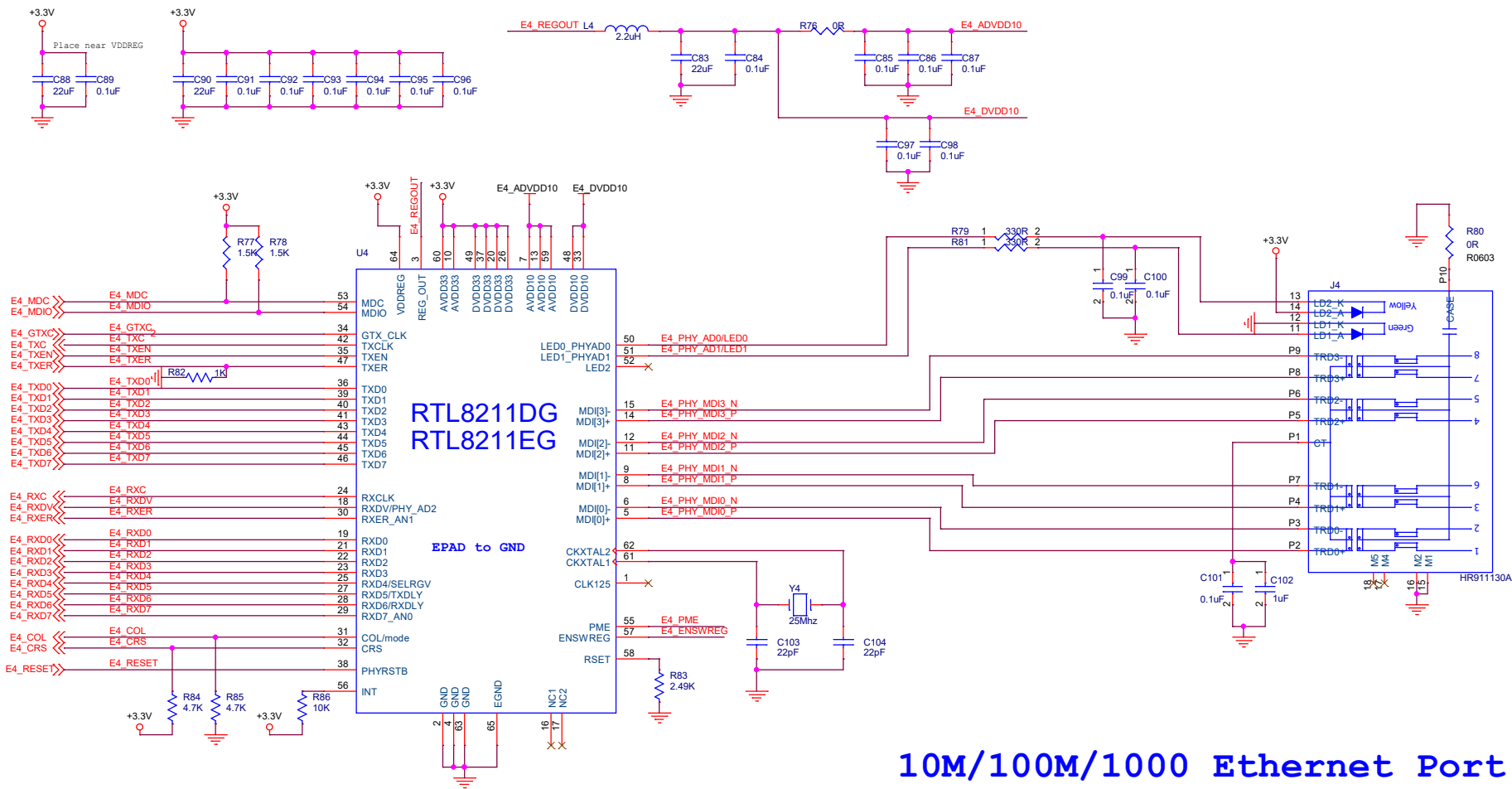


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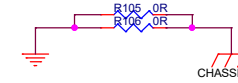
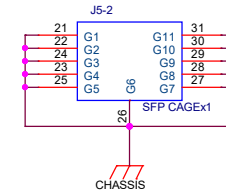
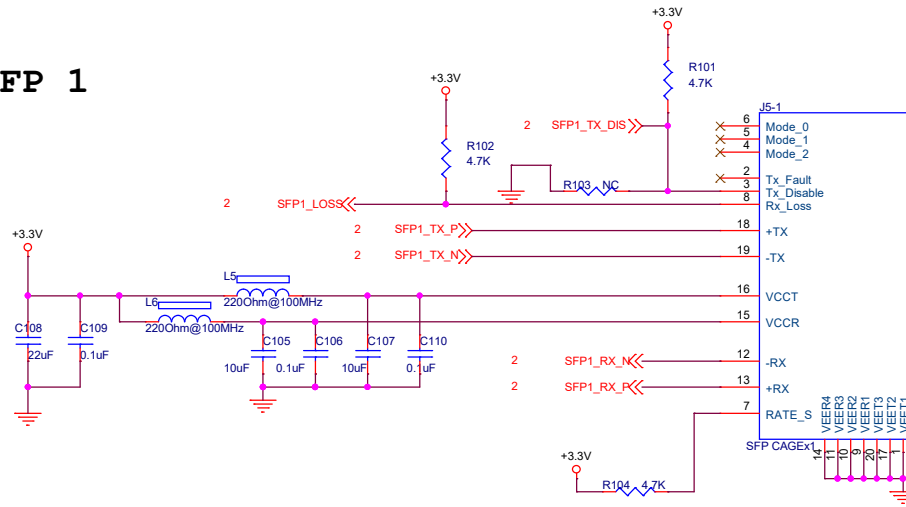




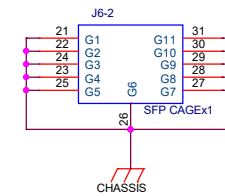
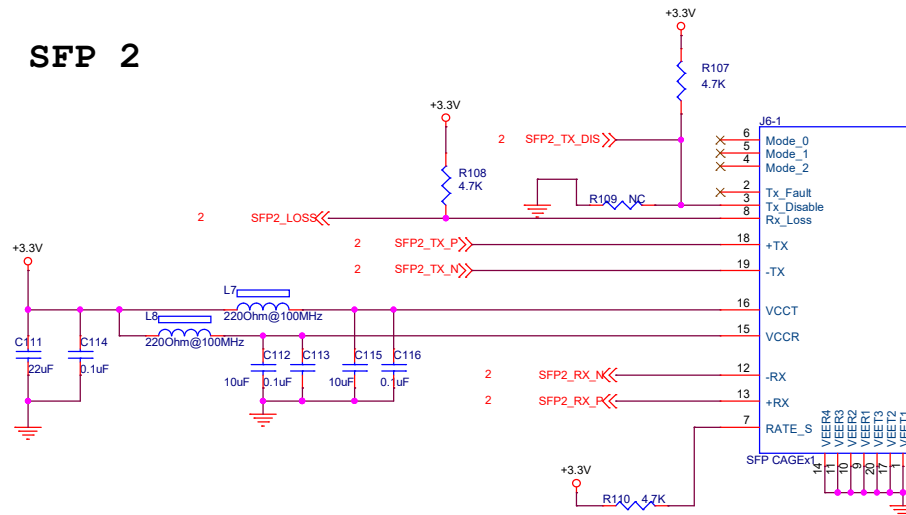
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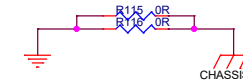
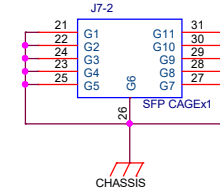
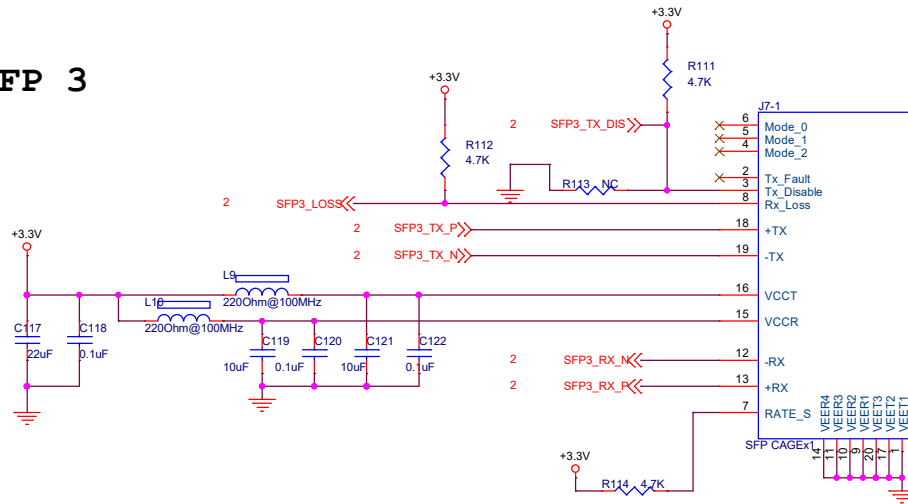
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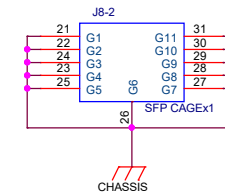
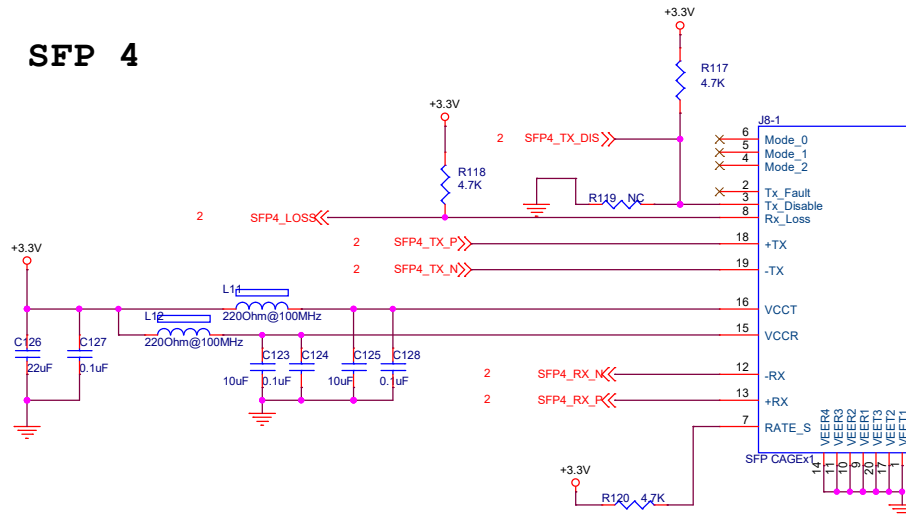
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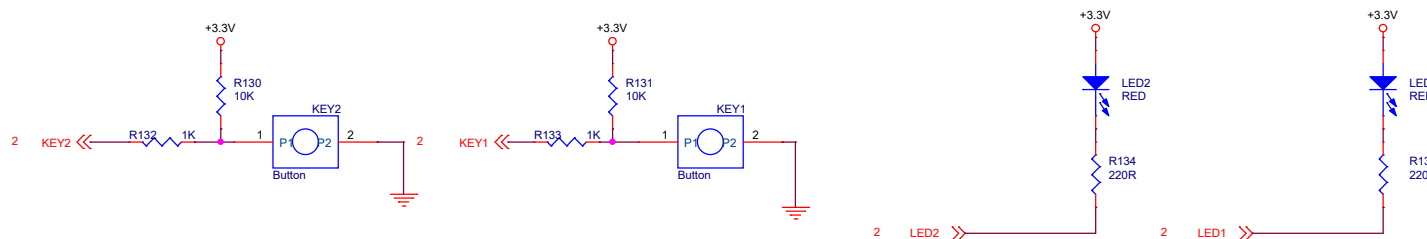
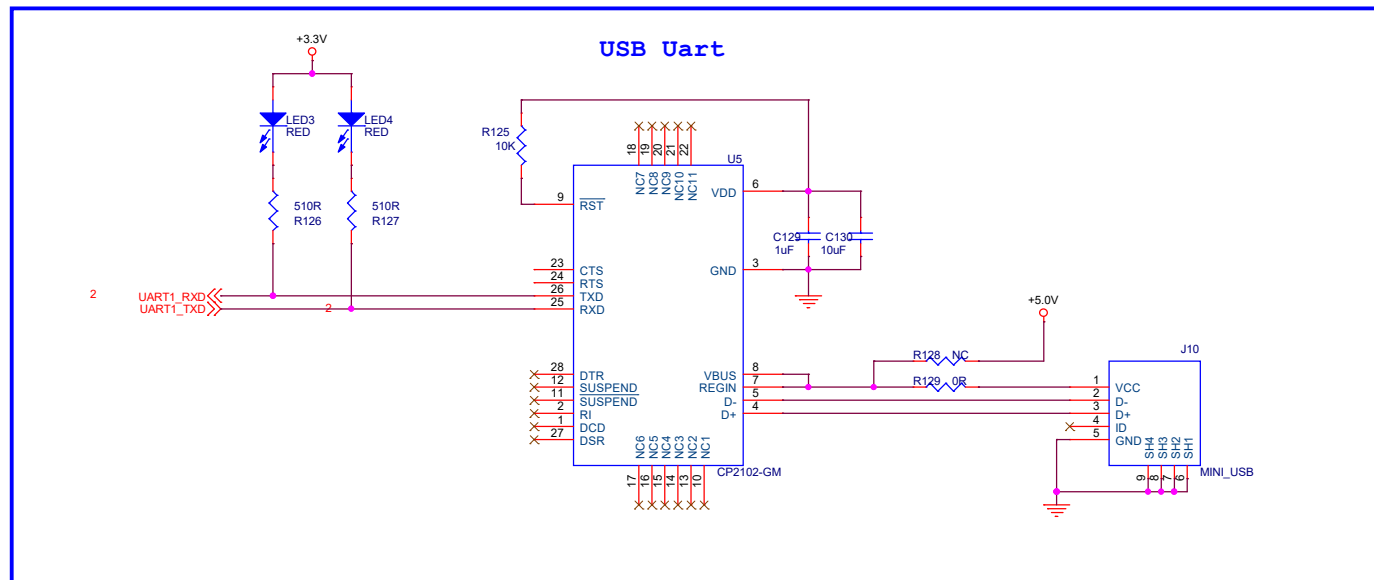
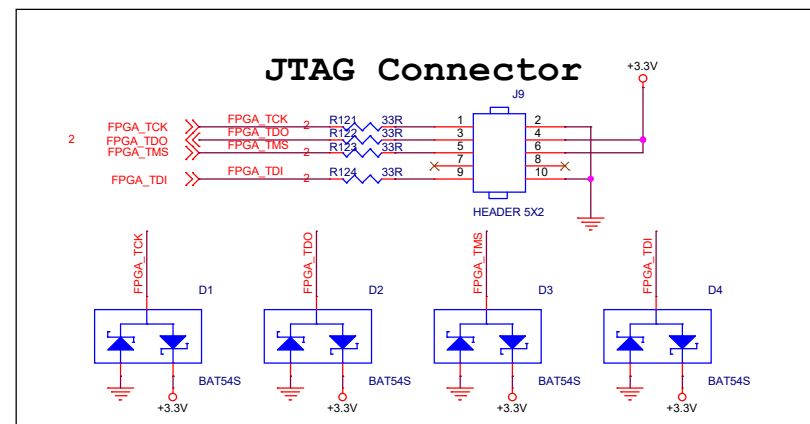


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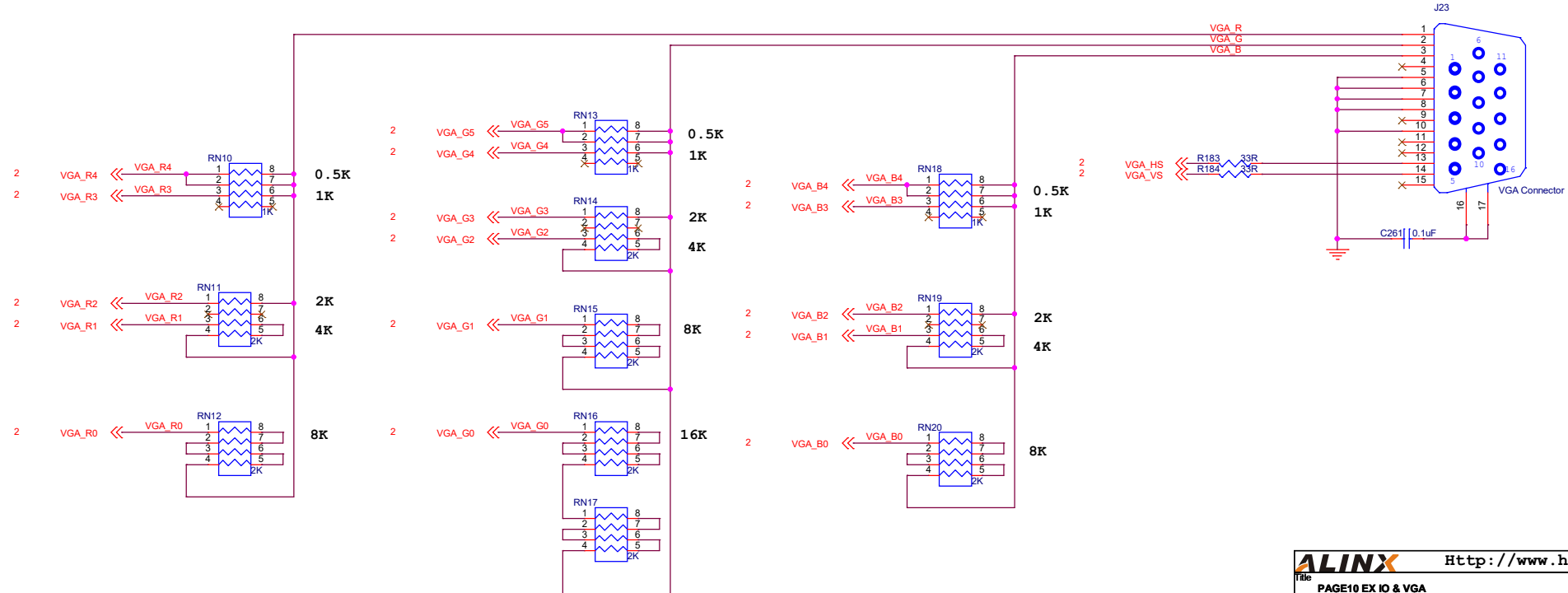
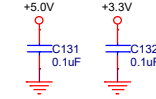
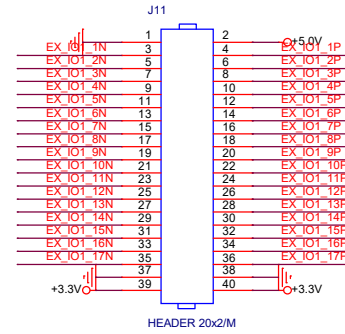
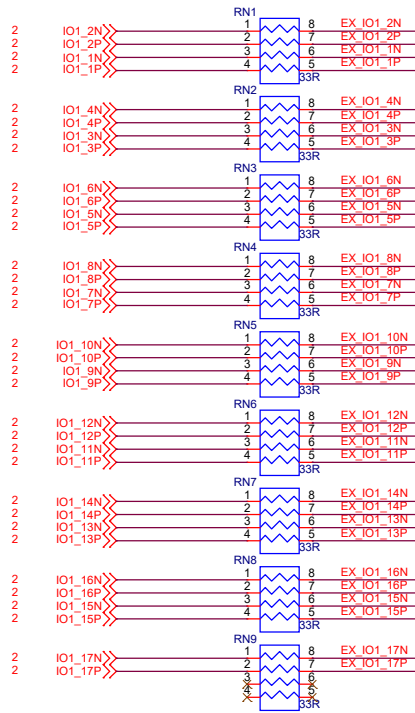


SFP 4



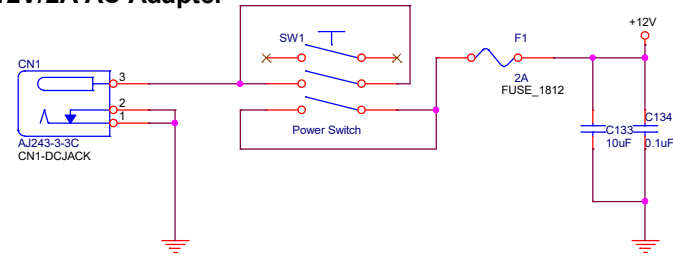


FPGA 40 PIN External IO

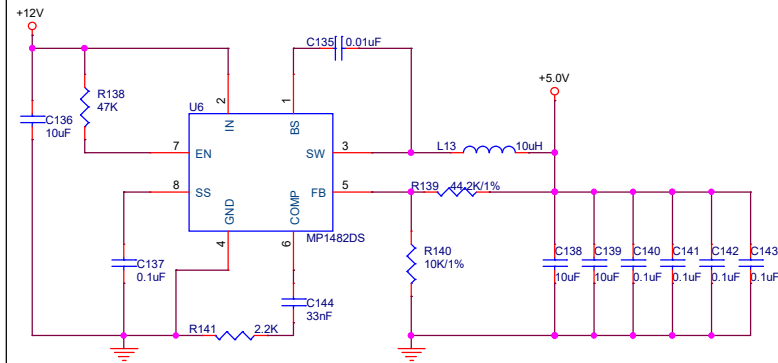


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12V/2A AC Adapter



+5.0V POWER



+3.3V POWER

