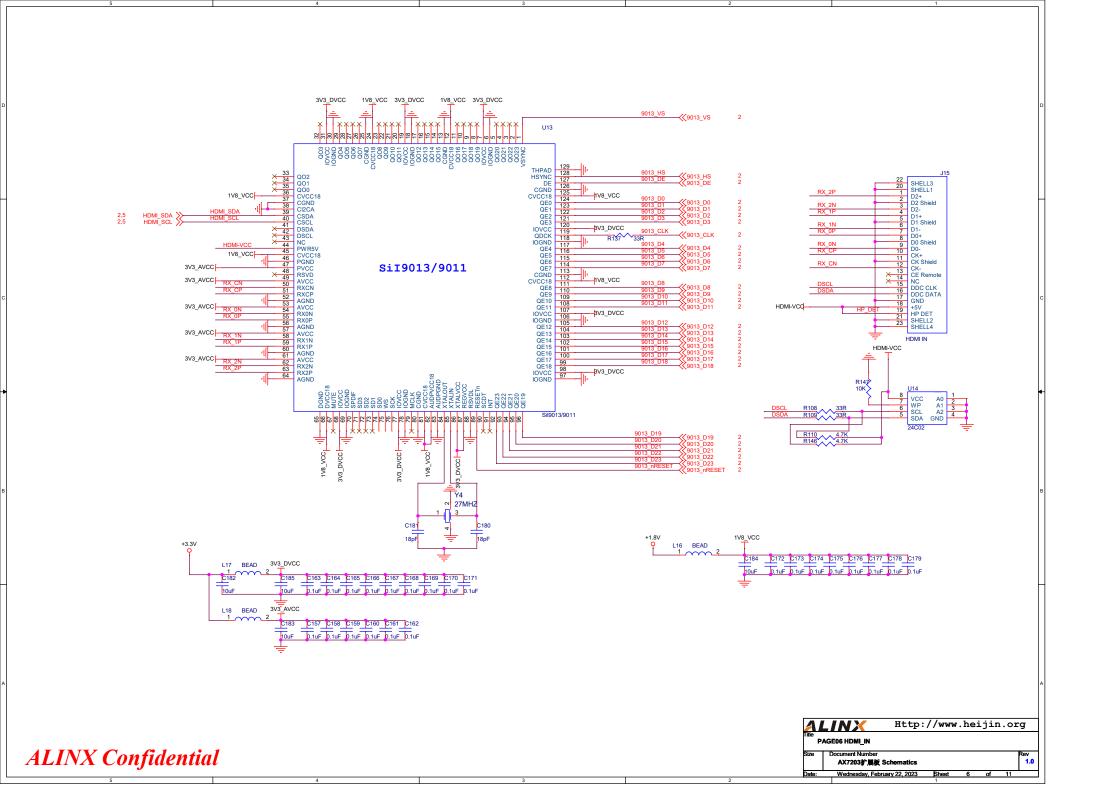
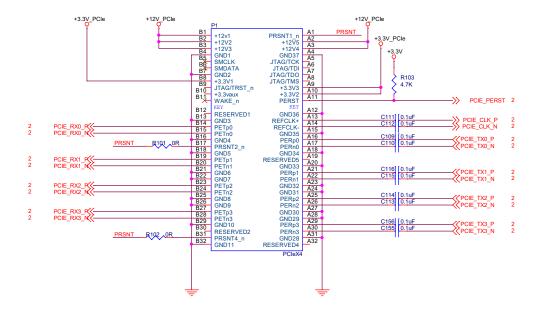


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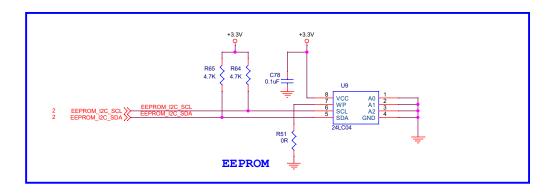
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AX7203計順板 Schematics Rev
1.0
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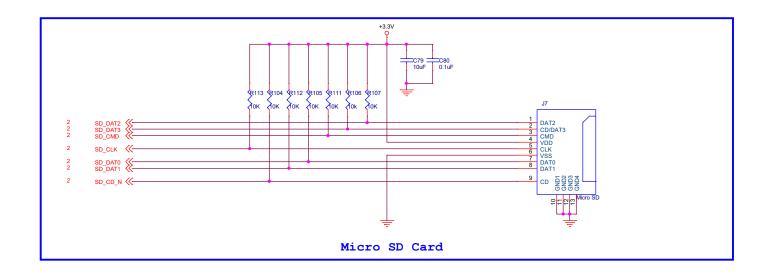


PCIE X4 SLOT



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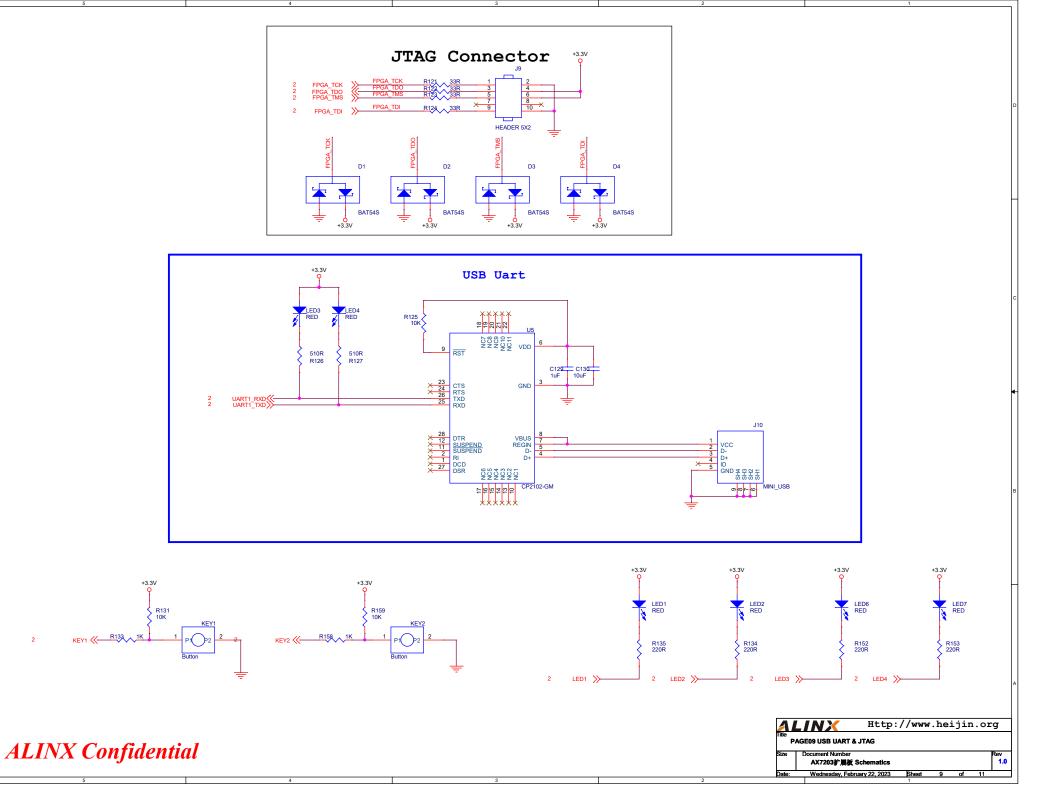


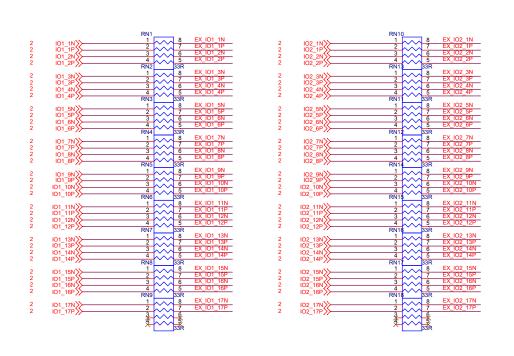


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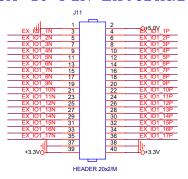
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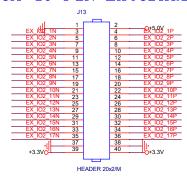




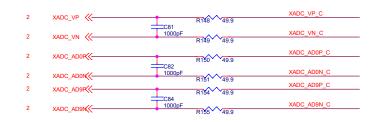
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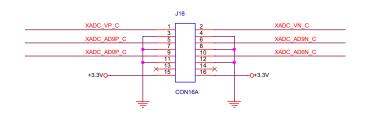


FPGA 40 PIN External IO



XADC Connector





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