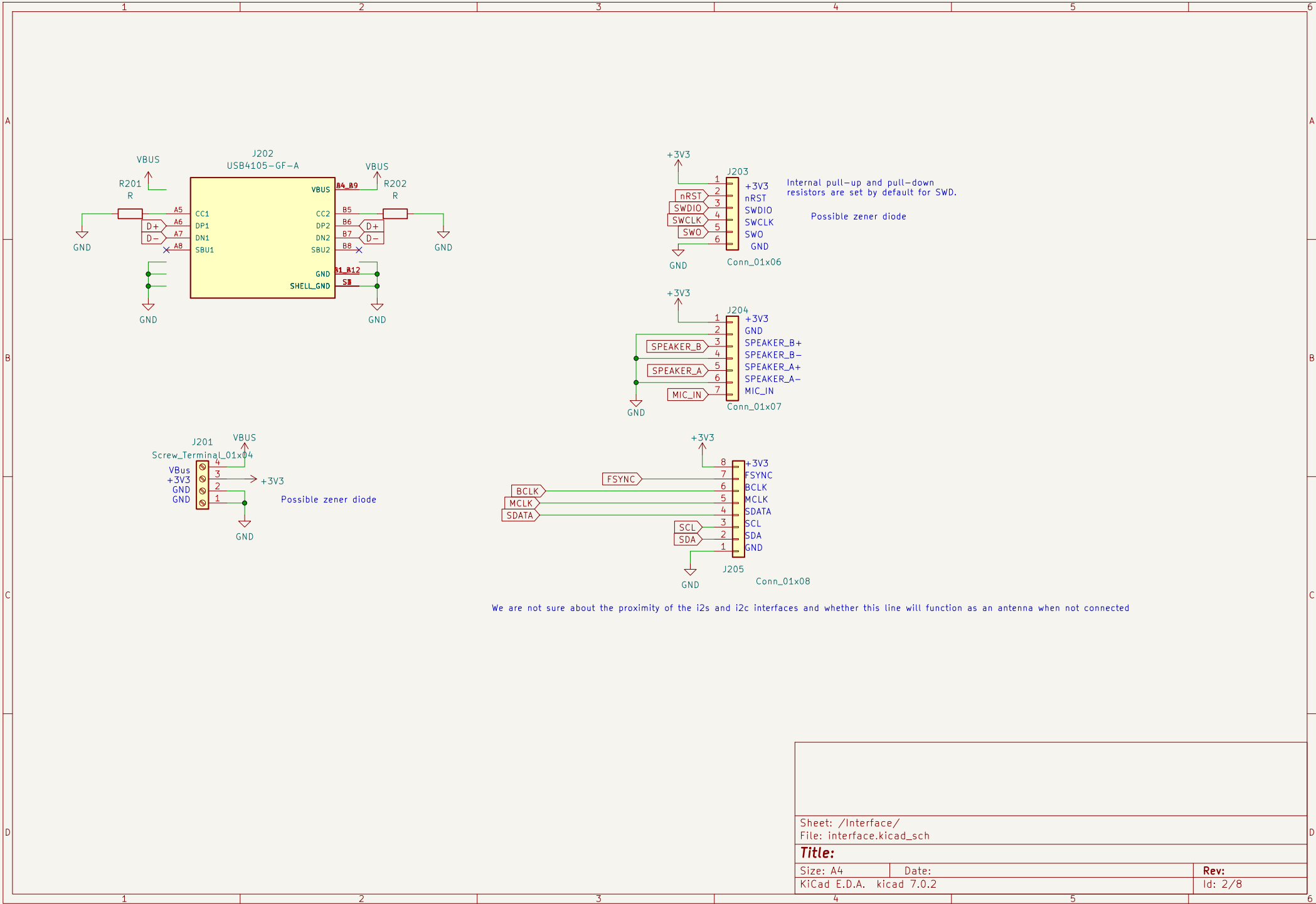
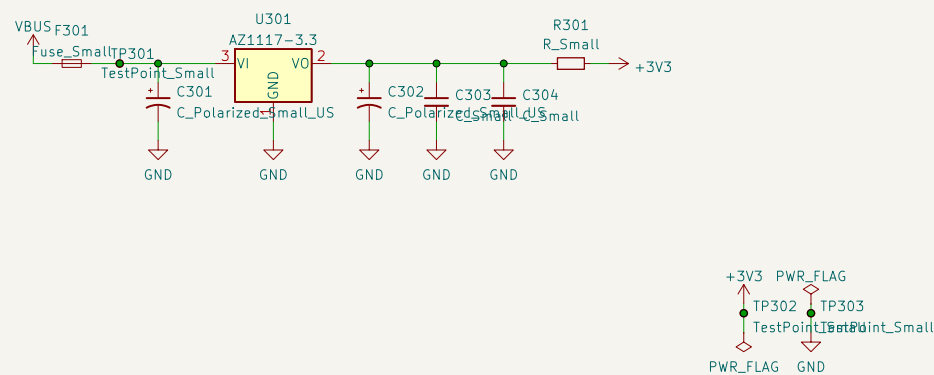
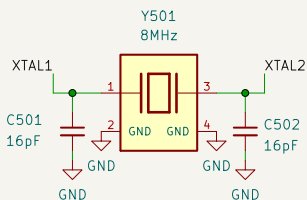
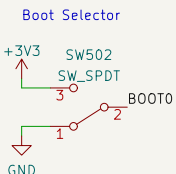
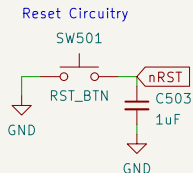


Designers: Venkadesh Eswaranandam, Kevin Lie–Atjam, Luke Nonas–Hunter, Wesley Soo–Hoo Global Health Affordable Design and Entrepreneurship Olin College of Engineering		
Sheet: / File: oae.kicad_sch		
Title: OAE		
Size: A4	Date: 2022–10–19	Rev: 0
KiCad E.D.A. kicad 7.0.2		Id: 1/8

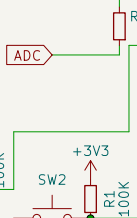
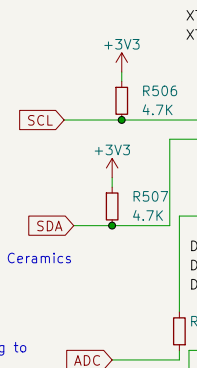
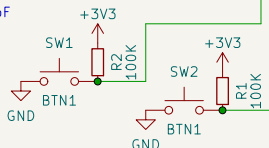




Sheet: /Power/ File: power.kicad_sch		
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Size: A4	Date:	Rev:
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Assume stray capacitance equals 10pF according to stm32 datasheet (pg. 119)
18 pF from oscillator datasheet (pg. 1)
Calculation from ST AN2867 (pg. 12)
 $cl = (cl1 * cl2) / (cl1 + cl2) + cs$
 $18pF = (cl1 * cl2) / (cl1 + cl2) + 10pF$
 $cl1 = cl2 = 16pF$



NOTE: See bypass caps in the next sheet

nRST is active low, with an internal pull up

Bypass Capacitors for Microcontroller

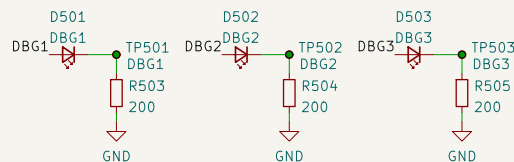
File: bypass_micro.kicad_sch

PA4 and PA5 are DAC1_OUT1 and DAC1_OUT2

SWDIO and SWCLK pins are fine to be on the same register as the DAC because they are only active when programming the microcontroller.

Check to see if we can ground while device is in use

Termination resistors (SDATA resistor on ADC because it is transmitting that signal)



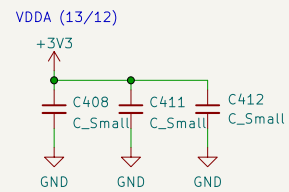
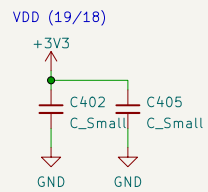
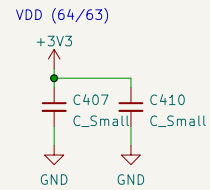
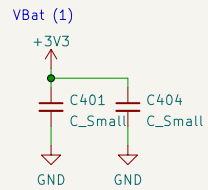
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Title:

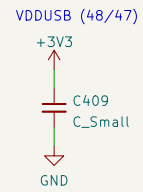
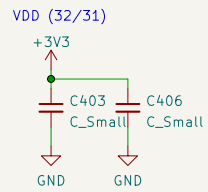
Size: A4
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Page 86 of the data sheet states:
Each power supply pair (V DD /V SS, V DDA /VSSA etc.) must be decoupled with filtering ceramic capacitors. These capacitors must be placed as close as possible to, or below, the appropriate pins on the underside of the PCB to ensure the good functionality of the device.



Layout note: Make sure these bypass caps are physically close to the noted pin

Sheet: /Microcontroller/Bypass Capacitors for Microcontroller/
File: bypass_micro.kicad_sch

Title:

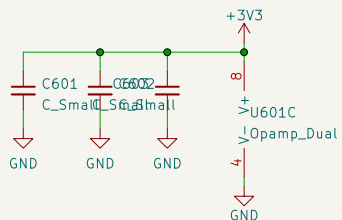
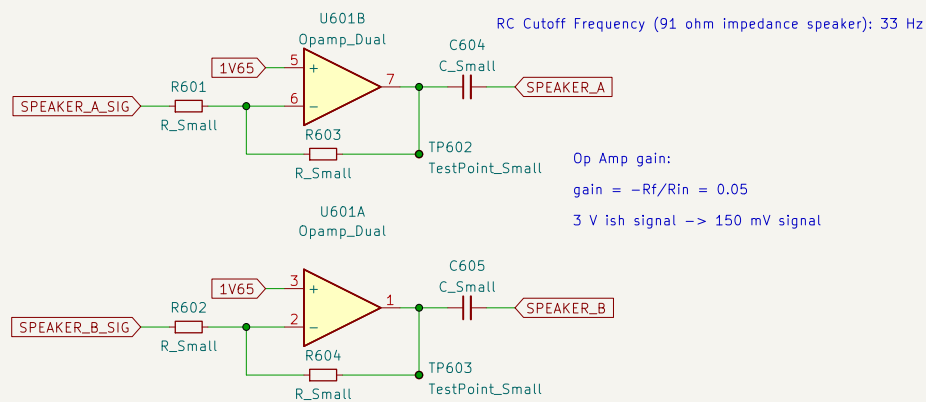
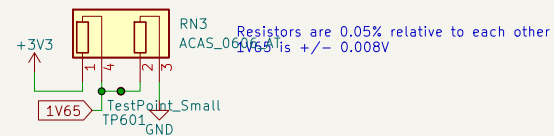
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Sheet: /Speaker Amps/
 File: speaker_amp.kicad_sch

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