Ji Min Huang

UESTC Chengdu

📶 *+86 159****5969*
✉ *jimagile@gmail.com*
⌨ *hmean.github.com*
🌐 *github.com/hmean*

Job Intention: Software Developer, Chengdu,Shenzhen,Shanghai

Education

2010–2014   **BS**, *School of Communication & Information Engineering*, Chengdu, GPA 3.1/4.0.

Projects

2013.10

MIT XV6 OS Lab.

Self study MIT 6.828 operating systems course lab. Through complement the code in MIT's Unix-like operating system Xv6, and debug on Qemu x86 emulator. Familiarized with PC bootstrap process, bootloader's work, and Unix-like OS storage management.

2012.06

TI Cup Morse Infrared Transceiver.

Designed and implement a morse infrared transceiver, with 2ASK modulation and the distance greater than 20m. Responsible for the digital part design and microcontroller coding. Based on TI microcontroller MSP430, used C language and assembly; implement input detection, data frame encapsulation decapsulation, baseband waveform generate.

2013.06

Digital Stopwatch on FPGA.

Design and implement a digital stopwatch with the accuracy of 0.01 seconds and control feature: start, pause, clear, display separate. Completion of the overall framework and module decomposition(display controller, Display separator, Counters and controller) and submodules interface definition; Complete Verilog implement and testbench coding, Modelsim simulation and verification. board level testing.

2013.07

Courier Notification Handheld Terminal.

Design and implement a Cortex M3-based terminals,with waybill barcode scanning then send arrival notification message automatically. As team leader, Responsible for the overall framework design, the module decomposition. Responsible for the UCOS II port to ARM Cortex M3,used C language and assembly Thumb2. and participate application tasks develop. Used Doxygen generate the project documentation eg: entire system function call graph, data structures, etc.

Skills

Professional abilities

Skilled   Skilled in C programming, good english reading; Familiar with RISC architecture and experience in MSP430 and ARM assembly; Experience in RTOS and knowledge of Linux; Experience in FPGA development, familiar with Verilog; With knowledge of OOP basics.

Tools   Skilled in version control tools Git; Familiar with code documentation tool Doxygen; Familiar with Gcc tool chain, shell programming.

Computers and Network   With knowledge of computer organization, operation system; Familiar with basic data structures and Scheme programing language; With knowledge of network protocols, TCPIP.

Languages · passed CET4 and CET6; good english reading and writing ability; gernal level in oral english communication.

Computer Skills · familiar with Windows, Linux environment; skilled in office software; ability to write simple script maintenance tool.

Honors & Awards

- ○ 2012.05 ME Cup Electronic Contest, First Prize
- ○ 2012.06 TI Cup Electronic Contest, Winner Award
- ○ 2013.09 Remin Scholarship, Third-Prize

Personalities

Hobbies · hiking, swimming, reading, Internet.

Self-Eval · Good practical and self-paced ability, enjoy learning new things, technology fans.