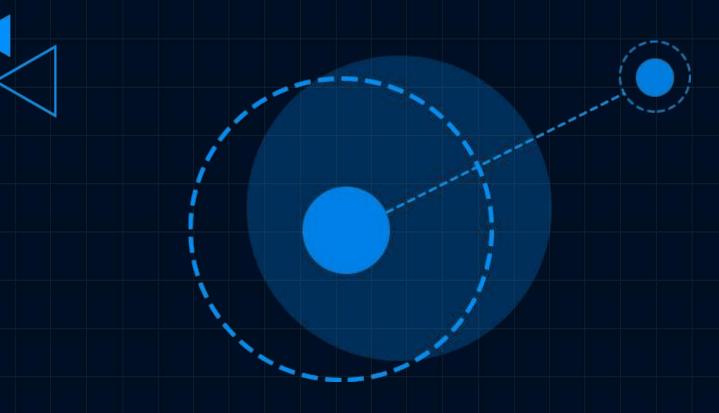


Cloud Firmware in ByteDance



演讲人

聂海涛 字节跳动系统部STE团队 袁 帅 字节跳动系统部STE团队

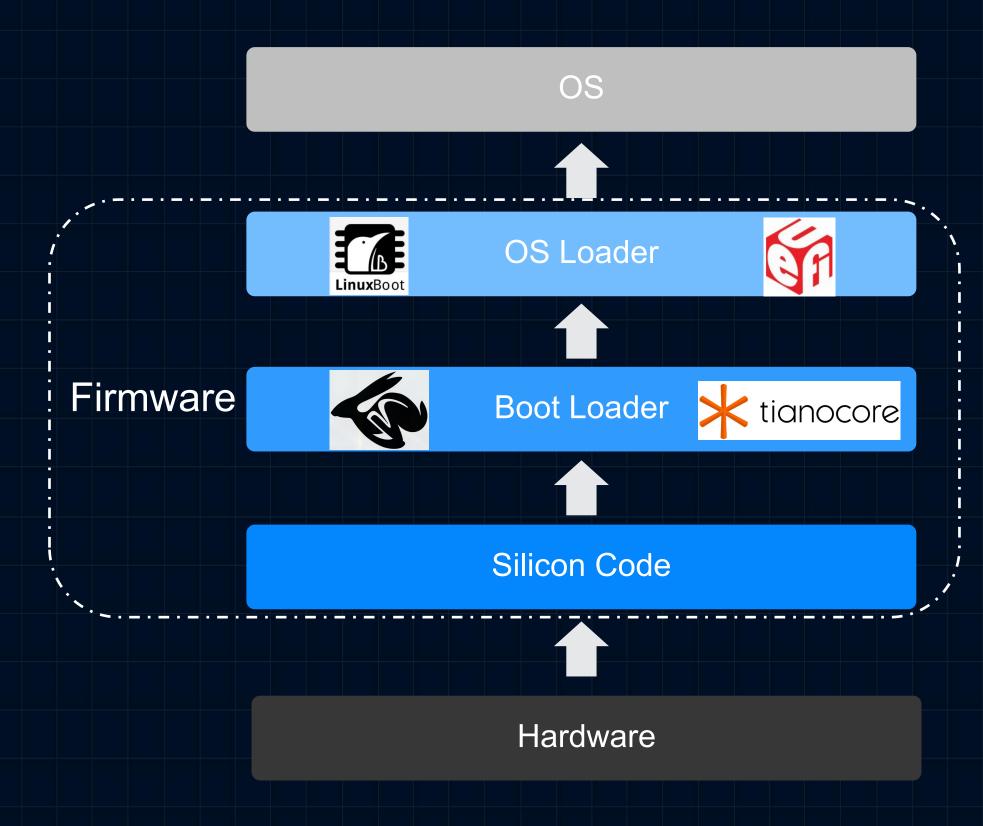








Firmware Components



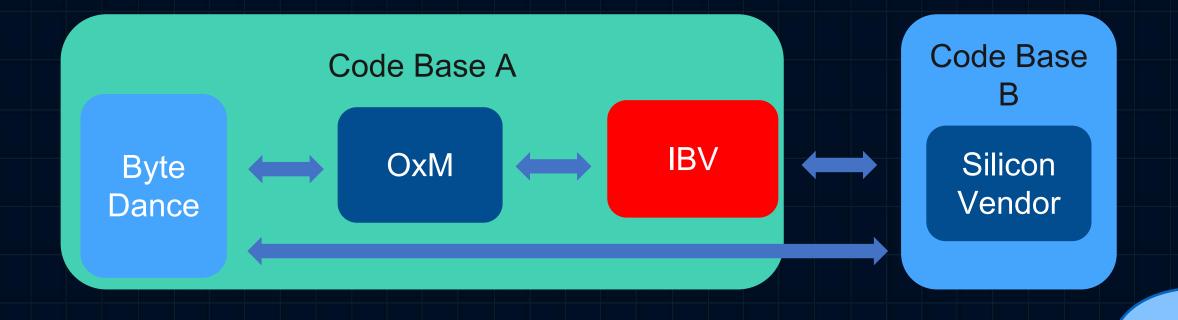
Is UEFI/Tiano good firmware solution right now?

Do we need others?



Tiano/UEFI Firmware Defects

- Comparing Linux community, UEFI community is NOT active
- Can't fix UEFI issue immediately since some key modules are controlled by IBV
- Working Model is NOT efficient.



Hardware got huge improvement:

- More Cores
- More memory
- More Flash Size
- High Frequency
- •

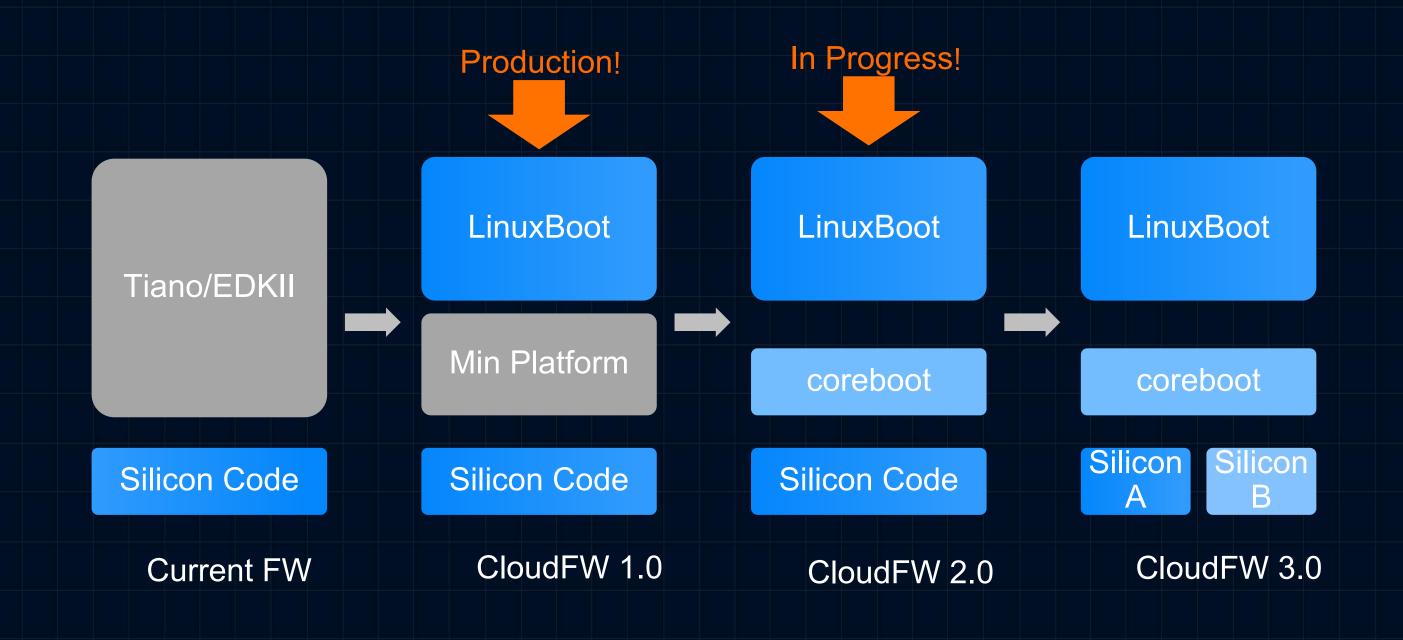


Firmware Eco-system

	UEFI/Tiano	SEC/PEI	DXE		
Current	Intel/AMD	Silicon Code	DXE/BDS		
	Intel Trend	Silicon Code	Tiano/CoreBoot	LinuxBoot	OS
Target					
	FB/Google	Silicon Code	CoreBoot	LinuxBoot	
	Target	Silicon Code	CoreBoot	LinuxBoot	OS



ByteDance Cloud FW Roadmap



	CloudFW 1.0	CloudFW 2.0	CloudFW 3.0
OS Loader	LinuxBoot	LinuxBoot	LinuxBoot
Bootloader	Min Platform	coreboot	coreboot
Silicon Code	Silicon Code	Silicon Code	Silicon Code A&B





Min Platform

Functional Objective

Stage



	Minimal Debug	Ochai port output, source debug chabica,		
Willimar Dobag		hardware debugger enabled		
II	Memory Functional	Basic hardware initialization necessary to reach memory initialization, permanent memory availa		
Ш	Boot to UI	Simple console input and output to a UI, UEFI sh		
IV	Boot to OS	Boot an operating system with the minimally required features		
V	Security Enable	UEFI Secure Boot, TCG measured boot, DMA protections		
VI	Advanced Feature Enable	Firmware update, power management, non-		

essential I/O

Example Capabilities

Serial port output, source debug enabled

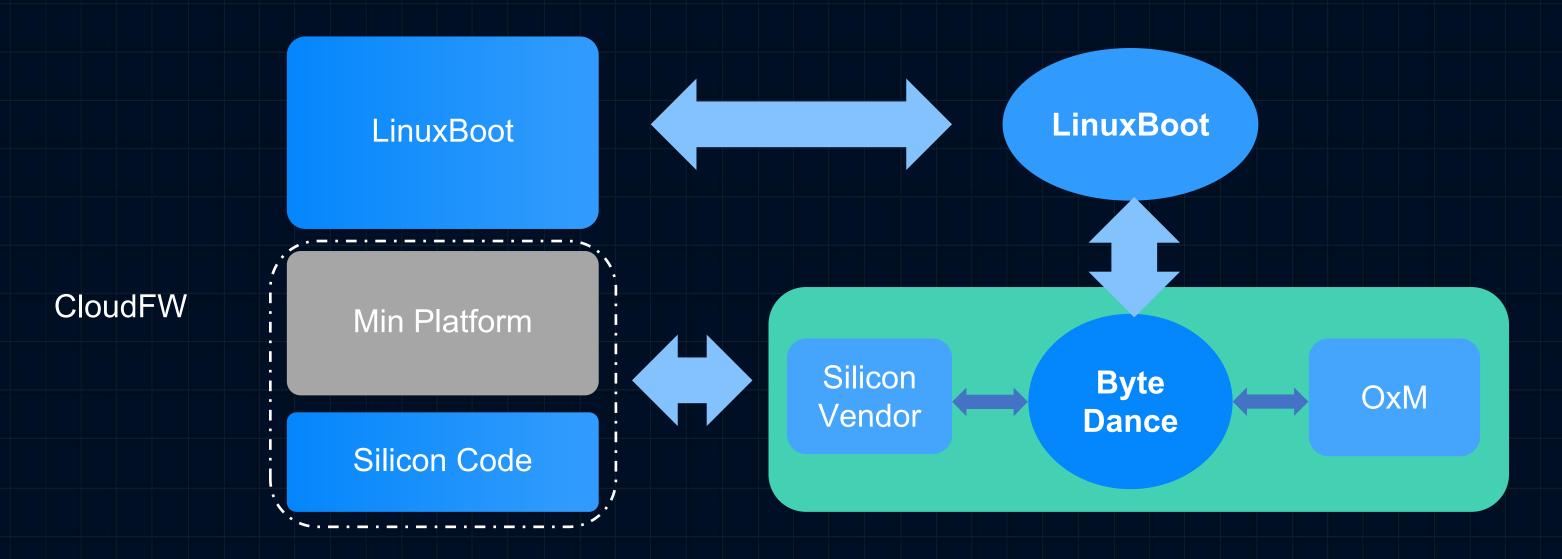
https://github.com/tianocore/edk2-

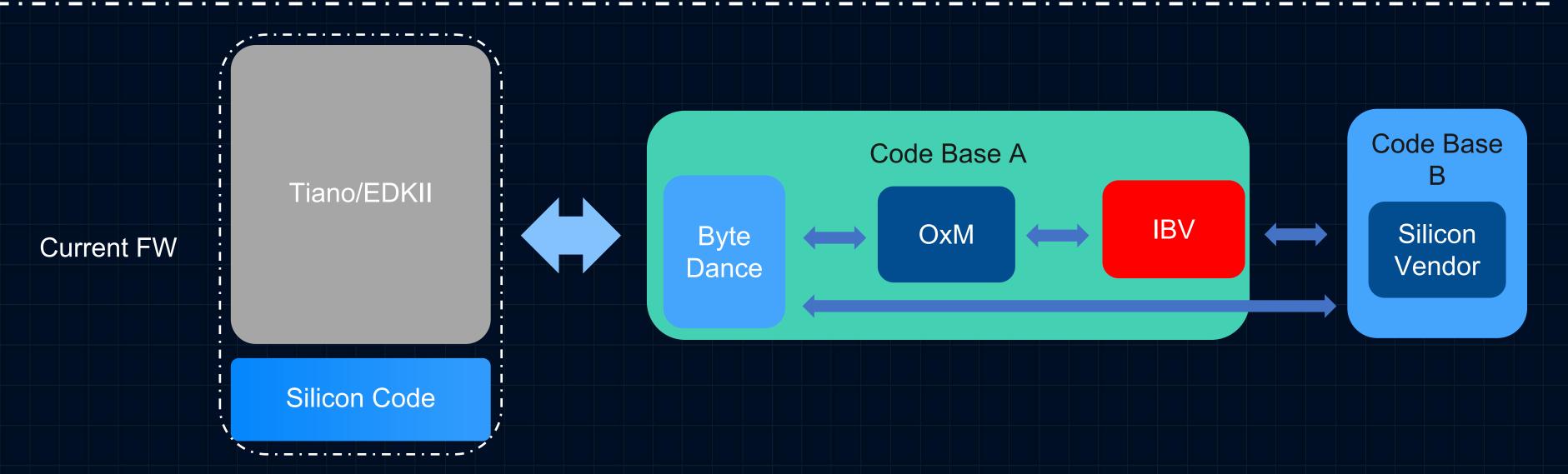
platforms/blob/master/Platform/Intel/Readme.md

https://edk2-docs.gitbook.io/edk-ii-minimum-platform-specification/



Min Platform Working Model









coreboot

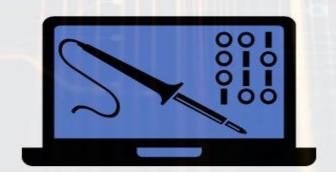


News	Calendar	Documents	Gerrit	Downloads
Job Board	Vandors	Bugtracker	Consulting	Vidoos

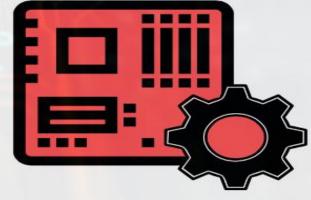
Fast, secure and flexible OpenSource firmware



For End Users



For Developers

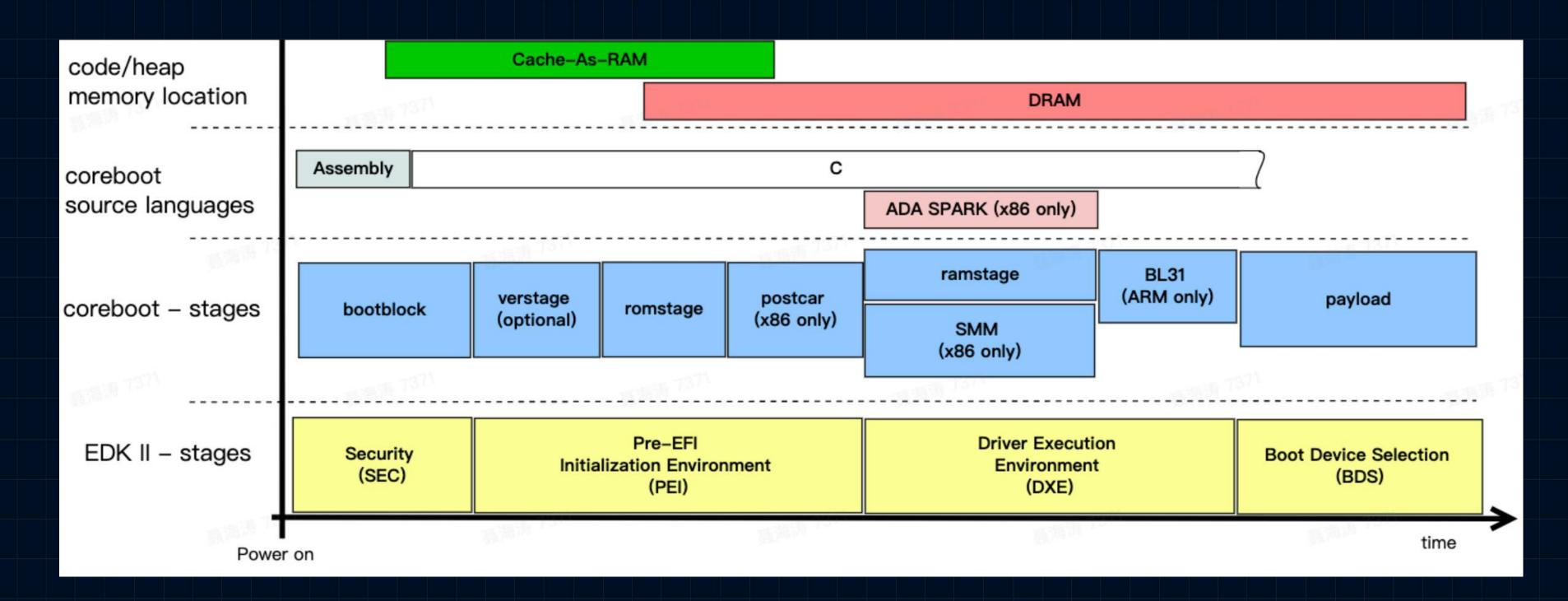


For OEMs/ODMs

https://coreboot.org



coreboot

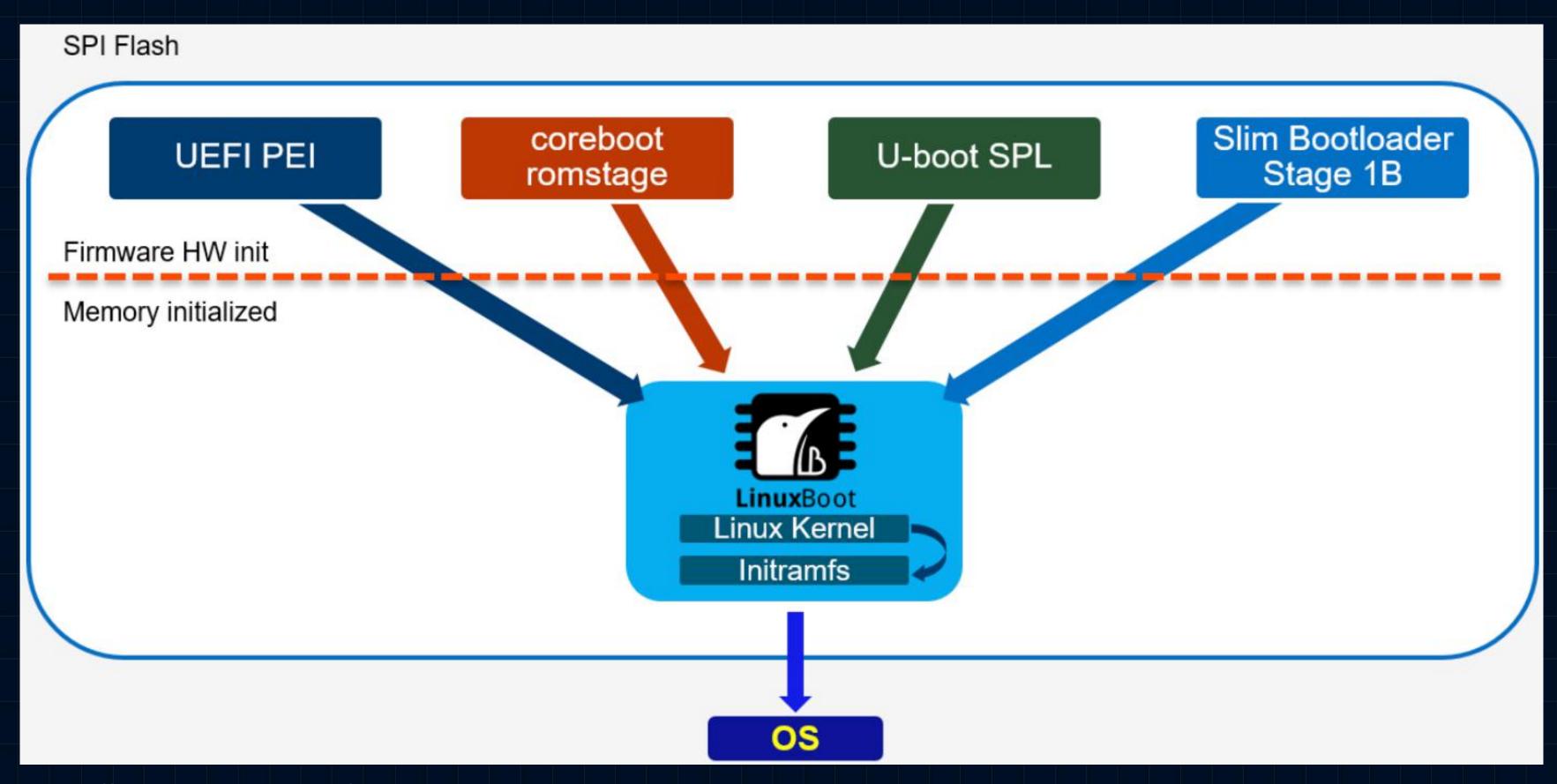


- 1. Bootblock: Set up for a C-environment
- 2. Romstage: Early device init DRAM init
- 3. Postcar: Leave CAR and run code from DRAM load Ramstage
- 4. Ramstage: Main device init (PCI、On-chip device、TPM、Graphics ...)
- 5. Payload: Firmware store SW (LinuxBoot, SeaBIOS, Tianocore ...)





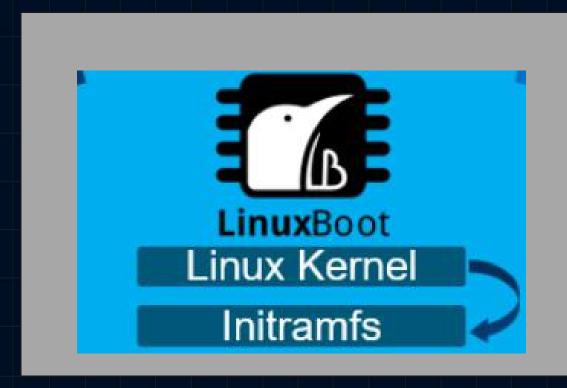
LinuxBoot



https://www.linuxboot.org/



LinuxBoot Content



Kernel	https//github.com/linuxboot/linux
u-root	https://github.com/u-root/
Drivers	Network etc
Application	Customization

Replace and Enhance Firmware Functions:

- PXE、HTTP boot
- Boot Option Manager
- Redfish
- ...

Provide a Diskless Linux Environment:

- Operation&Maintenance components
- System Stress Tools
- •



Key Example in LinuxBoot

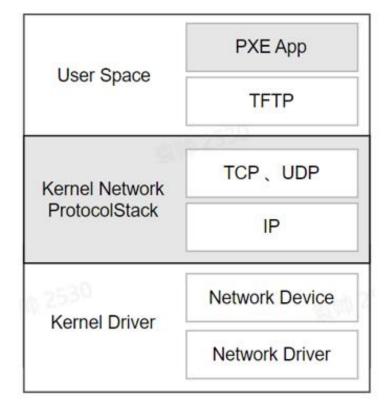
UEFI Disadvantage

- UEFI network stack is not powerful
- Hard to optimize
- •

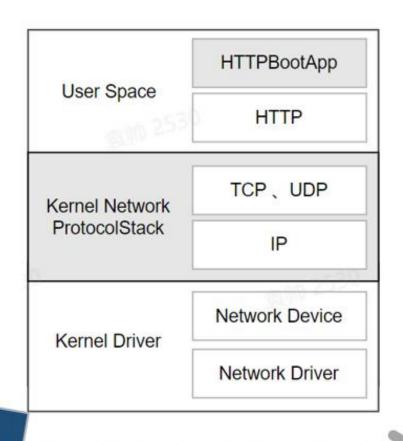
LinuxBoot Advantage

- Linux Network Stack is powerful
- Independent of firmware vendor
- More Linux network experts

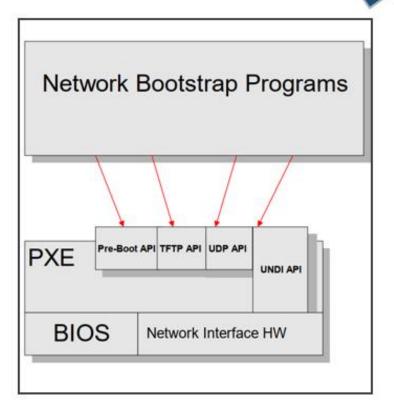
Linux PXE



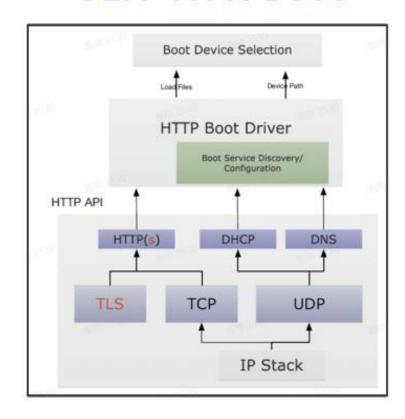
Linux HTTPBoot



UEFI PXE



UEFI HTTPBoot





Summary

	UEFI/Tiano	Min Platform	LinuxBoot	CoreBoot
Eco-system				
community				
Boot time				
Difficulty				

- ByteDance has launched Cloud Firmware 1.0;
- ByteDance is working on Cloud Firmware 2.0 Solution.

Looking forward to seeing more companies collaboration in New Firmware Ecosystem!



Q&A



字节跳动 STE 团队技术交流



该二维码7天内(10月27日前)有效,重新进入将更新



Thanks



