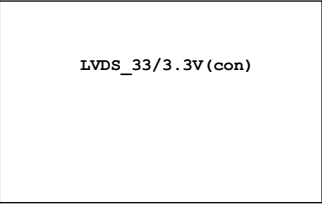
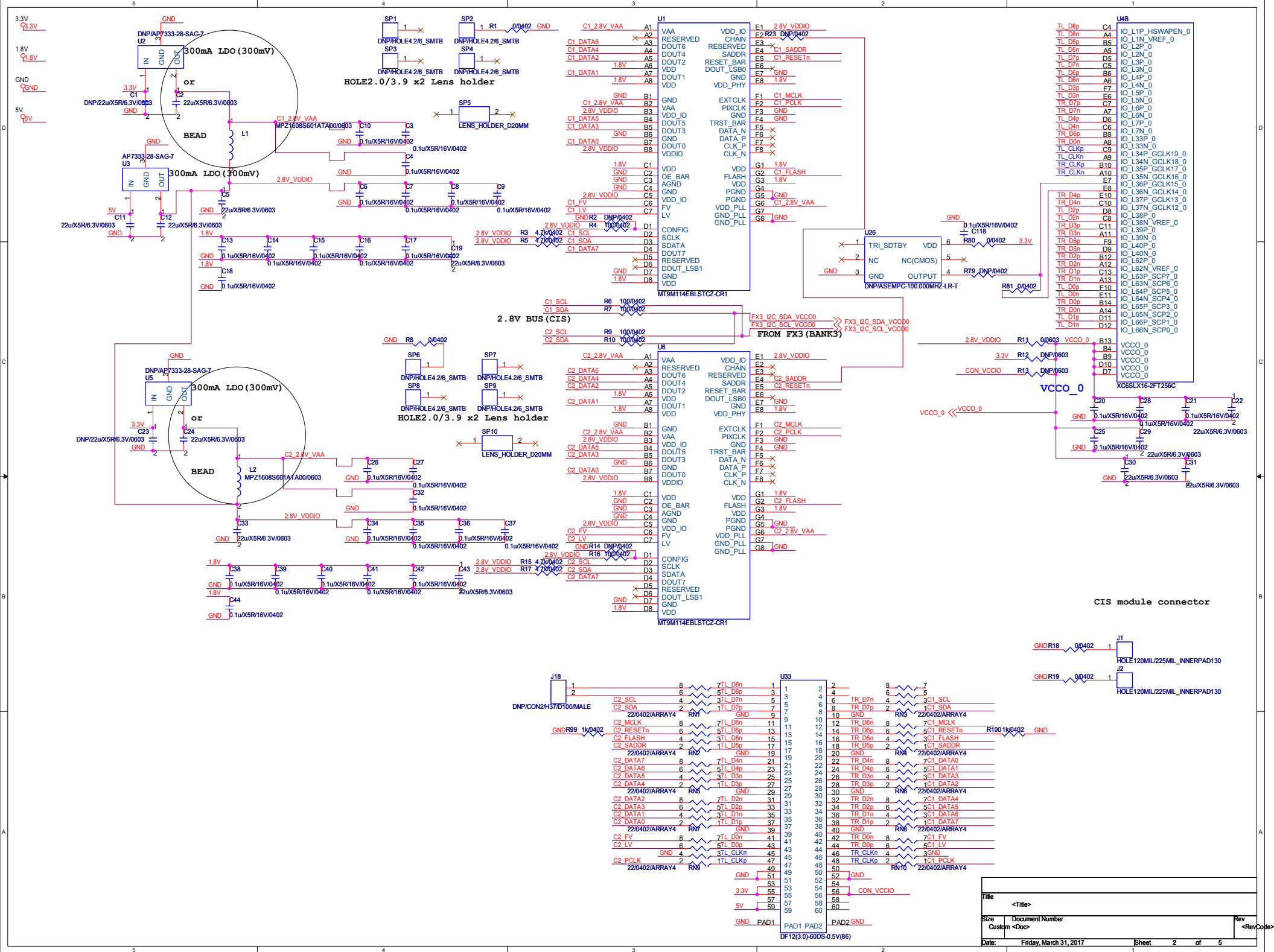
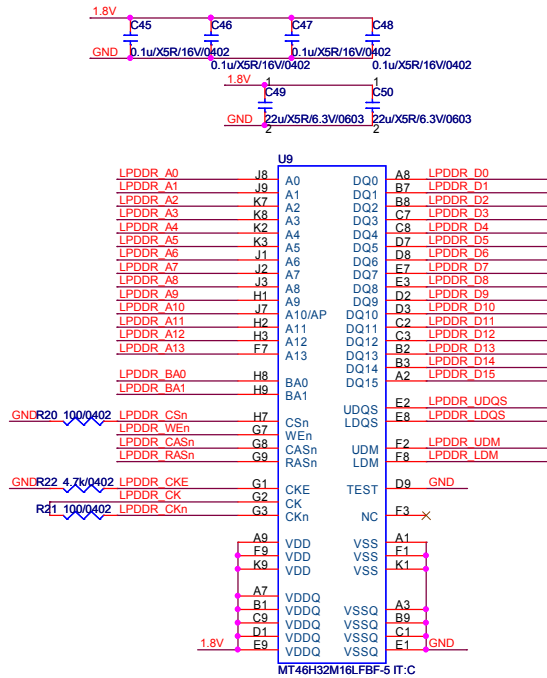


FT(G)256 Package—LX9, LX16, and LX25 I/O Bank Diagram





1.8V
Q1.8V
GND
Q3GND
3.3V
Q3.3V



All the IO resources in an IO tile which contains DQSP/UDQSP are used
irrespective of a single-ended or differential DQS design. Any signal that
is connected to the free pin of the same IO tile in a single-ended design
will be unrouted. Hence, the IOB cannot be used as general purpose IO.
CONFIG PROHIBIT = N16,T15;

#RZQ is required for all MCB designs. Do not move the location #
#of this pin for ES devices. For production devices, RZQ can be moved to any #
#valid package pin within the MCB bank. For designs using Calibrated Input Termination, #
#a 2R resistor should be connected between RZQ and ground, where R is the desired #
#input termination value. Otherwise, RZQ should be left as a no-connect (NC) pin. #

