POLI No :- 131FE 17 Sem-N Exam Seat No: - A 4363 Subject - Digital Electronics Signature: Main que No: - 3 Sub que No: A sur que No:- i) Clarification of Nemonies based on Physical characteristics and Mode of Access: Ansi) RAM:a) RAM is the abbreviated form of Random Access Memory. It is possible to read as well as write data into a RAM, here it is also called as Read write memory 6) The memory bocations can be quickly travel due to the quick memory logics. This is one of the benefits of using RAM over sequestial circuits c) RAMYS are fabricated using Bipolar Transistos or MOSFETS. d) Read and write processes can take place almost at the same time. e) RAMS are further classified into two types: Antic Rams of SRAMS and Dynamic RAMS or DRAMS i) ROM :a) ROM is the short hand of Read only Memory. The data in ROMS is written by the manufacturer as per the user requirements b) Data writter one cannot be an rewriter again there only reading of memory data is possible in ROMS. d) The provers of writing data is much more complex than reading data. d) This drawbook is rectified in EPROM and EPROM. e) EPROM: - EPROM is a type of programmable ROM in which data can be crosed and newritten to the ROM. f) EEPROM: - EEPROM is vertically erasable programmable ROM in which data can be maked sequentially and newritten. (Page 1/3) FOR EDUCATIONAL USE

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	axam Seat No:- A4363 Subject: Digital Electronico Signature:
	Main que No:- 3
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	Sub que No:- ii)
	Complex Programmable Logic Devices:
fa -	1) The standard Programmable Logic Arrays and Programmable Array Dogics
	inputs, product forms and outputs.
	Statuted PLDS like PLAS and PALS can often inputs and ordinal
	and 32 which is very less.
	Herre, we make use of complex Progreammable Logic devices (CPLOS) to preside
	more reacher of product furns, outputs and injus.
19	The Complex Programmable 2001/2 Davices can provide upto 10000 injust &
	output.
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	there is the transfer of the transfer to the box transfer of the
	damage to the strategy of the strategy
eleva v	in the parties of place wines or proper order and are the terminal and another than the
	and the graphest had a styles mondales at the sail of the sail of the
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bati	the state that with a time of the state of t
	a letter Autom 1927 alter management planten, a team a company (see
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	exam Seat No: A4363 Subject Digital Electronics Signature:
	Subject Biginal Lacinovas Signal
	Main que No:- 3
	Sub que No:- B
	Sut que No:- i)
	* Drawbacks of R-2K ladder D/A converter:
14	i) The time conversion for the Digital to molog conversion takes a bot of time and
	is time consuming.
	ii) This drawback can be overcome with the keep of a Modified weighted Resistor D/A
	convertes by applying a known reference and age viril resistance Rret
	0 3
	in) for modified weighted nexistor;
	Jo = I1 + I2 + 1 + In.
	$-1. \ \ J_0 = \ \ V \ 2d^{-1} + V \ 2d^{-2} + \cdots \cdot V \ 2^{-n}d^{-n}.$
	R R R
	$= N \left[2d^{-1} + 2^{-2}d^{-2} + 2 \cdot \cdot \cdot + 2^{-n}d^{-n} \right].$
_	R
	Vo.i.e. output roltage is given by;
	-1. Vo = & To x Rref
	= V. Rret [2d"+2-2d-2++2-d-1].
	Let Rref = K
	$V_0 = KV \left[2d^{-1} + 2^{-2}d^{-2} + \dots + 2^{-n}d^{-n} \right].$
	· V = KV [20 +2 0 + + 2 0]
	(Page 3)3).

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