

# Chinmay Rozekar

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## PROFESSIONAL SUMMARY

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- Software QA Engineer with 5+ years in semiconductor design validation (Siemens EDA, AMD). Experienced in regression automation, SoC validation, and data-driven QA optimization using Python, TCL, and Linux.

## EXPERIENCE

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### Software QA Engineer

July 2024 – present

*Siemens EDA (Mentor Graphics)*

*Wilsonville, OR*

- Designed and maintained detailed test cases for Calibre PERC features, achieving over 90% regression coverage and contributing to quarterly product releases.
- Automated key QA tasks such as test case cloning, export management, and validation log parsing for SVRF, DRC, and LVS tests, reducing manual errors and cutting regression cycle times by 20%.
- Collaborated with developers and QA engineers to troubleshoot and resolve critical product issues, improving test failure diagnostics and reducing defect resolution time by 15% over the project period.
- Implemented standardized workflows to improve test data handling and result validation, resulting in consistent and reproducible test outputs verified on monthly validation cycles.

### Product Development Engineer

July 2020 – March 2024

*Advanced Micro Devices (AMD)*

*Austin, TX*

- Led system-level testing for AMD's Ryzen 8040 'Hawkpoint' series Accelerated Processing Units (APUs), designing and overseeing 100+ test scenarios to test and validate 14 SoC IPs.
- Spearheaded Test Time Reduction (TTR) initiative, eliminating 10% of redundant tests and reducing operational costs by 5% through detailed DPPM Yield Analysis in Production.
- Generated detailed test reports, collected parametric data for voltage, frequency, and thermal plots, leading to a 10% improvement in product reliability over 3 release cycles.
- Analyzed failure logs and debugged issues, creating and tracking JIRA tickets and leveraging Confluence for documentation. Promptly escalated critical issues and collaborated with cross-functional teams across different time zones to ensure swift resolutions.
- Collaborated in SoC design changes and dynamically revised test plan strategies, identifying early-stage software bugs. Proactive contributions led to significant refinements in test content, boosting overall quality and reliability.
- Led defect management efforts, categorizing low Sidd (Static Idd) marginal units from Typical-Typical (TT)/ fast units, and conducted SoC characterization on marginal units to pinpoint stress tests that caused IP failures. Utilized box plots to verify if VF values met product specifications.
- Effectively managed customer RMA issues by diagnosing and resolving the majority of hardware failures in-house, minimizing escalations. Contributed to root cause analyses and documentation in RMA meetings, enhancing product reliability and customer trust.

## TECHNICAL SKILLS

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**Languages:** Shell, TCL/TK, SVRF, Python

**AI/ML:** TensorFlow, PyTorch, Scikit-learn, Pandas, NumPy, SMOTE, Ensemble Methods, Neural Networks, Computer Vision, NLP, RAG, ChromaDB, SentenceTransformers

**Cloud/DevOps:** Docker, Flask, Streamlit, Hugging Face Spaces, CI/CD, Git, MLOps

**Tools:** Vim, VS Code, Jupyter, CVS, Git, Jira, Confluence

**QA:** Test Automation, SVRF, DRC, LVS, Regression Testing, Calibre PERC

**Hardware:** JTAG, PCIE, DFT, Boundary SCAN, ATPG, System-Level Testing, SoC Validation

**Simulation/Design:** Calibre PERC

**Yield Analysis:** Statistical Process Control, Cp/Cpk, Box Plots, Heat Maps, Parametric Analysis, R

### Data Science

#### *FoodHub - Delivery Business Intelligence System*

*Jan 2025 – Feb 2025*

- Conducted comprehensive data analysis of 1,898 food delivery orders across 178 NYC restaurants using Python (pandas, matplotlib, seaborn), identifying \$6,166 in commission revenue and delivering 8 specific business recommendations that could reduce average delivery time by 21% (from 28.34 to 22.47 minutes) and potentially increase customer feedback rates from 61% to 85% through targeted engagement strategies
- **Repository:** [github.com/chinmayrozekar/PGPAIML\\_UT\\_Austin](https://github.com/chinmayrozekar/PGPAIML_UT_Austin)

### Artificial Intelligence & Machine Learning

#### *Personal Loan Campaign*

*Feb 2025 – March 2025*

- Built machine learning classification model for personal loan targeting using Python (scikit-learn, pandas) on 5,000 customer dataset, achieving 99.3% recall with post-pruned decision tree that identified income > \$92.5K as primary predictor, enabling targeted campaigns to improve conversion from 9.6% baseline
- **Repository:** [github.com/chinmayrozekar/PGPAIML\\_UT\\_Austin](https://github.com/chinmayrozekar/PGPAIML_UT_Austin)

#### *EasyVisa - US Visa Approval Prediction*

*March 2025 – May 2025*

- Developed ensemble machine learning models for US visa approval prediction using 25,480 applications with 12 features, implementing Gradient Boosting with SMOTE oversampling to achieve 73.6% accuracy and 81.1% F1-score, identifying education level and prevailing wage as key approval factors for OFLC certification process optimization
- **Repository:** [github.com/chinmayrozekar/PGPAIML\\_UT\\_Austin](https://github.com/chinmayrozekar/PGPAIML_UT_Austin)

#### *Neural Network-Based Customer Churn Prediction*

*March 2025 – May 2025*

- Built deep neural network models for bank customer churn prediction using TensorFlow on 10,000 customer records, implementing SMOTE oversampling and comparing SGD vs Adam optimizers to achieve optimal recall performance for early churn detection and proactive customer retention strategies
- **Repository:** [github.com/chinmayrozekar/PGPAIML\\_UT\\_Austin](https://github.com/chinmayrozekar/PGPAIML_UT_Austin)

#### *Natural Language Processing RAG-powered medical AI assistant*

*May 2025 – June 2025*

- Developed RAG-based medical AI assistant using Mistral-7B LLM and 4,000+ page medical manual, implementing document chunking, vector embeddings (SentenceTransformers), and ChromaDB to achieve high accuracy and reduced hallucinations for healthcare decision support, with LLM-as-judge evaluation showing superior performance over baseline models
- **Repository:** [github.com/chinmayrozekar/PGPAIML\\_UT\\_Austin](https://github.com/chinmayrozekar/PGPAIML_UT_Austin)

### Computer Vision

#### *HelmNet: AI Powered Helmet Detection System*

*June 2025 – July 2025*

- Developed computer vision safety monitoring system using VGG-16 transfer learning and CNN architectures on 631 workplace images, implementing data augmentation and achieving high accuracy for automated helmet detection to enhance workplace safety compliance in construction and industrial environments
- **Repository:** [github.com/chinmayrozekar/PGPAIML\\_UT\\_Austin](https://github.com/chinmayrozekar/PGPAIML_UT_Austin)

### Pipeline

#### *End-to-End ML Pipeline Project*

*July 2025 – Aug. 2025*

- Deployed production-ready MLOps solution for SuperKart retail forecasting using Flask REST API backend and Streamlit frontend, containerized with Docker and hosted on Hugging Face Spaces, serving real-time sales predictions through scalable microservices architecture processing 8,763+ transaction records with 66.8% model accuracy for quarterly inventory planning.
- **Repository:** [github.com/chinmayrozekar/PGPAIML\\_UT\\_Austin](https://github.com/chinmayrozekar/PGPAIML_UT_Austin)

## Academic Projects

### ***Thin Film Technologies for Flexible Electronics***

*Jan 2020 – May 2020*

- Conducted comprehensive review of thin film materials and fabrication techniques for next-generation flexible electronic devices, focusing on material properties and integration challenges.

### ***MEMS Silicon Micro-robot***

*May 2018 – Dec 2018*

- Designed and fabricated thermally actuated MEMS micro-robot using CAD/SolidWorks design and COMSOL multi-physics simulation for heat transfer analysis.

## EDUCATION

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### **University of Texas at Austin**

Austin, TX

*Post Graduate Program in Artificial Intelligence and Machine Learning*

*Jan. 2025 – Sept. 2025*

### **Rochester Institute of Technology**

Rochester, NY

*Master of Science, Electrical Engineering*

*Aug. 2017 – May 2020*

### **Bharati Vidyapeeth University**

Pune, India

*Bachelor of Technology, Electrical Engineering*

*Aug. 2012 – June 2016*