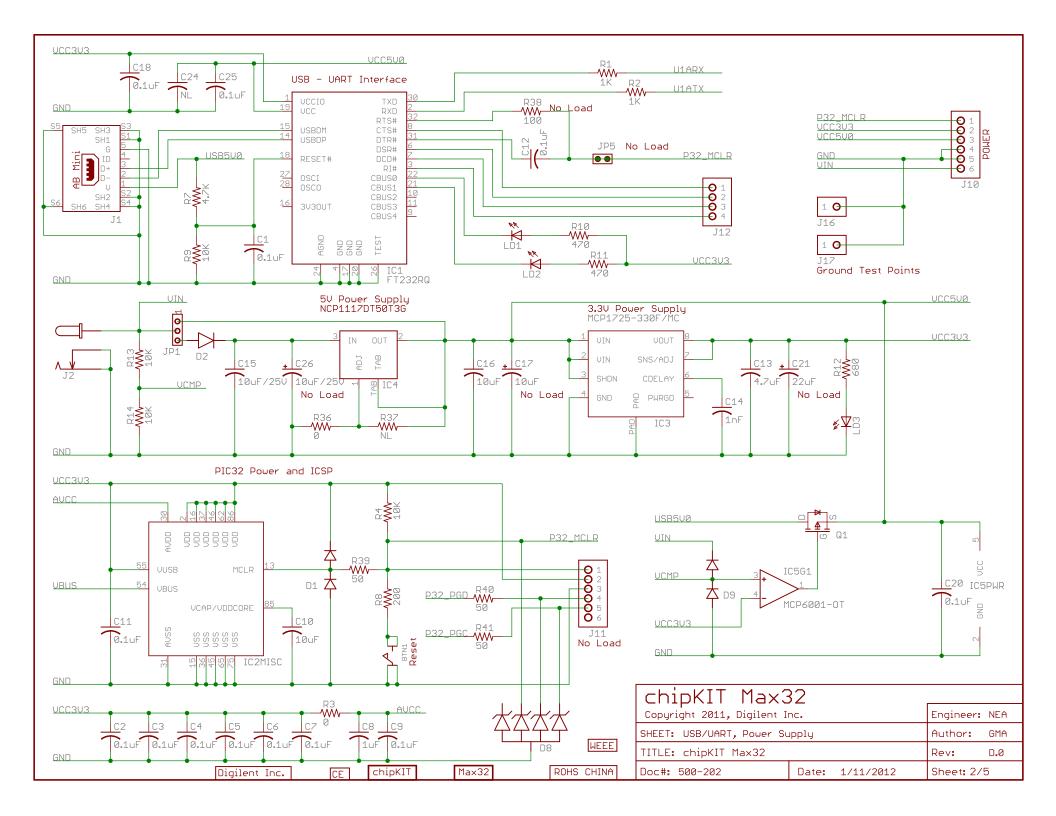
chipKIT™ Max32™ Reference Design

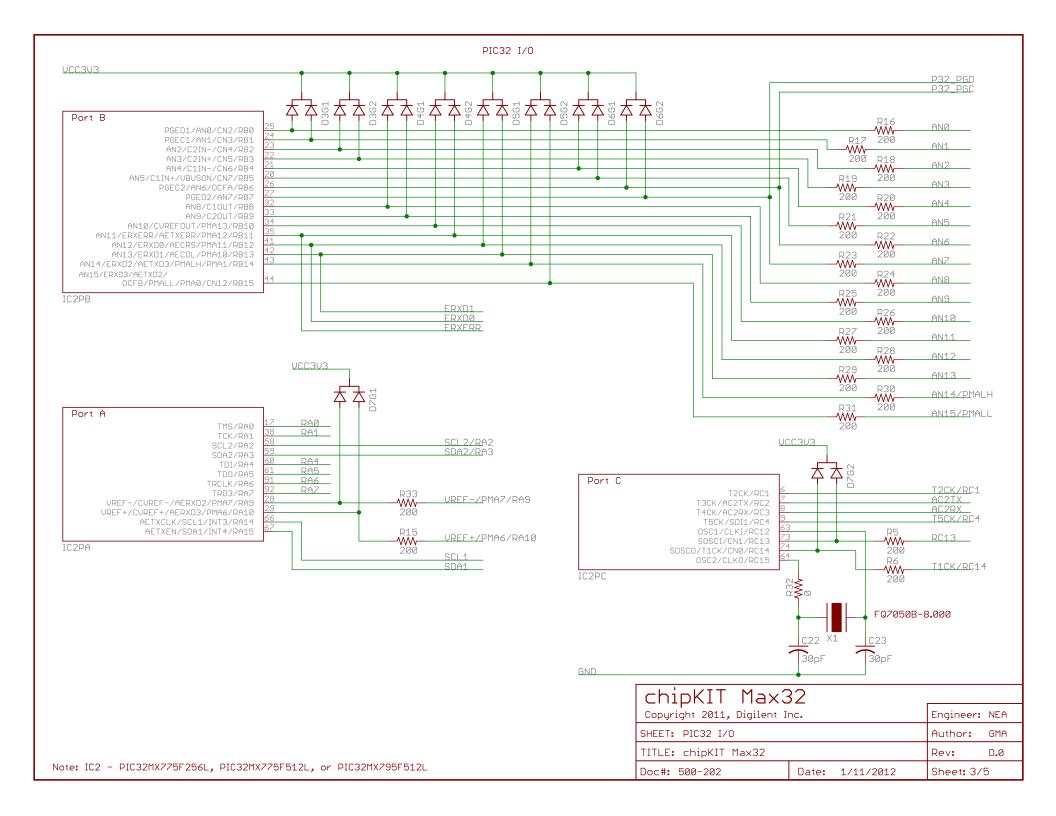
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chipKIT Max32					
l			Engineer:	Engineer: NEA	
SHEET: License Agreement		Author:	GMA		
TITLE: chipKIT Max32		Rev:	D.0		
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PIC32 I/0

Port D SD01/0C1/INT0/RD0 0C2/RD1	72 76	0C1/RD0 0C2/RD1
0C3/RD2 0C4/RD3	77 78 81	OC3/RD2 OC4/RD3 OC5/PMWR/RD4
OC5/PMWR/CN13/RD4 PMRD/CN14/RD5 ETXEN/PMD14/CN15/RD6	82 83	PMRD/RD5 ETXEN/RD6
ETXCLK/PMD15/CN16/RD7 RTCC/EMDIO/AEMDIO/IC1/RD8	84 RDZ 68	EMDIO.
SS1/IC2/RD9 SCK1/IC3/PMCS2/PMA15/RD10 EMDC/AEMDC/IC4/PMCS1/PMA14/RD11	69 RD9 70 71	PMCS2 EMDC/PMCS1
ETXD2/IC5/PMD12/RD12 ETXD3/PMD13/CN19/RD13 AETXD0/SS1A/U18RX/U1ACTS/CN20/RD14	79 80 RD13 47	IC5 U1BRX
AETXD1/SCK1A/U1BTX/U1ARTS/CN21/RD15	48	U1BTX

IC2PD

Port E	
PMDØ/REØ PMD1/RE1 PMD2/RE2	93 PMD0 94 PMD1 98 PMD2 99 PMD3
PMD3/RE3 PMD4/RE4 PMD5/RE5	100 PMD4 3 PMD5 4 PMD6
PMD6/RE6 PMD7/RE7 AERXD0/INT1/RE8 AERXD1/INT2/RE9	5 PMDZ 18 INT1/RE8 19 INT2/RE9

IC2PE

Port F

C1RX/ETXD1/PMD11/RF0 C1TX/ETXD0/PMD10/RF1 SDA1A/SDI1A/U1ARX/FF2 USBID/RF3 SDA3A/SDI3A/U3ARX/PMA9/CN17/RF4 SCL3A/SD03A/U3ATX/PMA8/CN18/RF5 SCL1A/SD01A/U1ATX/RF8 AC1RX/SS3A/U3BRX/U3ACTS/RF12 AC1TX/SCK3A/U3BTX/U3ARTS/RF13

87	ETXD1
88	ETXDØ
52	U1ARX
51	USBID
49	U3ARX
50	U3ATX
53	U1ATX
40	U3BRX/AC1RX/RF12
39	U3BTX/AC1TX/RF13

IC2PF

Port G	
C2RX/PMD8/RG0 C2TX/ETXERR/PMD9/RG1	90 RGØ 89 RG1
D+/RG2 D-/RG3	57 USBD+ 56 USBD-
ECOL/SCK2A/U2BTXU2ARTS/PMA5/CN8/RG6 ECRS/SDA2A/SDI2A/U2ARX/PMA4/CN9/RG7	10 PMA5/SCK2A/RG6 11 PMA4/SDI2A/RG7
ERXDU/AERXDU/ECRSDU/AECRSDU/ SCL2A/SDO2A/U2ATX/PMA3/CN10/RG8	12 PMA3/ECRSDU/SD02A/RG8
ERXCLK/AERXCLK/EREFCLK/AEREFCLK/ SS2A/U2BRX/U2ACTS/PMA2/CN11/RG9 TRD1/RG12 TRD0/RG13 TRD2/RG14 AERXERR/RG15	14 PMA2/EREFCLK/SS2A/RGS 96 RG12 97 RG13 95 RG14 1 RG15

IC2PG

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