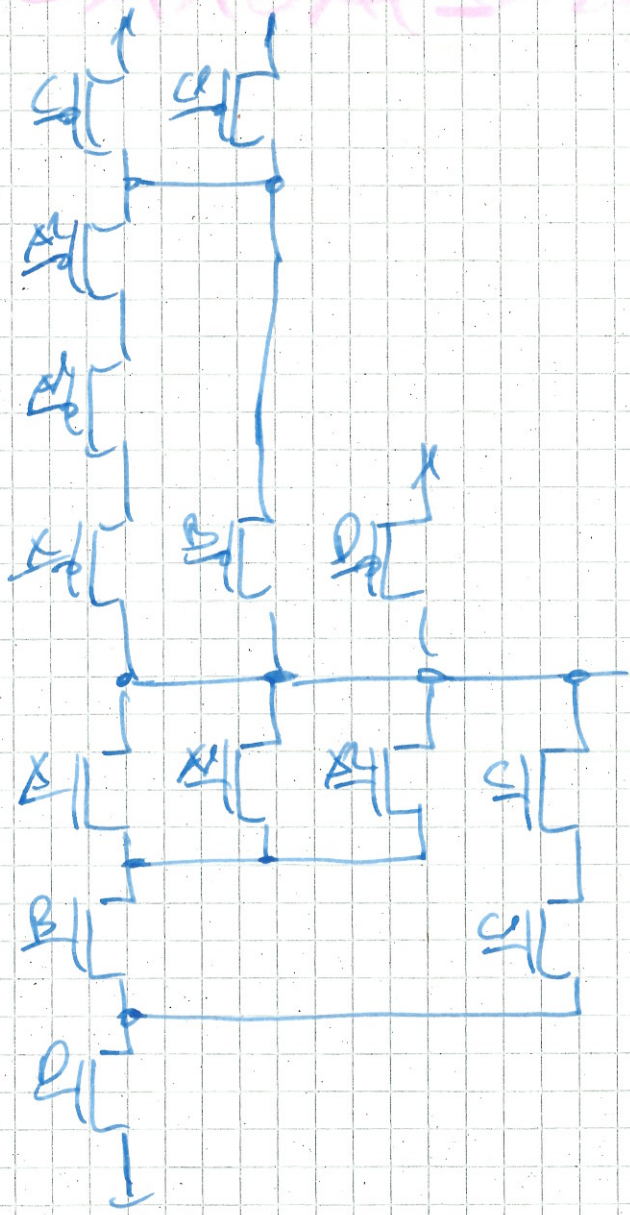
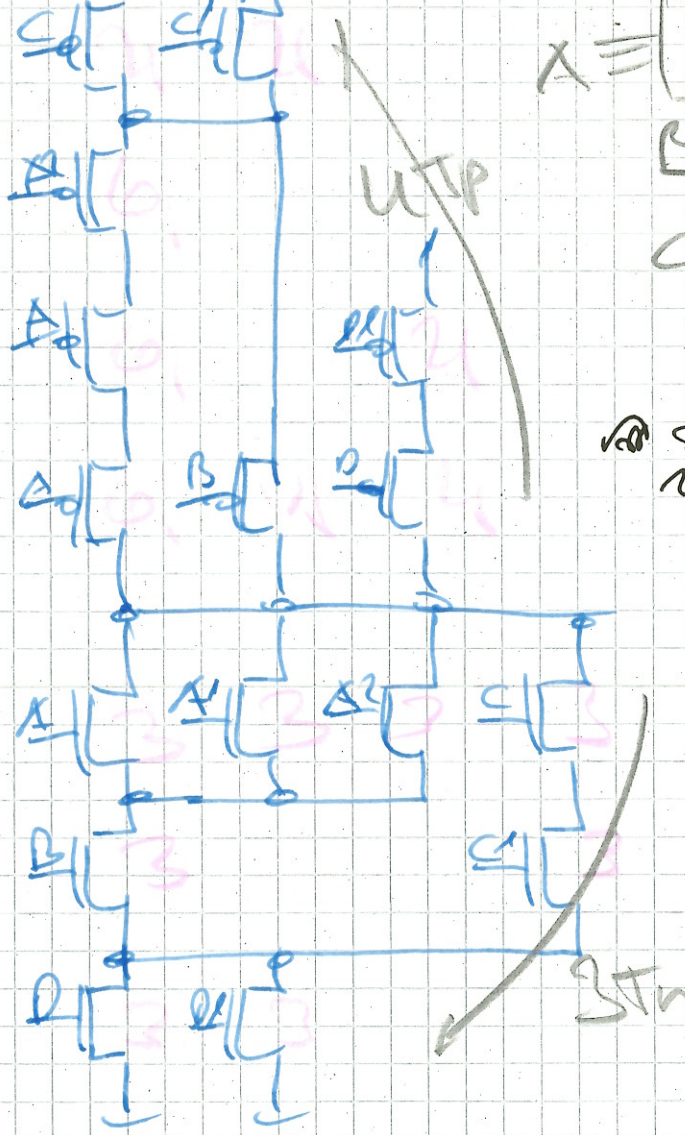


input

output



input



UTP

$$A = \begin{bmatrix} 1 & R & 1 & 8 \\ 5 & & & \\ C & & & \\ D & & & \end{bmatrix}$$

see section 10 or 12

3Tn