

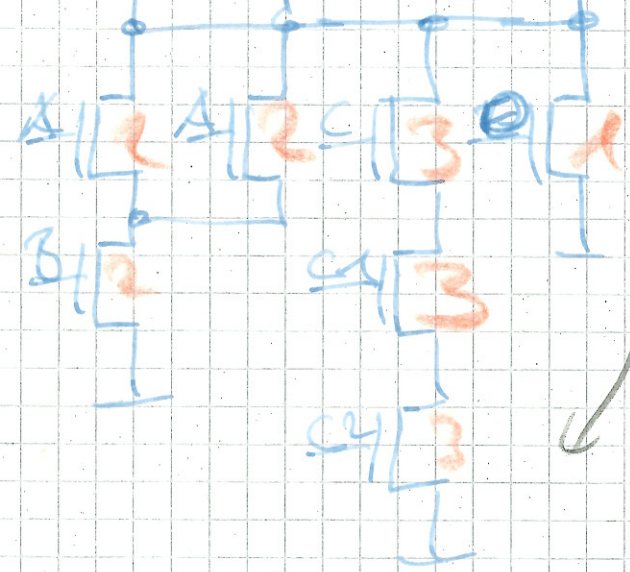
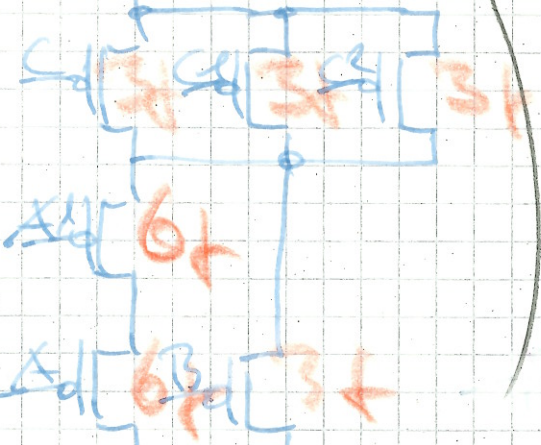
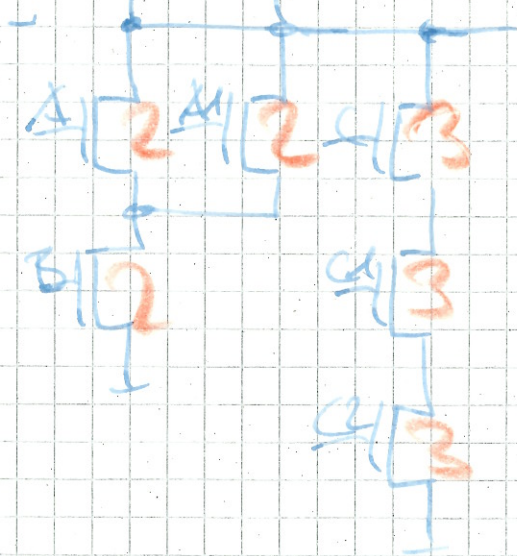
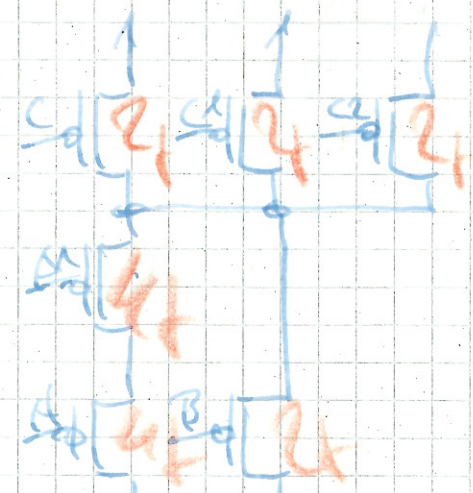
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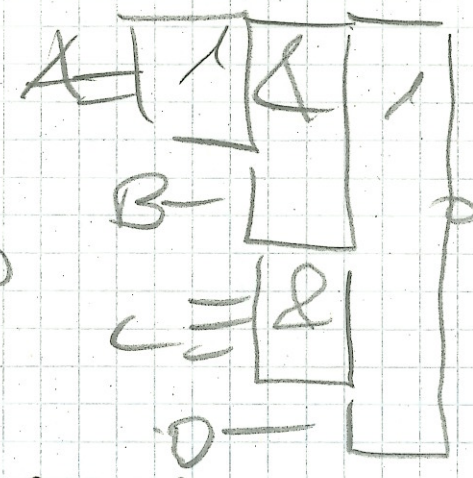
pcd
plan
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4Tn

3Tn



section notation

circuit or vld

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