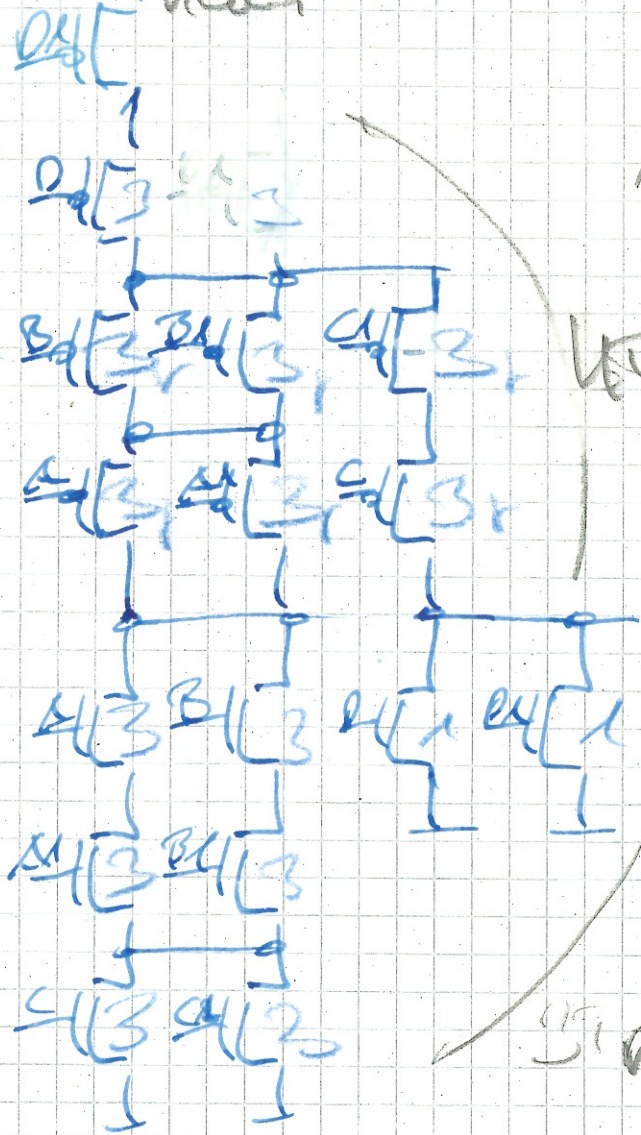
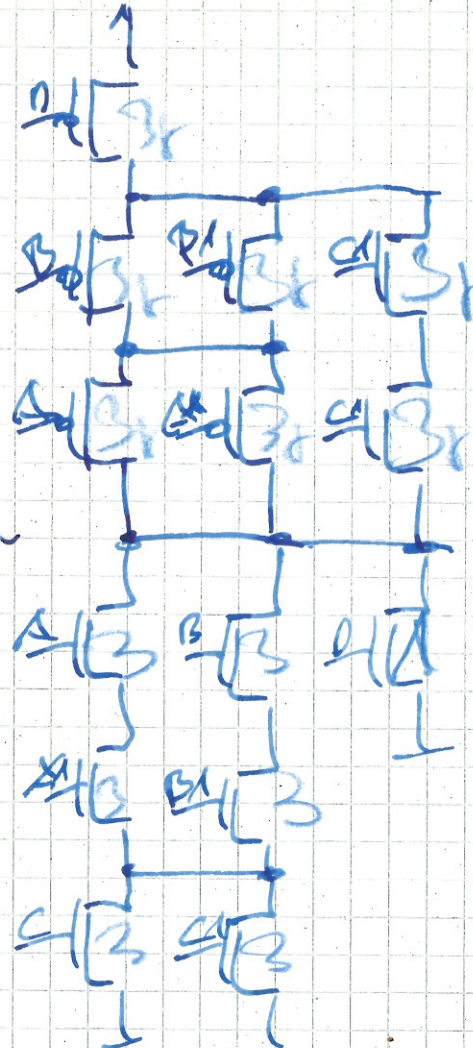


$A \begin{pmatrix} 1 & 0 & 0 & 1 & 2 & 2 & 2 & 1 \end{pmatrix} \xrightarrow[\text{non}]{\text{not}} A \begin{pmatrix} 0 & 0 & 1 & 0 & 1 & 2 & 2 & 2 \end{pmatrix}$

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परिचय



$$\begin{aligned}
 A &= \begin{bmatrix} 2 & 1 & 2 & 1 \\ 2 & & & \\ 1 & & & \end{bmatrix} \\
 B &= \begin{bmatrix} 2 & & & \\ & & & \\ & & & \end{bmatrix} \\
 C &= \begin{bmatrix} 1 & & & \end{bmatrix}
 \end{aligned}$$

An addition
 circuit
 for 3 bits
 A sketch
 of a circuit
 for 3 bits

5th