





Note:

PIO_A7 is used for PHY interrupt.

PIO_C13 is used for 5V detection when plugged into a USB host port.

			Ethernet 10/100 & DBGU/Serial				
			& USB Host/Device				
٦	TITLE		GPLv3				
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T91SAM9260	U1					
	PA23/TWD/ETX2	208	FXSINT			
	PB0/SPI1_MISO/TIOA3	9	SPI1_MISO			
	PB1/SPI1_MOSI/TIOB3	10	SPI1_MOSI			
	PB2/SPI1_SPCK/TIOA4	11	SPI1 SPCK			
	PB3/SPI1 NPCS0/TIOA5	12	SPI1 NPCS0			
	PB16/TK0/TCLK3	23	TK0			
	PB17/TF0/TCLK4	26	TF0	I_		
	PB18/TD0/TIOB4	27	TD0			
	PB19/RD0/TIOB5	28	RD0			
	PB20/RK0/ISI D0	163				
	PB21/RF0/ISI D1	164				

Clock: TK0 and RK0 are connected together, with RK0 configured as an input. FSYNC: TF0 and RF0 are connected together, with RF0 configured as an input.

SSC interface GPLv3			(for FXS Si321x)		
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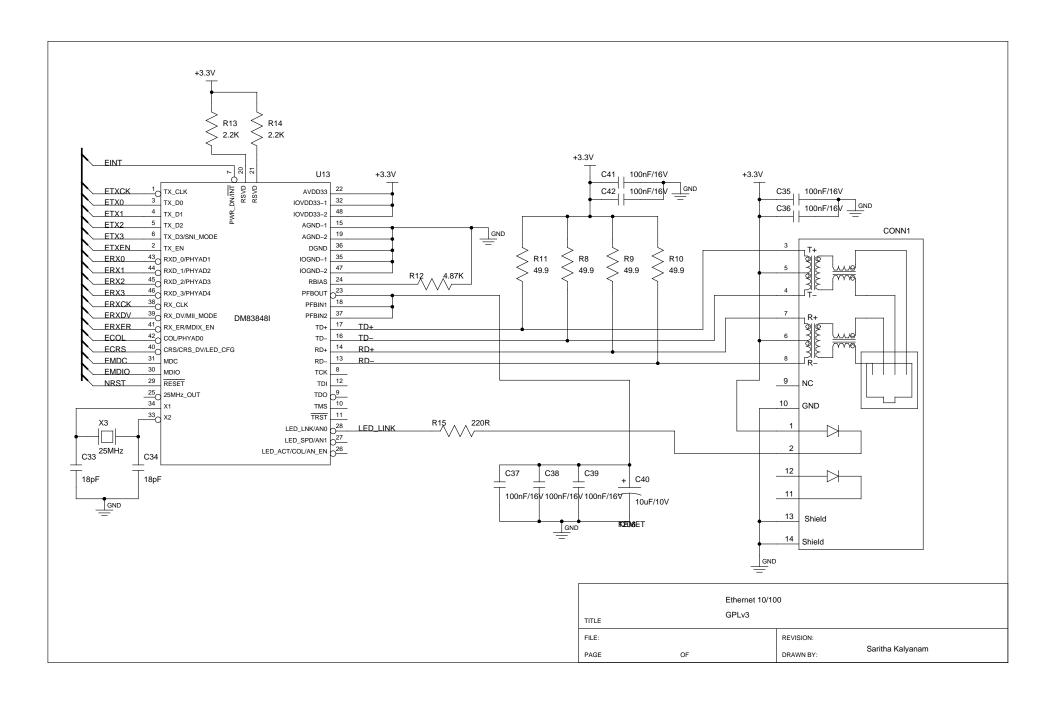
AT91SAM9260	U1			
	PA4/RTS2/MCDB2	183	SW1	
	PA5/CTS2/MCDB1	184	SW2	
P	B28/RTS1/ISI_PCK	175	LED1	
PB2	9/CTS1/ISI_VSYNC	176	LED2	/

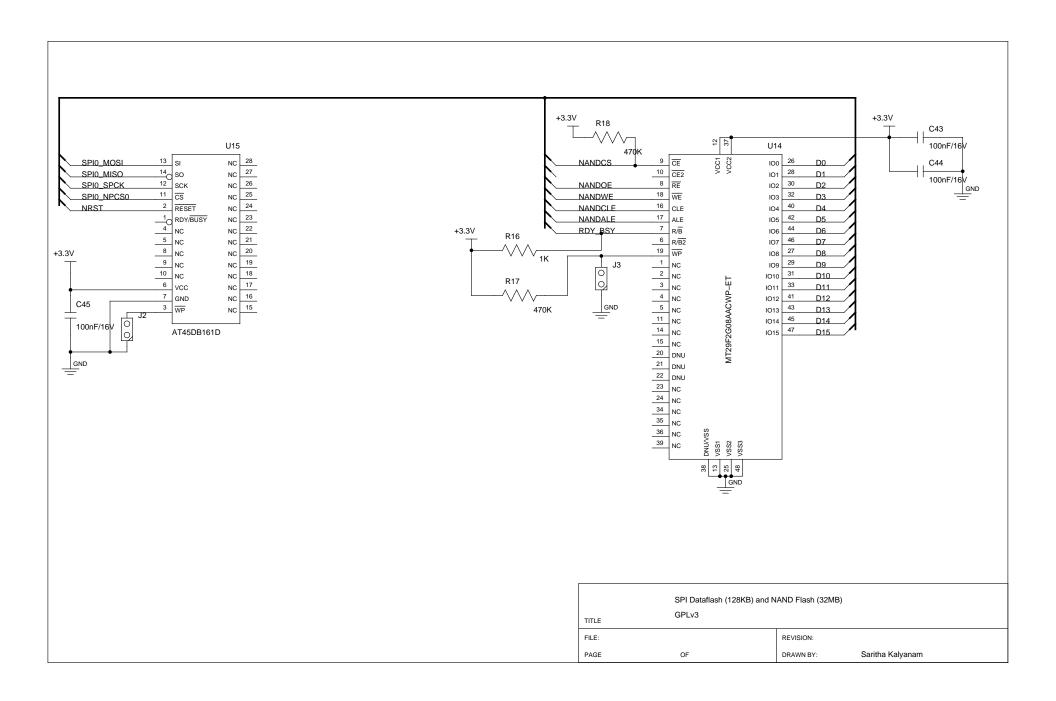
TITLE		User Interface (LED	s and Interrupts)	
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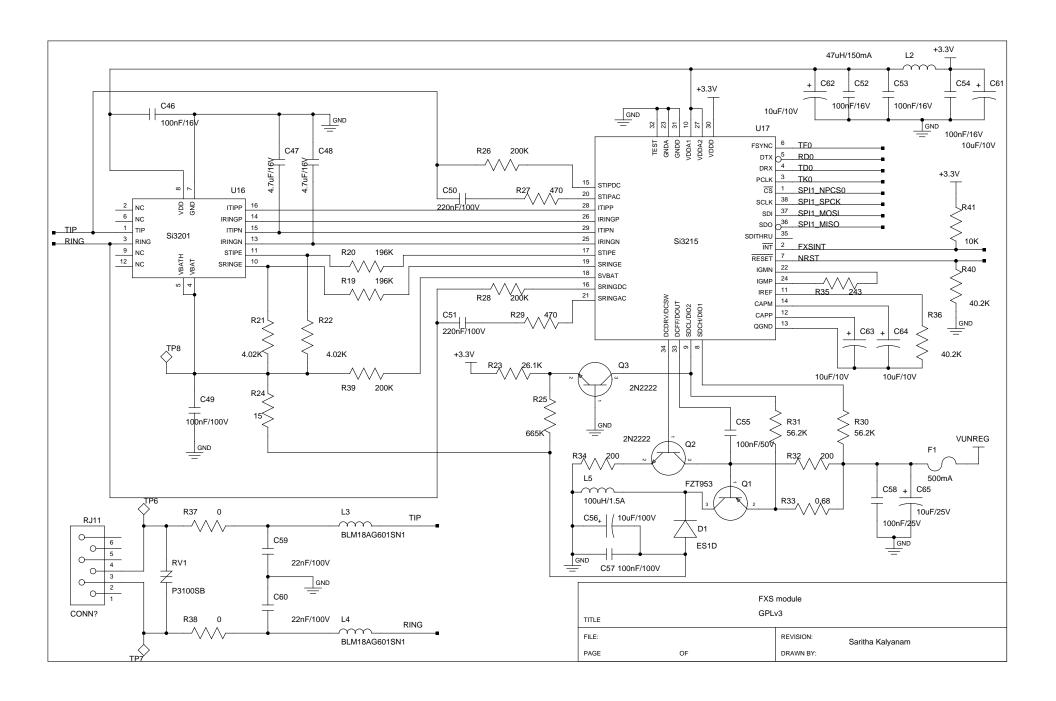
U1 AT91SAM9260 185 PA6/MCDA0 NC 185 PA6/MCDA0
189 PA8/MCCK
190 PA9/MCDA1
1 PA24/TWCK/ETX3
17 PB6/TXD1/TCLK1
18 PB7/RXD1/TCLK2
19 PB8/TXD2
20 PB9/RXD2
161 PB10/TXD3/ISL_D8 PB30/PCK0/ISI_HSYNC 177 PB31/PCK1/ISI_MCK 178 NRD/CFOE 069
NWR0/NWE/CFWE 070 NWR0/NWE/CFWE 0/0
NCS0 0/68
A12 0/55
A15 0/72
A18 0/76
A20 0/75
PC5/A24/SPI1_NPCS1 67 162 PB11/RXD3/ISI_D9 165 PB22/DSR0/ISI_D2 166 PB23/CDC0/ISI_D3 167 PB24/DTR0/ISI_D4 168 PB25/RI0/ISI_D5 158 PC0 159 PC1 62 PC4/A23/SPI1_NPCS2 63 PC6/TIOB2/CFCE1 64 PC7/TIOB1/CFCE2 61 PC8/NCS4/CFCS0/RTS3 60 PC9/NCS5/CFCS1/TIOB0 58 PC10/A25/CFRNW/CTS3

127 PC15/NWAIT/IRQ1

TITLE		Unconnected GPLv3	pins	
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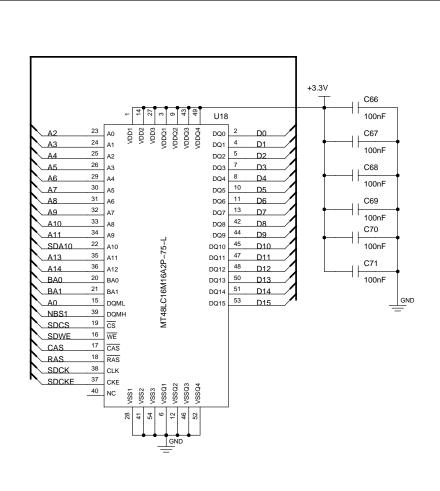




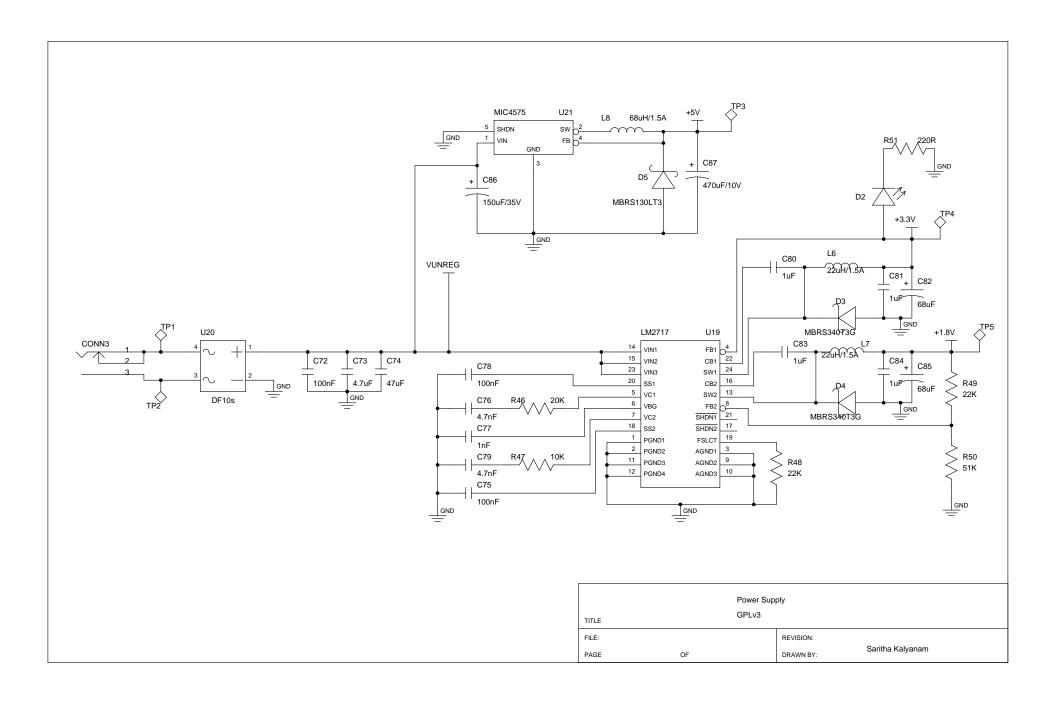


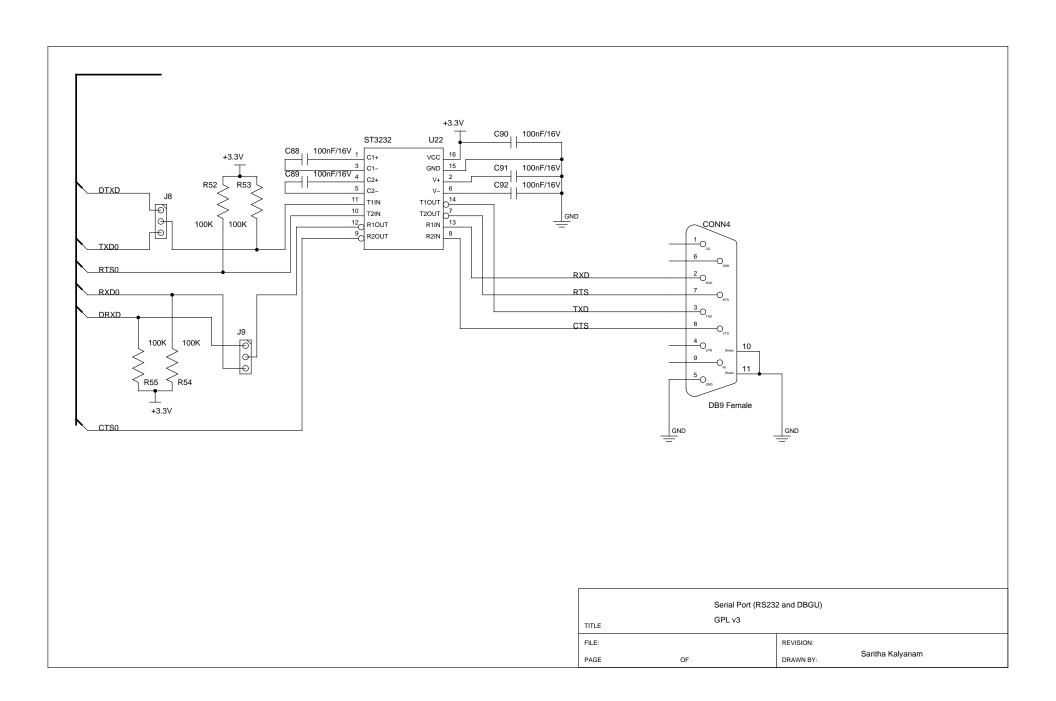


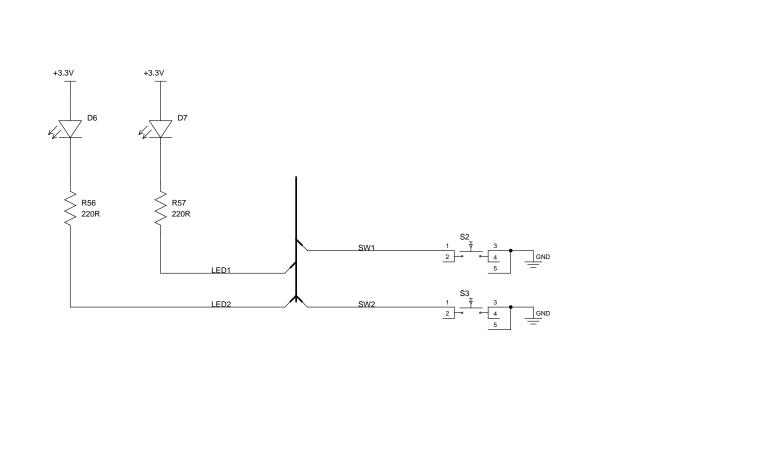
JTAG/ICE interfa			ace	
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TITLE	SDR 32 N	AM IB (16-bit mode)	mode)		
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UI GPLv3

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