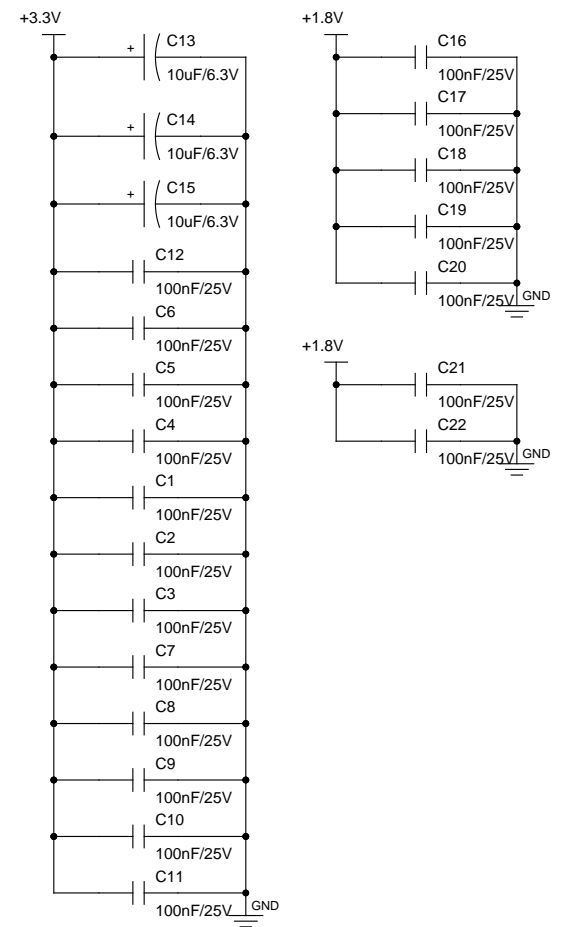




AT91SAM9260 based Telephone ATA Design (GPL v3)		
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TITLE		Power GPLv3	
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Note:

REMAP=0, BMS=1 to boot using AT91SAM9260's internal ROM boot program.

OSCSEL=1, external slow clock (32.768 KHz)

Clock and JTAG/ICE	
GPLv3	
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Note:

PIO\_C11 is used for RDY\_BSY interrupt from the NAND flash.

SDRAM & Serial SPI Data flash & NAND flash			
GPLv3			
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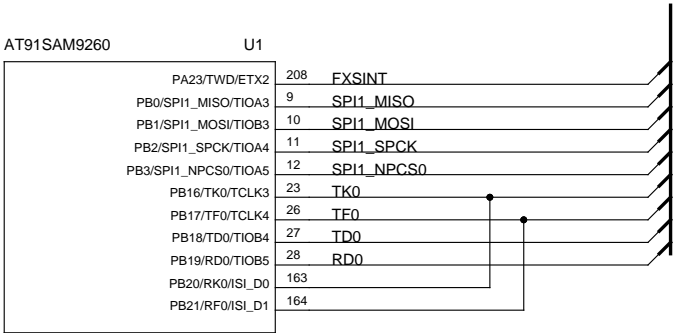


Note:

PIO\_A7 is used for PHY interrupt.

PIO\_C13 is used for 5V detection when plugged into a USB host port.

Ethernet 10/100 & DBGU/Serial & USB Host/Device	
TITLE GPLv3	
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Note:

Clock: TK0 and RK0 are connected together, with RK0 configured as an input.  
FSYNC: TF0 and RF0 are connected together, with RF0 configured as an input.

SSC interface (for FXS Si321x)			
GPLv3			
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AT91SAM9260

U1

PA4/RTS2/MCDB2
PA5/CTS2/MCDB1
PB28/RTS1/ISI_PCK
PB29/CTS1/ISI_VSYNC

183  
184  
175  
176

SW1  
SW2  
LED1  
LED2

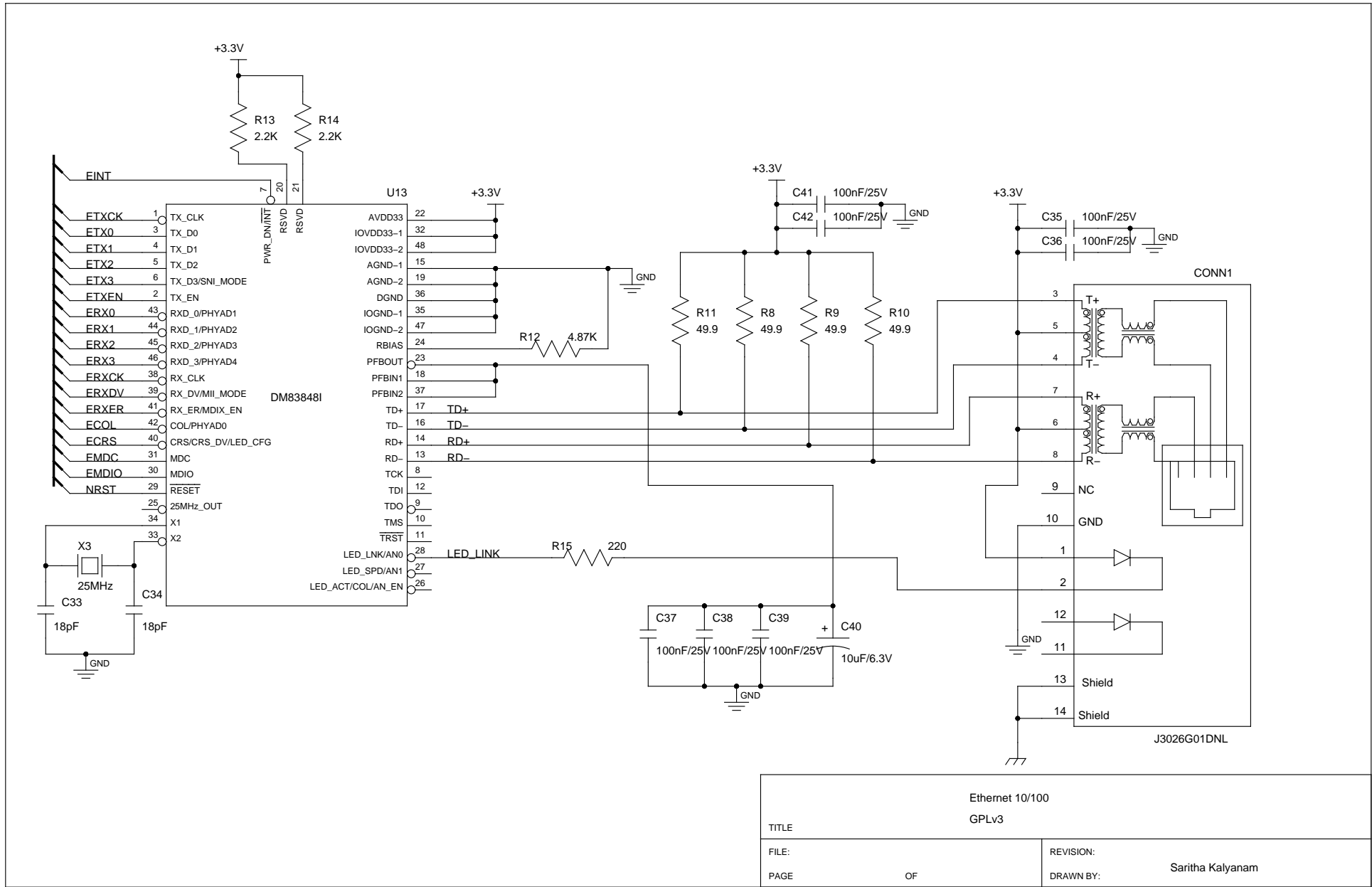


User Interface (LEDs and Interrupts)	
GPLv3	
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Unconnected pins	
GPLv3	
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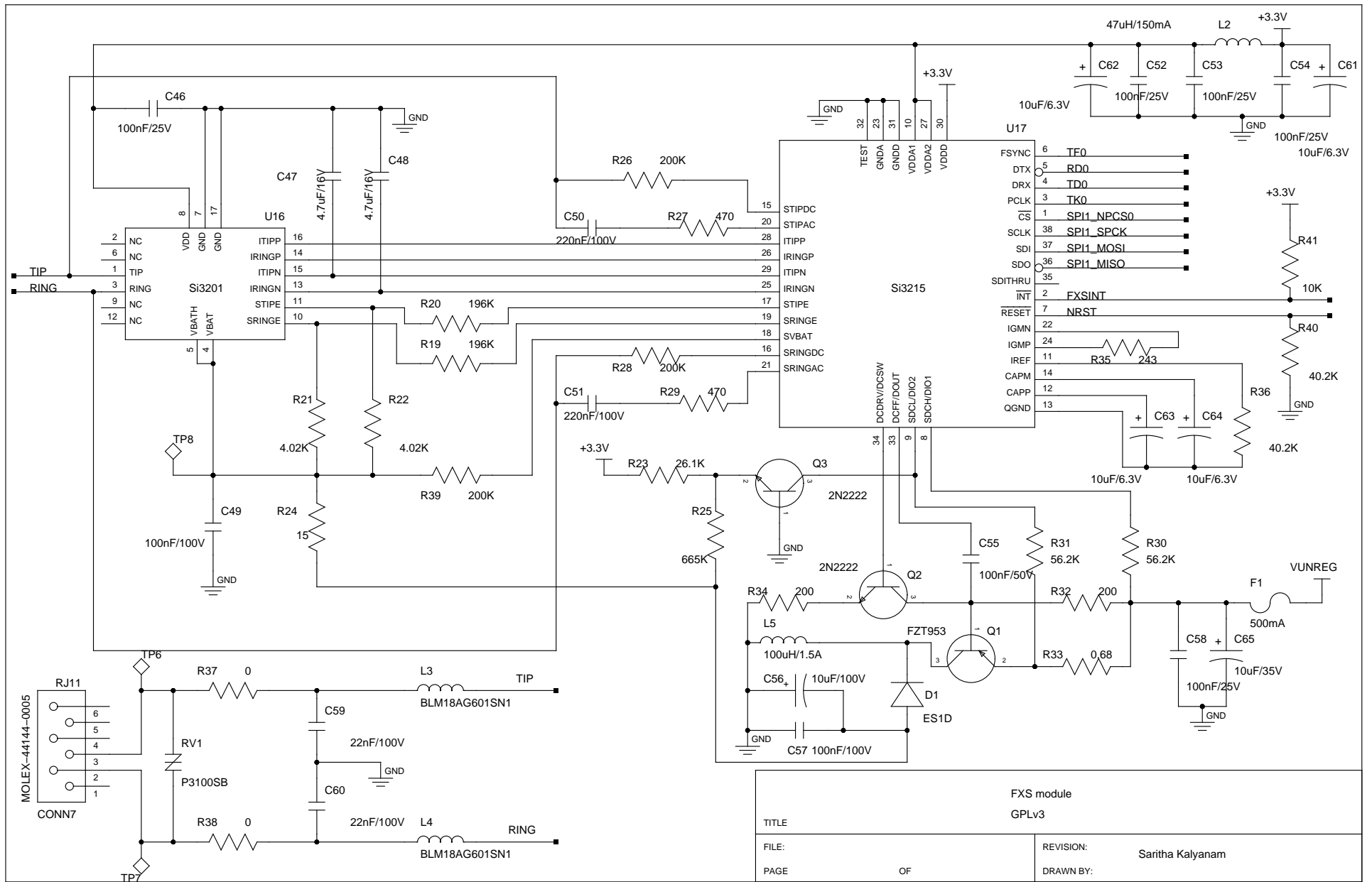


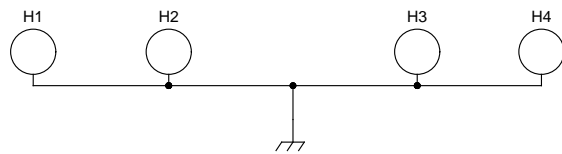


Ethernet 10/100 GPLv3		
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TITLE			SPI Dataflash (128KB) and NAND Flash (32MB)		
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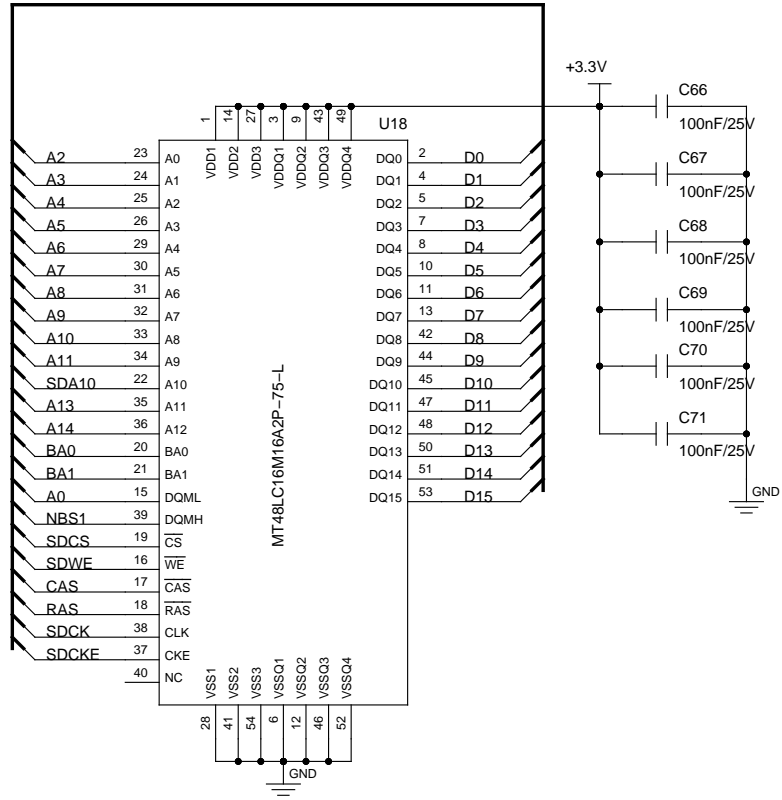




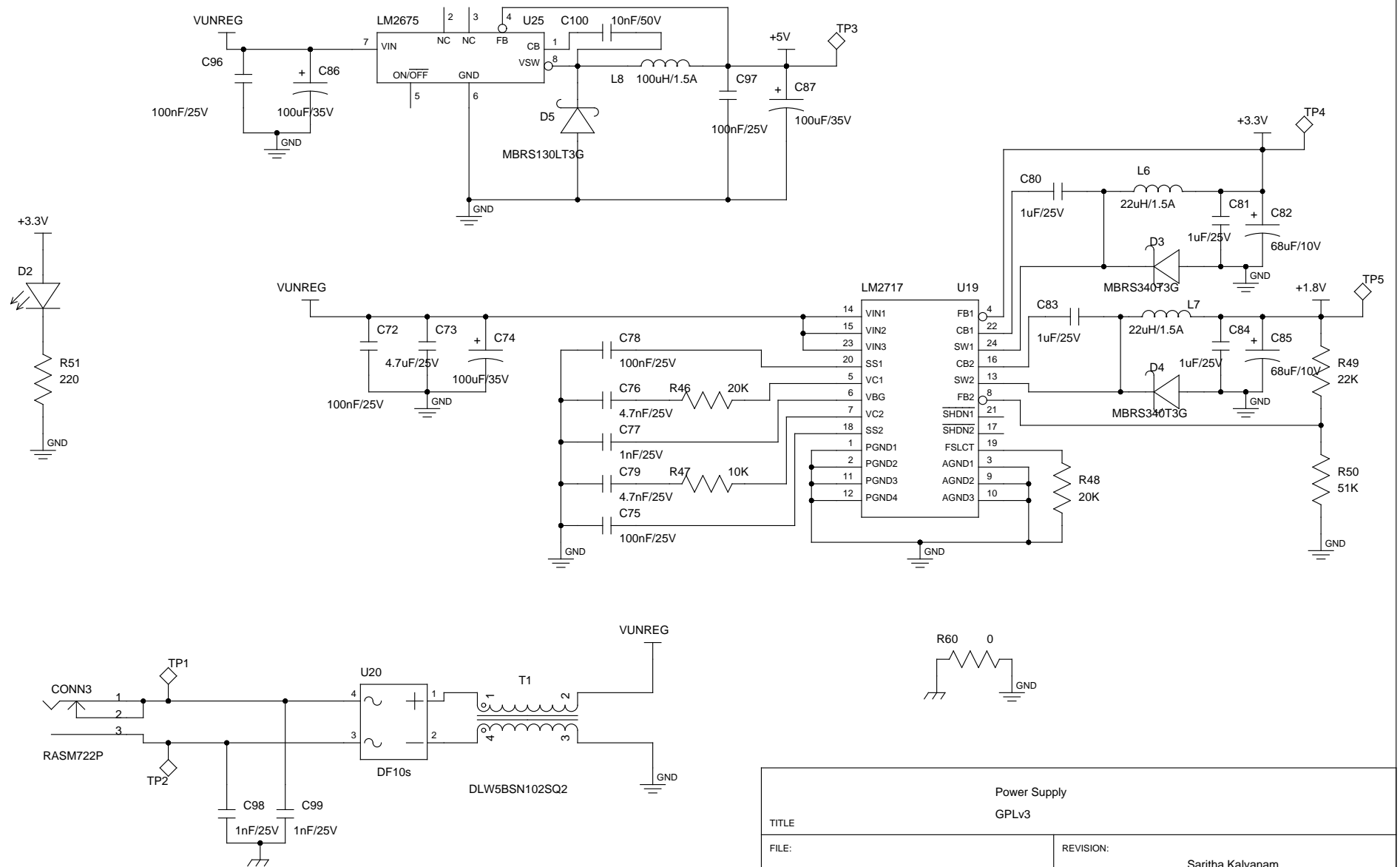
TITLE			Mounting holes GPLv3	
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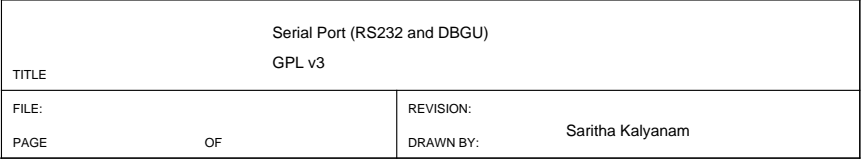
TITLE		JTAG/ICE interface GPLv3	
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SDRAM			
32 MB (16-bit mode)			
TITLE			
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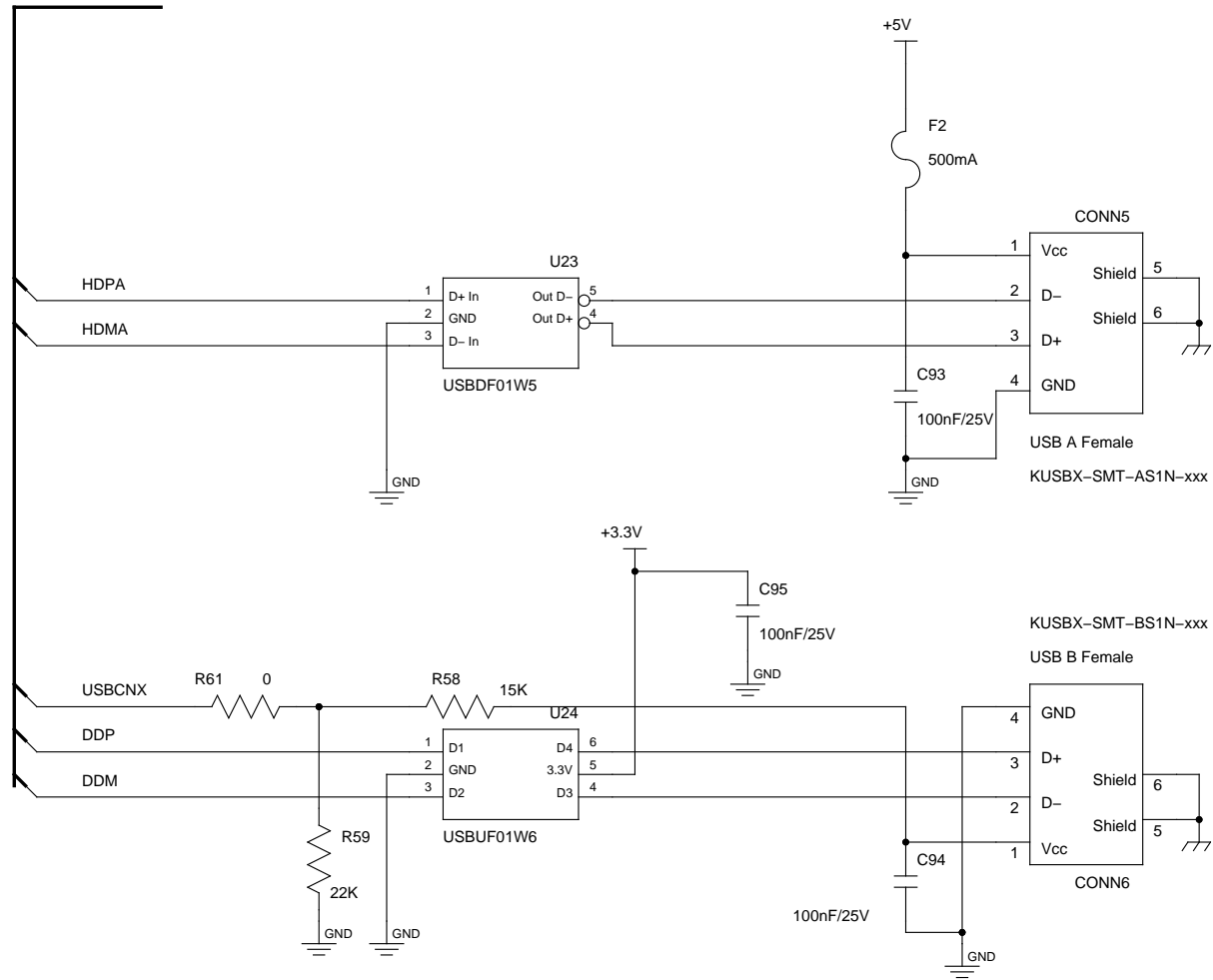
Power Supply		
GPLv3		
TITLE		
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USB Host and Device	
GPLv3	
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