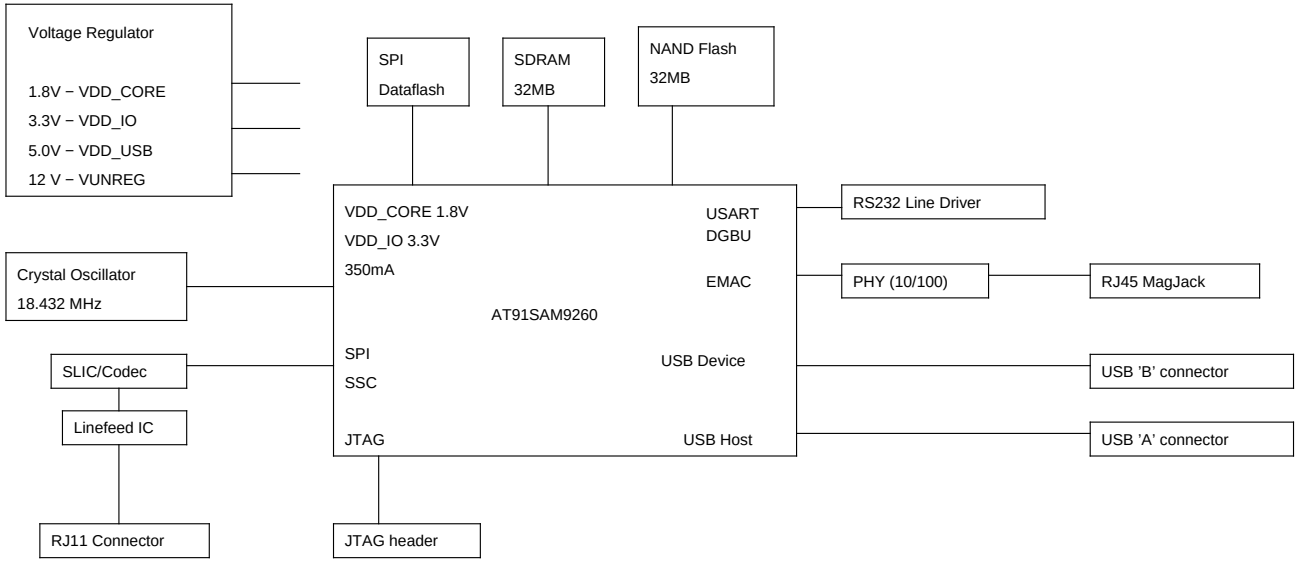
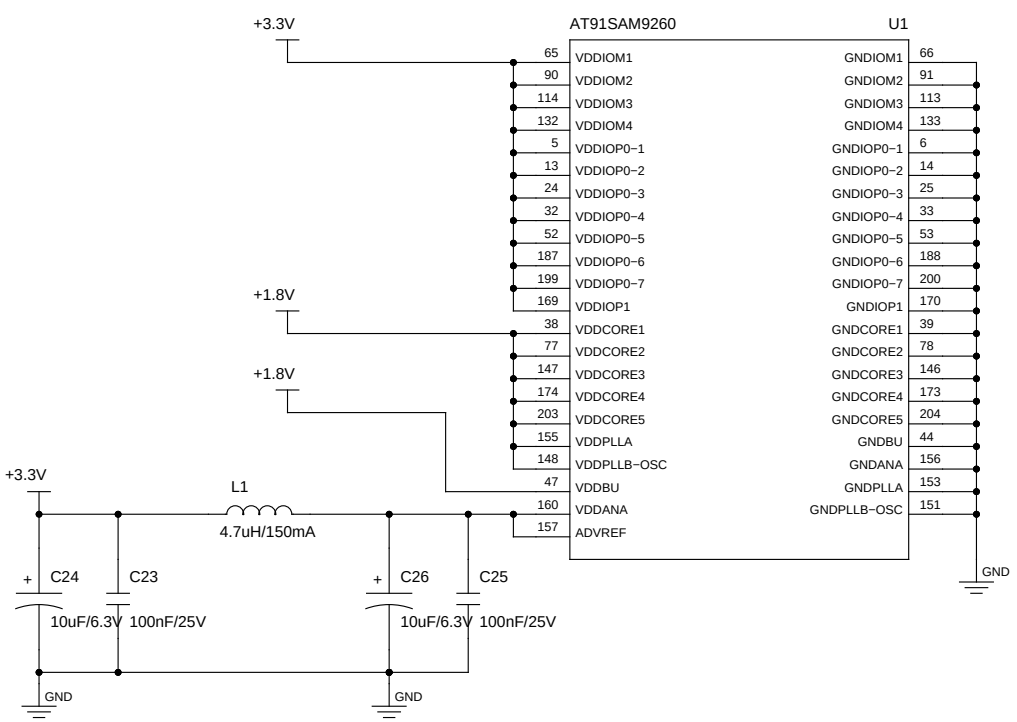
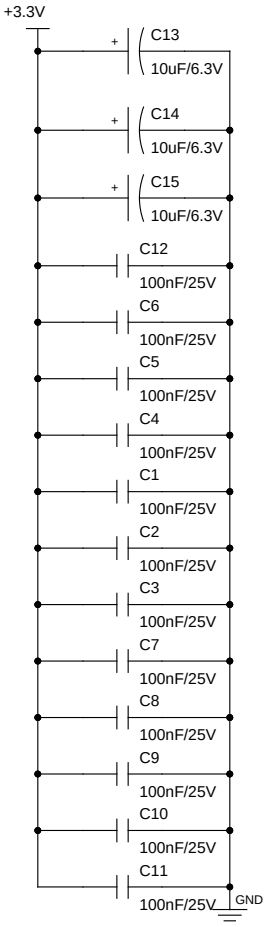
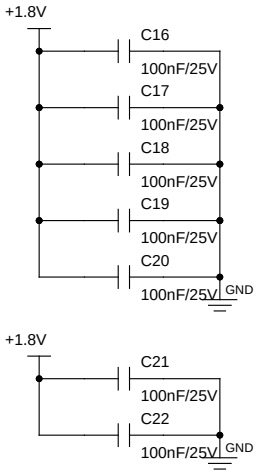


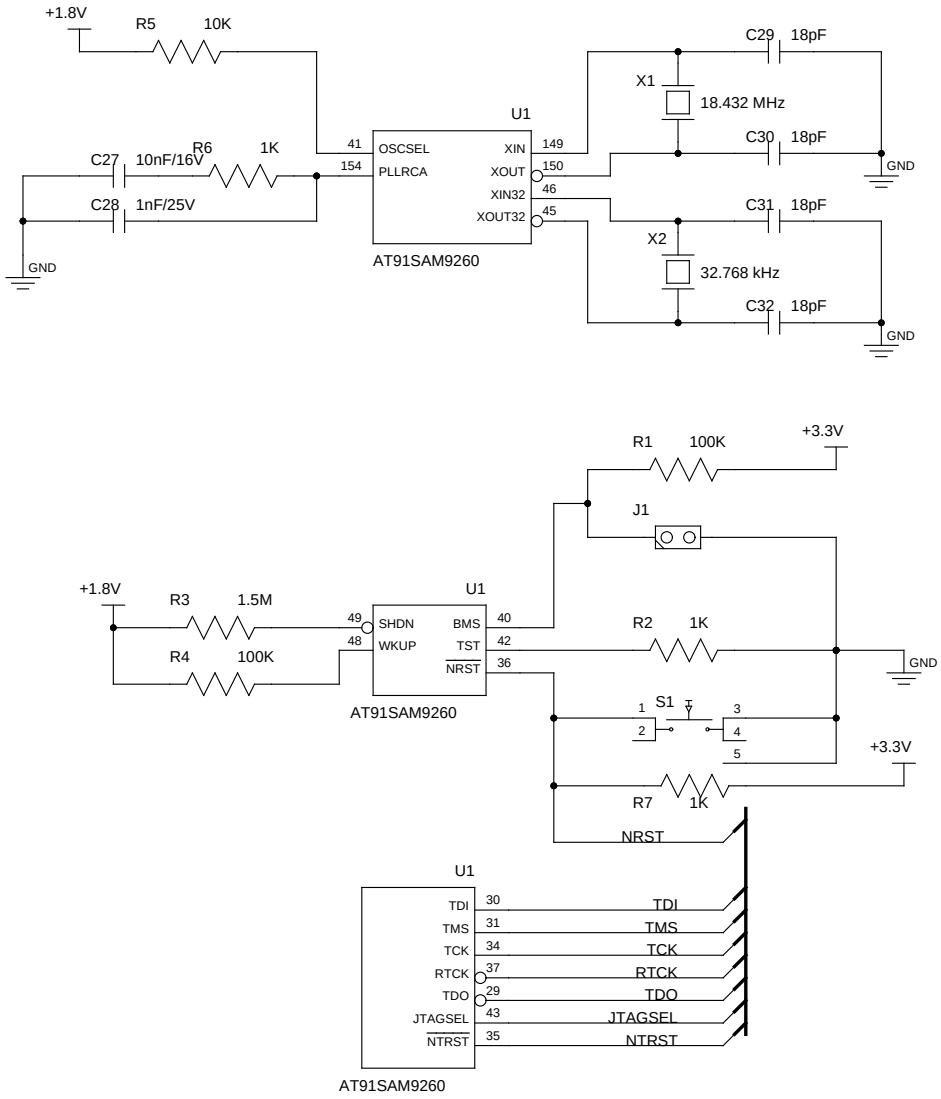
TITLE: AT91SAM9260 based Telephone ATA Design (GPL v3)	
FILE:	REVISION:
PAGE OF	DRAWN BY: Saritha Kalyanam







TITLE	Power GPLv3	
FILE:		REVISION:
PAGE OF		DRAWN BY: Saritha Kalyanam

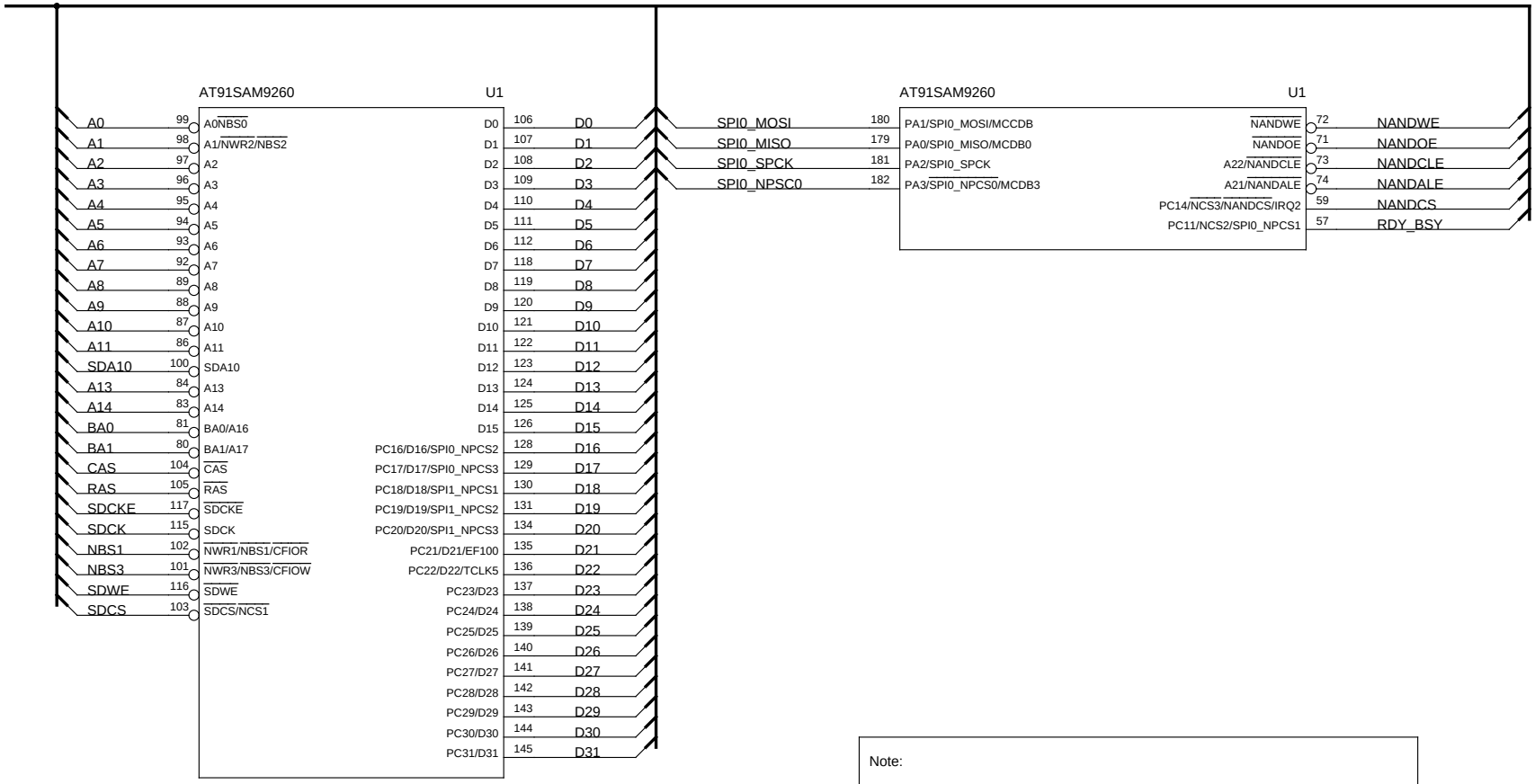


Note:

REMAP=0, BMS=1 to boot using AT91SAM9260's internal ROM boot program.

OSCSEL=1, external slow clock (32.768 KHz)

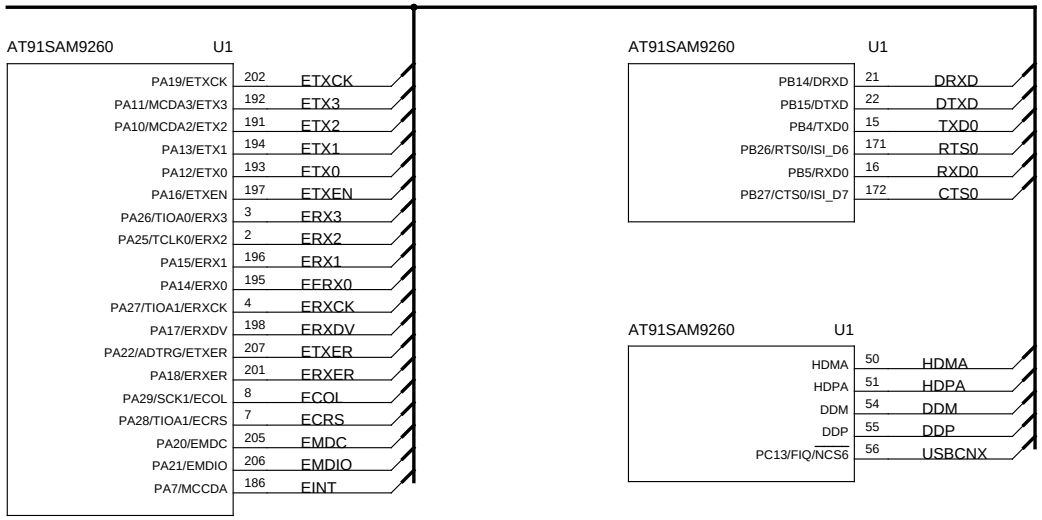
TITLE	Clock and JTAG/ICE	
	GPLv3	
FILE:		REVISION:
PAGE	OF	DRAWN BY: Saritha Kalyanam



Note:

PIO_C11 is used for RDY_BSY interrupt from the NAND flash.

TITLE: SDRAM & Serial SPI Data flash & NAND flash GPLv3	
FILE:	REVISION:
PAGE OF	DRAWN BY: Saritha Kalyanam

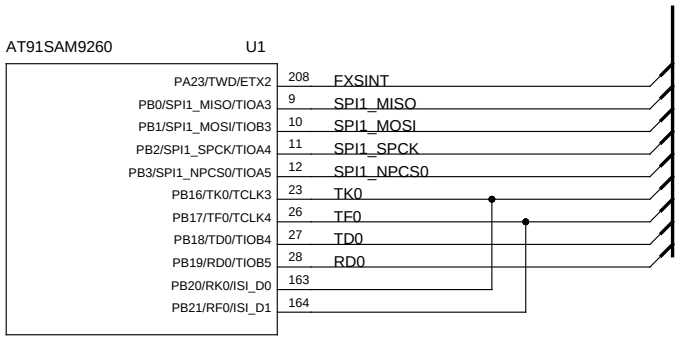


Note:

PIO_A7 is used for PHY interrupt.

PIO_C13 is used for 5V detection when plugged into a USB host port.

	Ethernet 10/100 & DBGU/Serial & USB Host/Device
TITLE	GPLv3
FILE:	REVISION:
PAGE OF	DRAWN BY: Saritha Kalyanam

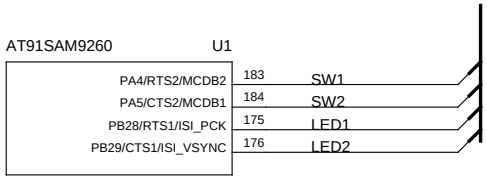


Note:

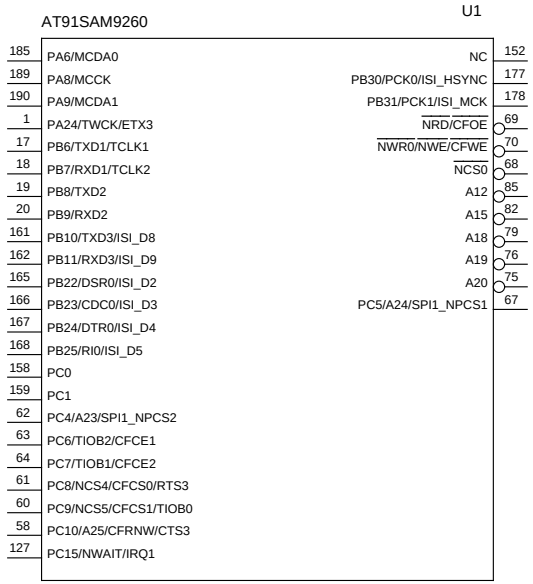
Clock: TK0 and RK0 are connected together, with RK0 configured as an input.

FSYNC: TF0 and RF0 are connected together, with RF0 configured as an input.

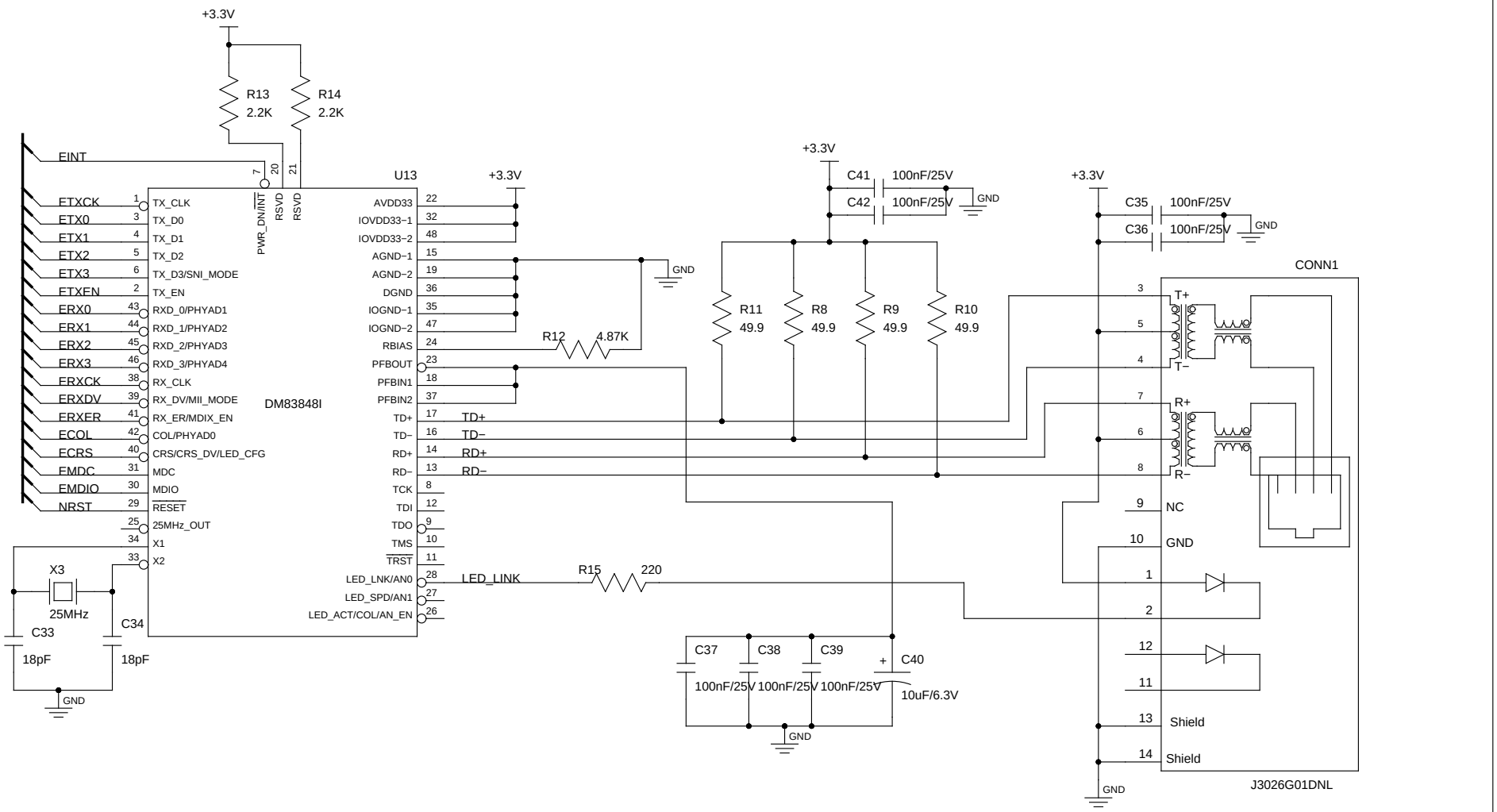
TITLE: SSC interface (for FXS Si321x)	
GPLv3	
FILE:	REVISION:
PAGE OF	DRAWN BY: Saritha Kalyanam

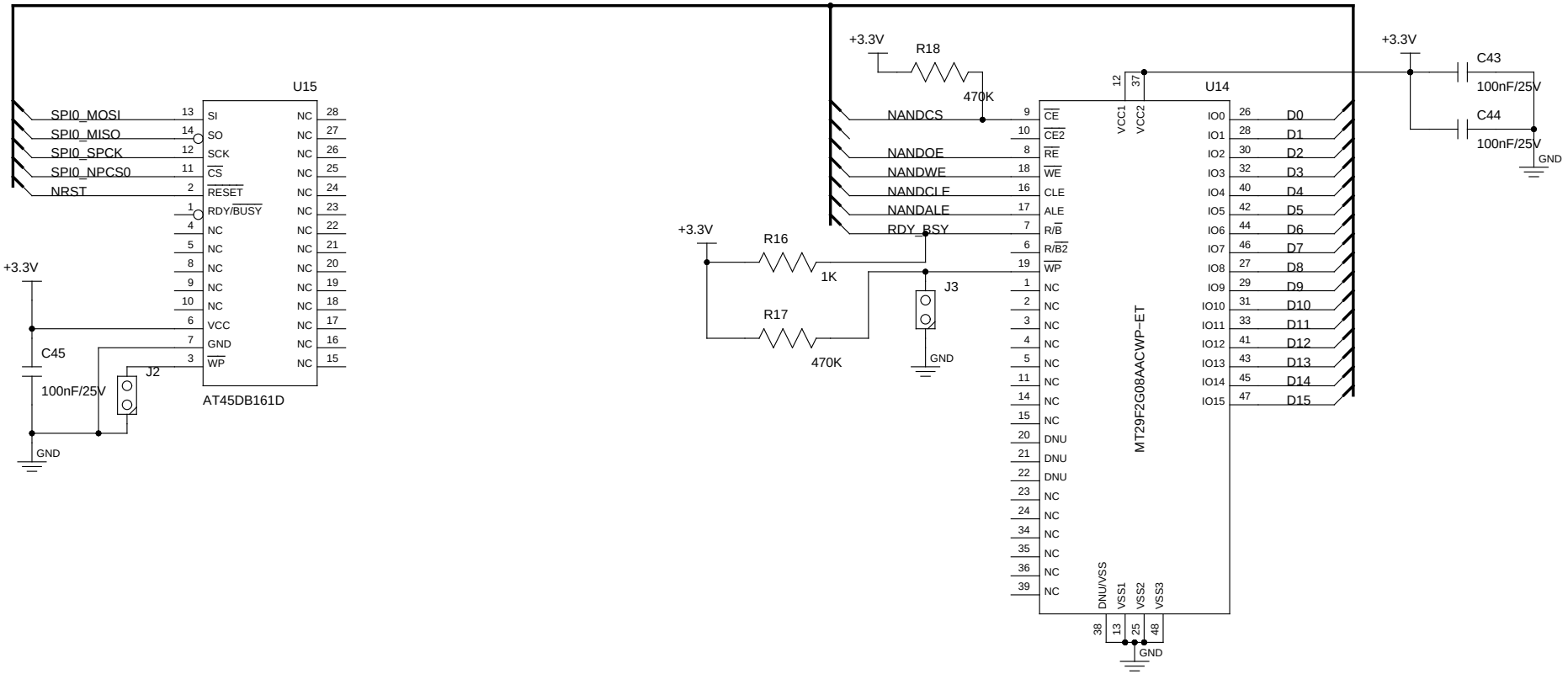


TITLE: User Interface (LEDs and Interrupts)	
GPLv3	
FILE:	REVISION:
PAGE OF	DRAWN BY: Saritha Kalyanam

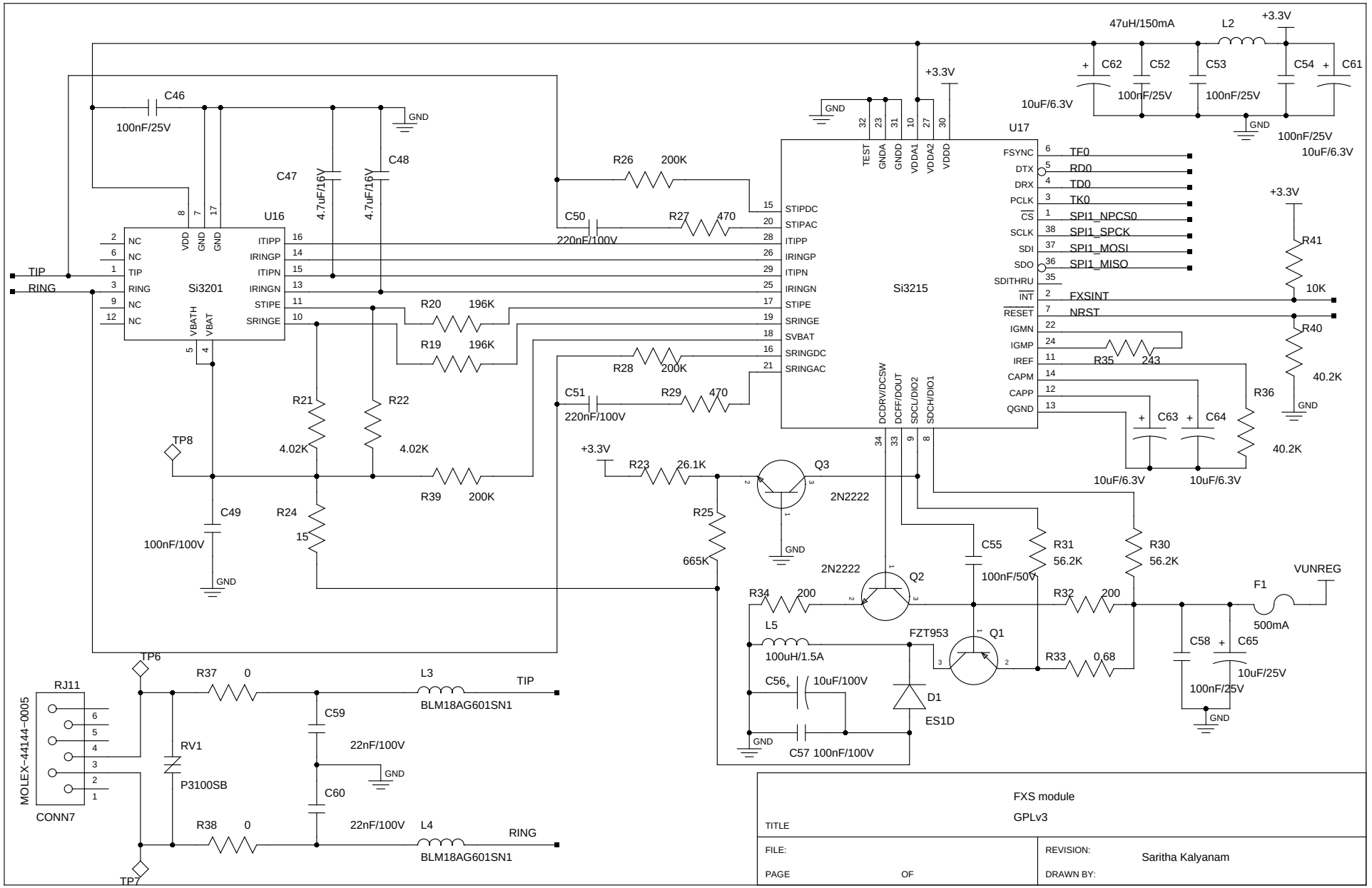


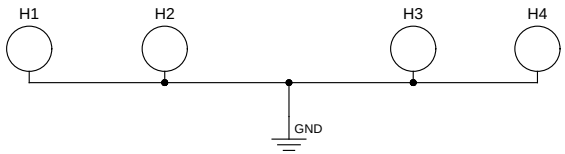
TITLE: Unconnected pins GPLv3	
FILE:	REVISION:
PAGE OF	DRAWN BY: Saritha Kalyanam


+3.3V
R13
2.2K
R14
2.2K
EINT
U13
DM8384BI
ETXCK
ETX0
ETX1
ETX2
ETX3
ETXEN
ERX0
ERX1
ERX2
ERX3
ERXCK
ERXDV
ERXER
ECOL
ECRS
EMDC
EMDIO
NRST
TX_CLK
TX_D0
TX_D1
TX_D2
TX_D3/SNI_MODE
TX_EN
RXD_0/PHYAD1
RXD_1/PHYAD2
RXD_2/PHYAD3
RXD_3/PHYAD4
RX_CLK
RX_DV/MII_MODE
RX_ER/MDIX_EN
COL/PHYAD0
CRS/CRS_DV/LED_CFG
MDC
MDIO
RESET
25MHz_OUT
X1
X2
PWR_DN/INT
RSVD
RSVD
AVDD33
IOVDD33−1
IOVDD33−2
AGND−1
AGND−2
DGND
IOGND−1
IOGND−2
RBIAS
PFBOUT
PFBIN1
PFBIN2
TD+
TD−
RD+
RD−
TCK
TDI
TDO
TMS
TRST
LED_LNK/AN0
LED_SPD/AN1
LED_ACT/COL/AN_EN
GND
R12
4.87K
+3.3V
C41
100nF/25V
C42
100nF/25V
GND
R11
49.9
R8
49.9
R9
49.9
R10
49.9
TD+
TD−
RD+
RD−
+3.3V
C35
100nF/25V
C36
100nF/25V
GND
CONN1
T+
T−
R+
R−
NC
GND
Shield
Shield
J3026G01DNL
X3
25MHz
C33
18pF
C34
18pF
GND
LED_LINK
R15
220
C37
100nF/25V
C38
100nF/25V
C39
100nF/25V
C40
10uF/6.3V
GND
GND
Ethernet 10/100
GPLv3
TITLE
FILE:
REVISION:
PAGE
OF
DRAWN BY:
Saritha Kalyanam

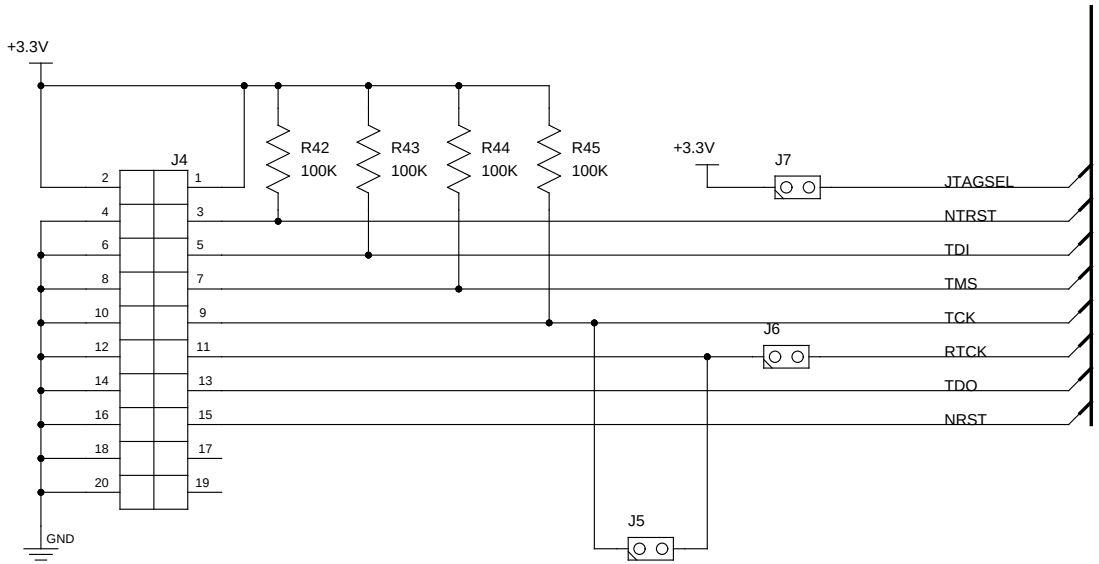


TITLE	SPI Dataflash (128KB) and NAND Flash (32MB) GPLv3	
FILE:		REVISION:
PAGE	OF	DRAWN BY: Saritha Kalyanam

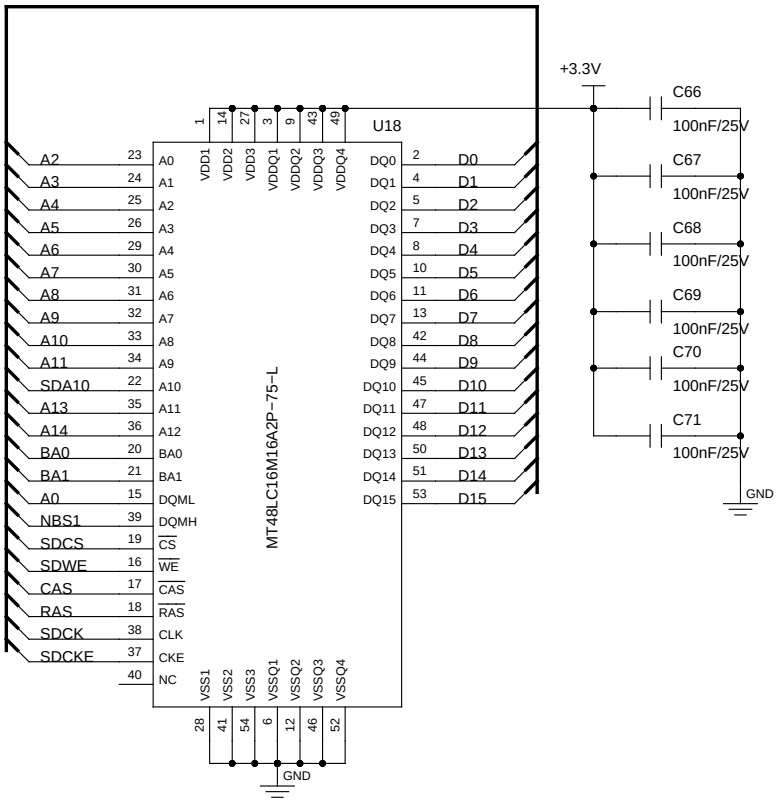

47uH/150mA
L2
+3.3V
C62
C52
C53
C54
C61
10uF/6.3V
100nF/25V
100nF/25V
100nF/25V
10uF/6.3V
C46
100nF/25V
GND
C47
C48
4.7uF/16V
4.7uF/16V
U16
Si3201
TIP
RING
R26 200K
C50
220nF/100V
R27 470
R20 196K
R19 196K
R28 200K
C51
220nF/100V
R29 470
U17
Si3215
TEST
GNDA
GNDD
VDDA1
VDDA2
VDDD
FSYNC
DTX
DRX
PCLK
CS
SCLK
SDI
SDO
SDITHRU
INT
RESET
IGMN
IGMP
IREF
CAPM
CAPP
QGND
TF0
RD0
TD0
TK0
SPI1_NPCS0
SPI1_SPCK
SPI1_MOSI
SPI1_MISO
FXSINT
NRST
R41
10K
R40
40.2K
R35 243
R36
40.2K
C63
C64
10uF/6.3V
10uF/6.3V
STIPDC
STIPAC
ITIPP
IRINGP
ITIPN
IRINGN
STIPE
SRINGE
SVBAT
SRINGDC
SRINGAC
DCDRV/DCSW
DCFF/DOUT
SDCL/DIO2
SDCH/DIO1
R21
R22
4.02K
4.02K
R39 200K
TP8
C49
100nF/100V
R24
15
R23 26.1K
Q3
2N2222
R25
665K
C55
100nF/50V
R31
56.2K
R30
56.2K
VUNREG
F1
500mA
2N2222
Q2
R34 200
R32 200
L5
100uH/1.5A
FZT953
Q1
R33 0.68
C58
C65
10uF/25V
100nF/25V
C56
10uF/100V
D1
ES1D
C57 100nF/100V
TP6
TP7
RJ11
MOLEX-44144-0005
CONN7
R37 0
R38 0
RV1
P3100SB
C59
C60
22nF/100V
22nF/100V
L3
L4
BLM18AG601SN1
BLM18AG601SN1
TIP
RING
FXS module
GPLv3
TITLE
FILE:
PAGE
OF
REVISION:
Saritha Kalyanam
DRAWN BY:



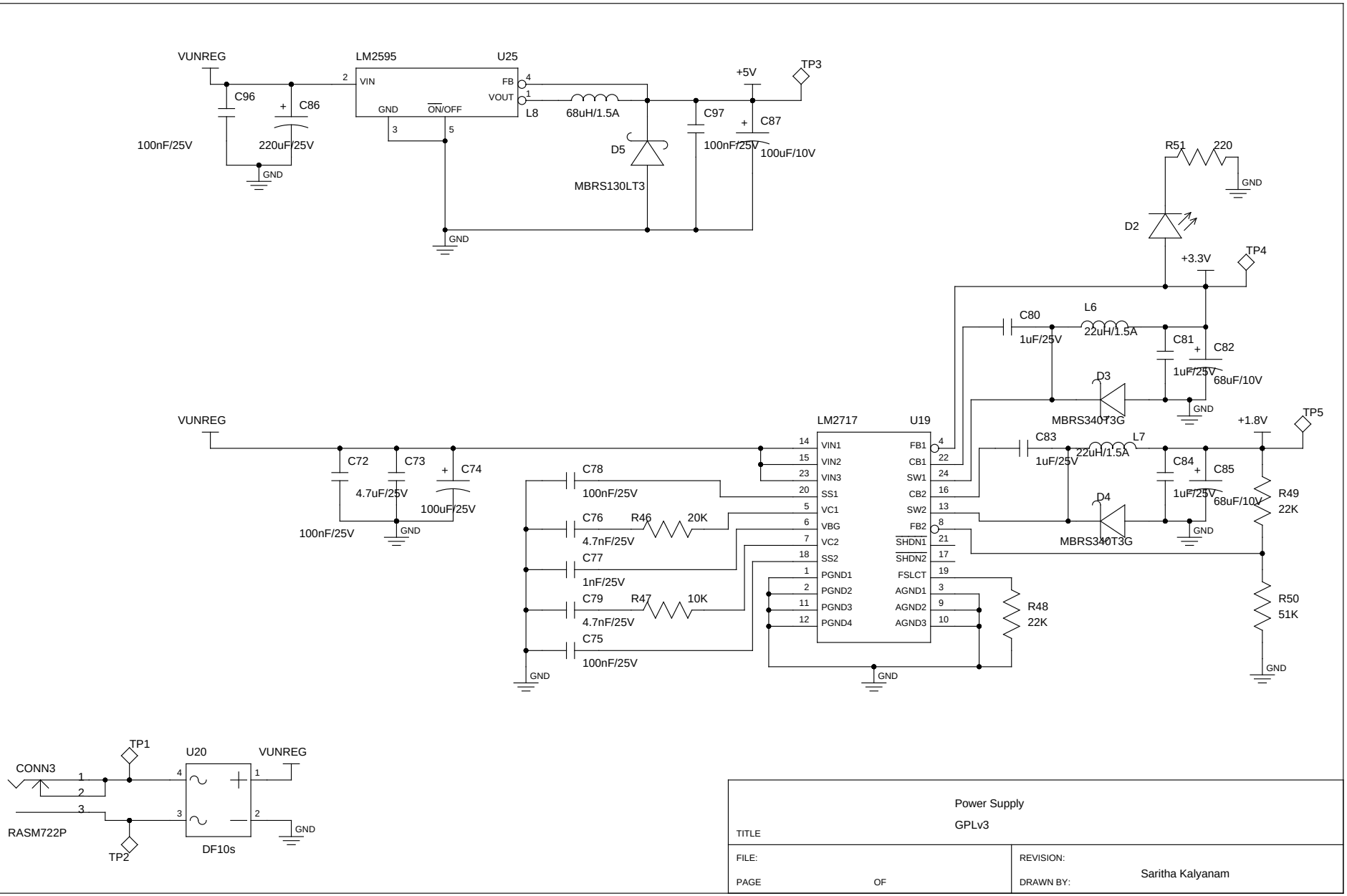
Mounting holes GPLv3 TITLE	
FILE: PAGE OF	REVISION: DRAWN BY: Saritha Kalyanam

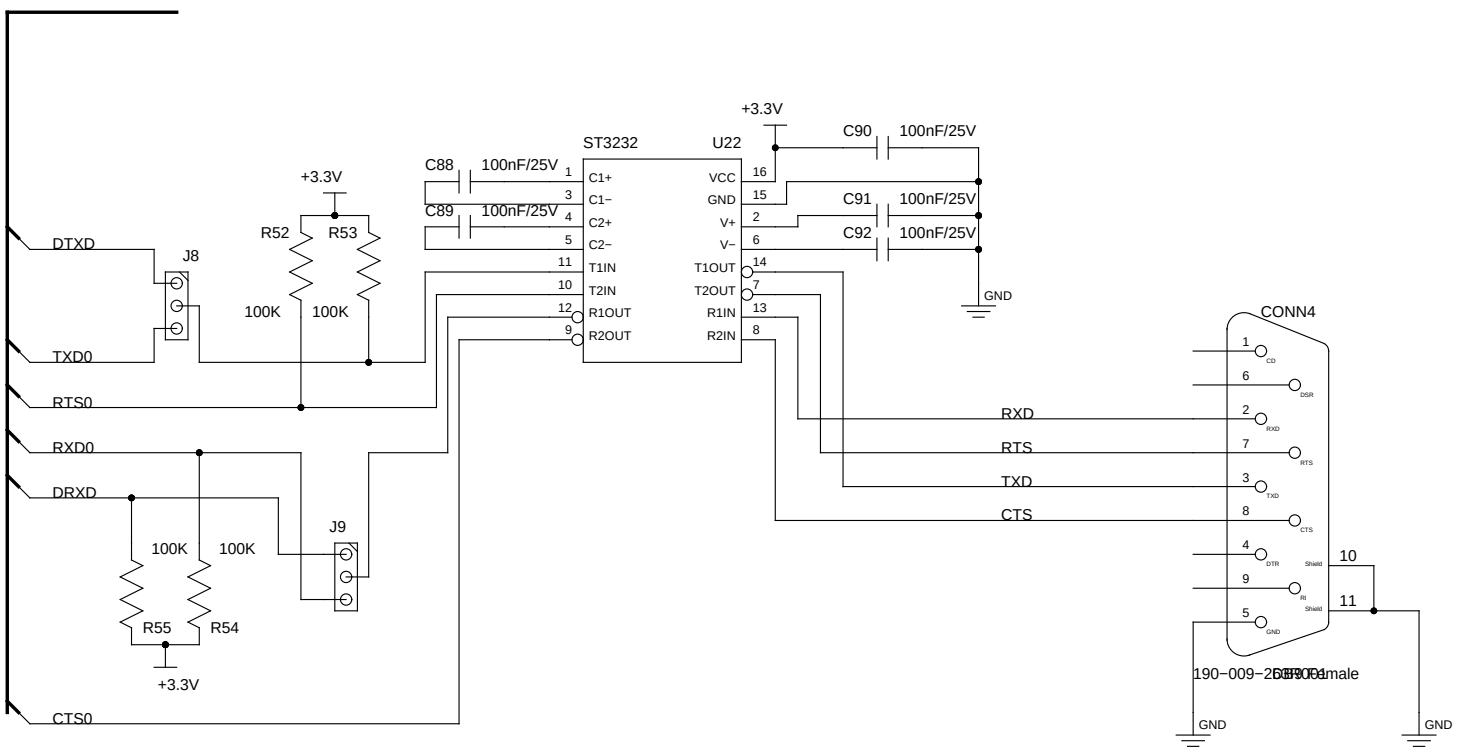


TITLE	JTAG/ICE interface GPLv3	
FILE:		REVISION:
PAGE OF		DRAWN BY: Saritha Kalyanam

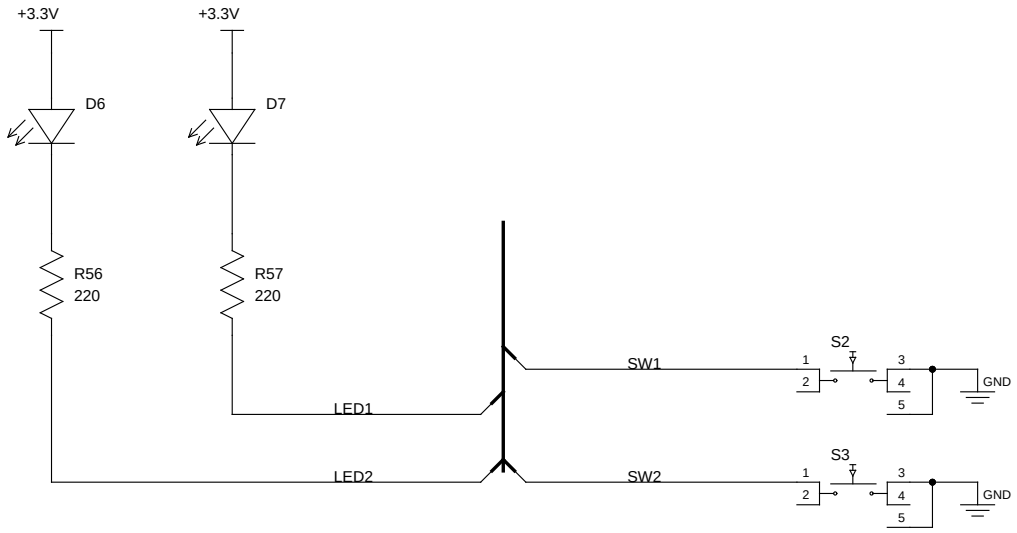


TITLE	SDRAM 32 MB (16−bit mode)		
FILE:		REVISION:	Saritha Kalyanam
PAGE	OF	DRAWN BY:	GPLv3

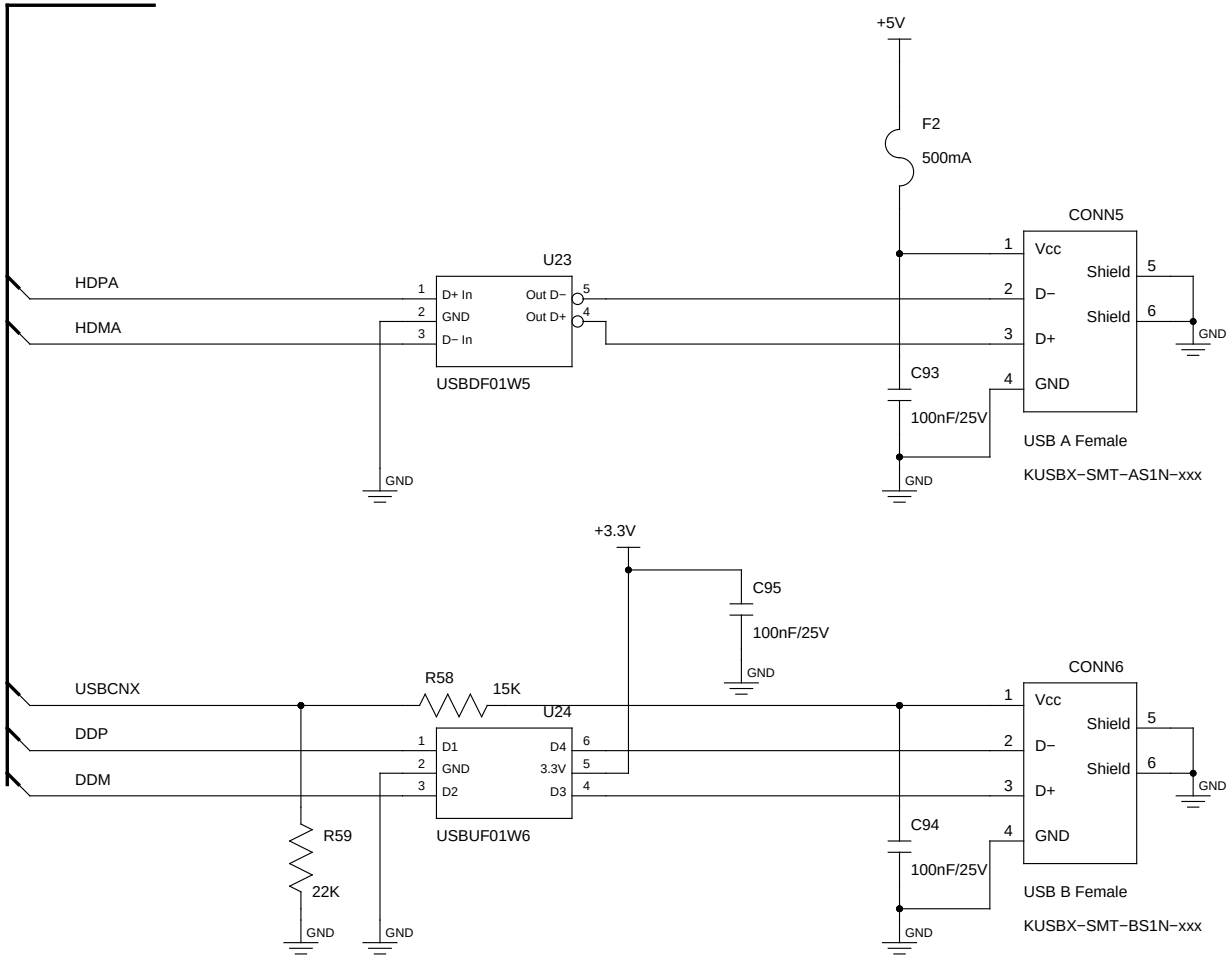

VUNREG
LM2595
U25
C96
C86
100nF/25V
220uF/25V
GND
VIN
GND
ON/OFF
FB
VOUT
L8
68uH/1.5A
D5
MBRS130LT3
C97
100nF/25V
+5V
C87
100uF/10V
TP3
R51
220
GND
D2
+3.3V
TP4
C80
1uF/25V
L6
22uH/1.5A
C81
1uF/25V
C82
68uF/10V
D3
MBRS340T3G
GND
VUNREG
LM2717
U19
C72
C73
C74
4.7uF/25V
100uF/25V
100nF/25V
GND
C78
100nF/25V
C76
R46
20K
4.7nF/25V
C77
1nF/25V
C79
R47
10K
4.7nF/25V
C75
100nF/25V
GND
VIN1
VIN2
VIN3
SS1
VC1
VBG
VC2
SS2
PGND1
PGND2
PGND3
PGND4
FB1
CB1
SW1
CB2
SW2
FB2
SHDN1
SHDN2
FSLCT
AGND1
AGND2
AGND3
GND
R48
22K
C83
1uF/25V
L7
22uH/1.5A
C84
1uF/25V
C85
68uF/10V
D4
MBRS340T3G
GND
+1.8V
TP5
R49
22K
R50
51K
GND
CONN3
RASM722P
TP1
TP2
U20
DF10s
VUNREG
GND
Power Supply
GPLv3
TITLE
FILE:
REVISION:
PAGE
OF
DRAWN BY:
Saritha Kalyanam



TITLE	Serial Port (RS232 and DBGU) GPL v3	
FILE:		REVISION:
PAGE	OF	DRAWN BY: Saritha Kalyanam



UI GPLv3 TITLE	
FILE: PAGE OF	REVISION: DRAWN BY: Saritha Kalyanam



TITLE: USB Host and Device GPLv3	
FILE:	REVISION:
PAGE OF	DRAWN BY: Saritha Kalyanam