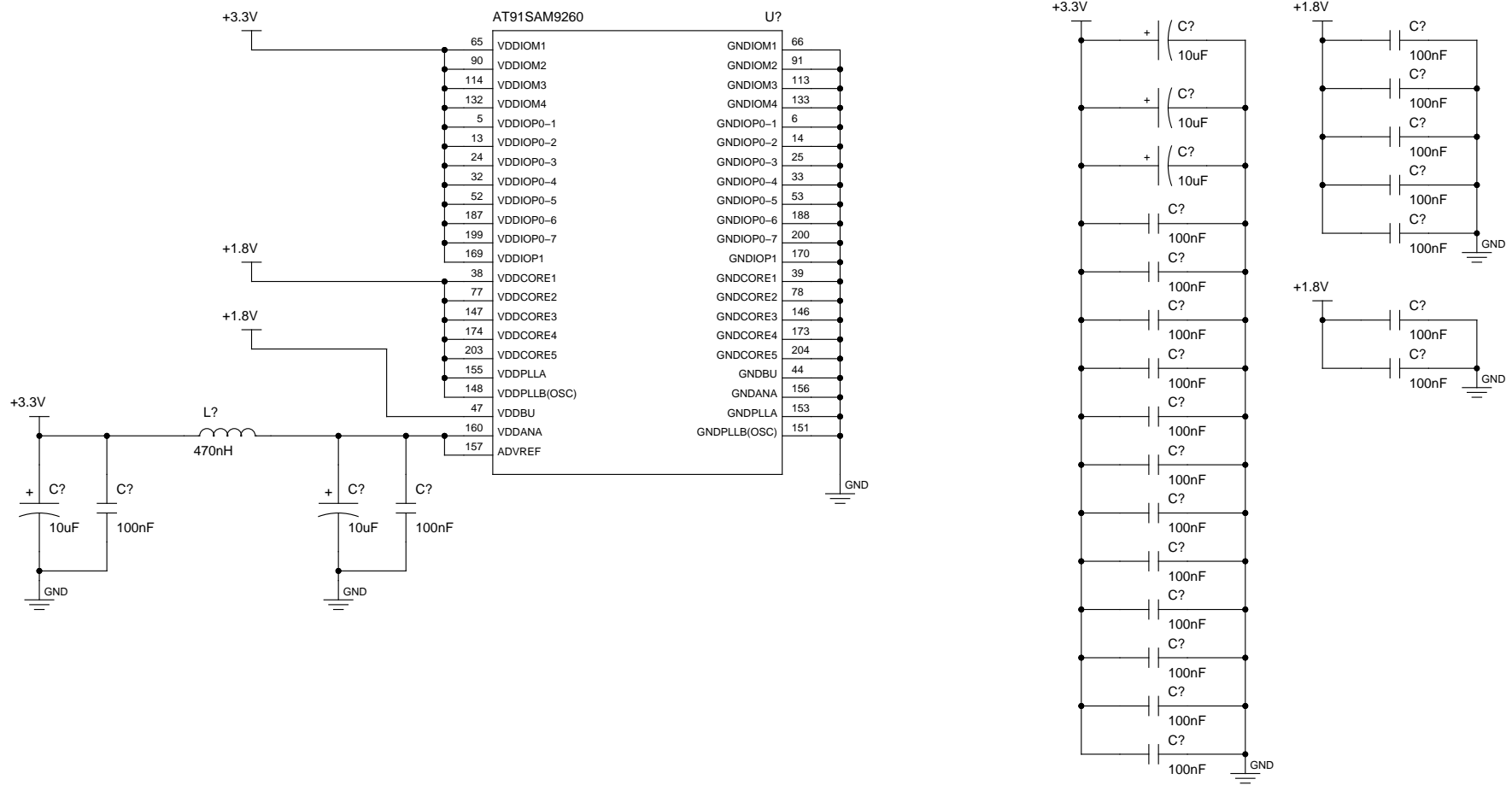
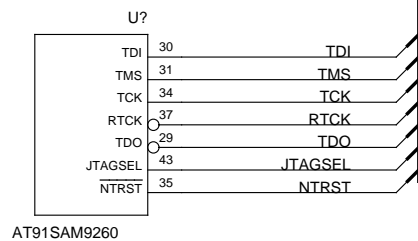
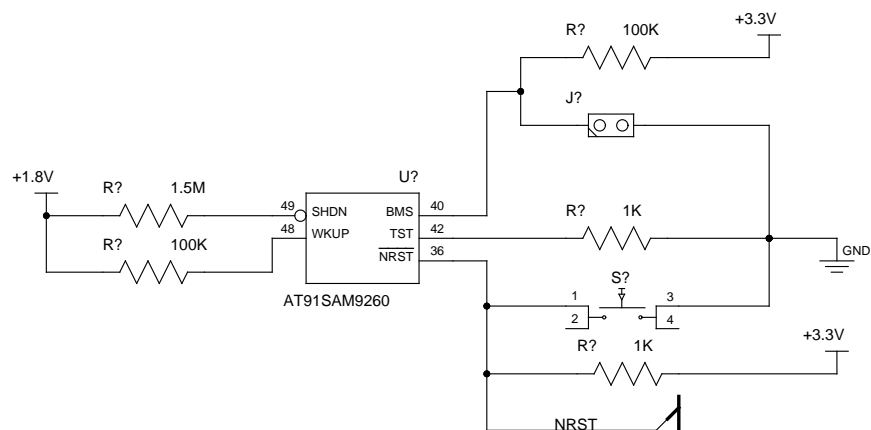
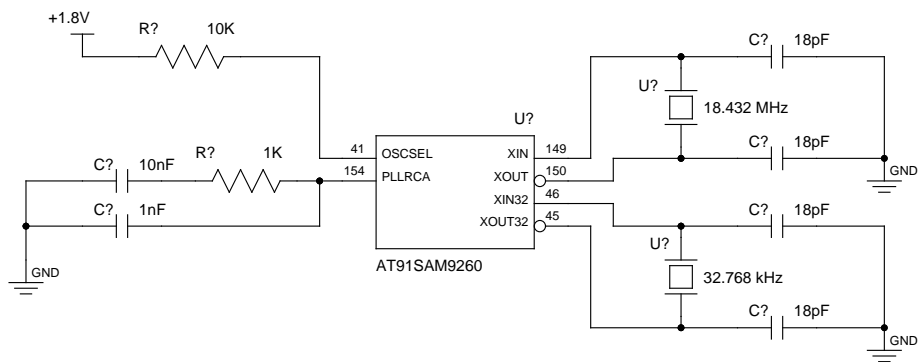




AT91SAM9260 based Telephone ATA Design (GPL v3)		
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Power		
GPLv3		
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Note:

REMAP=0, BMS=1 to boot using AT91SAM9260's internal ROM boot program.  
OSCSEL=1, external slow clock (32.768 KHz)

Clock and JTAG/ICE  
GPLv3

FILE:

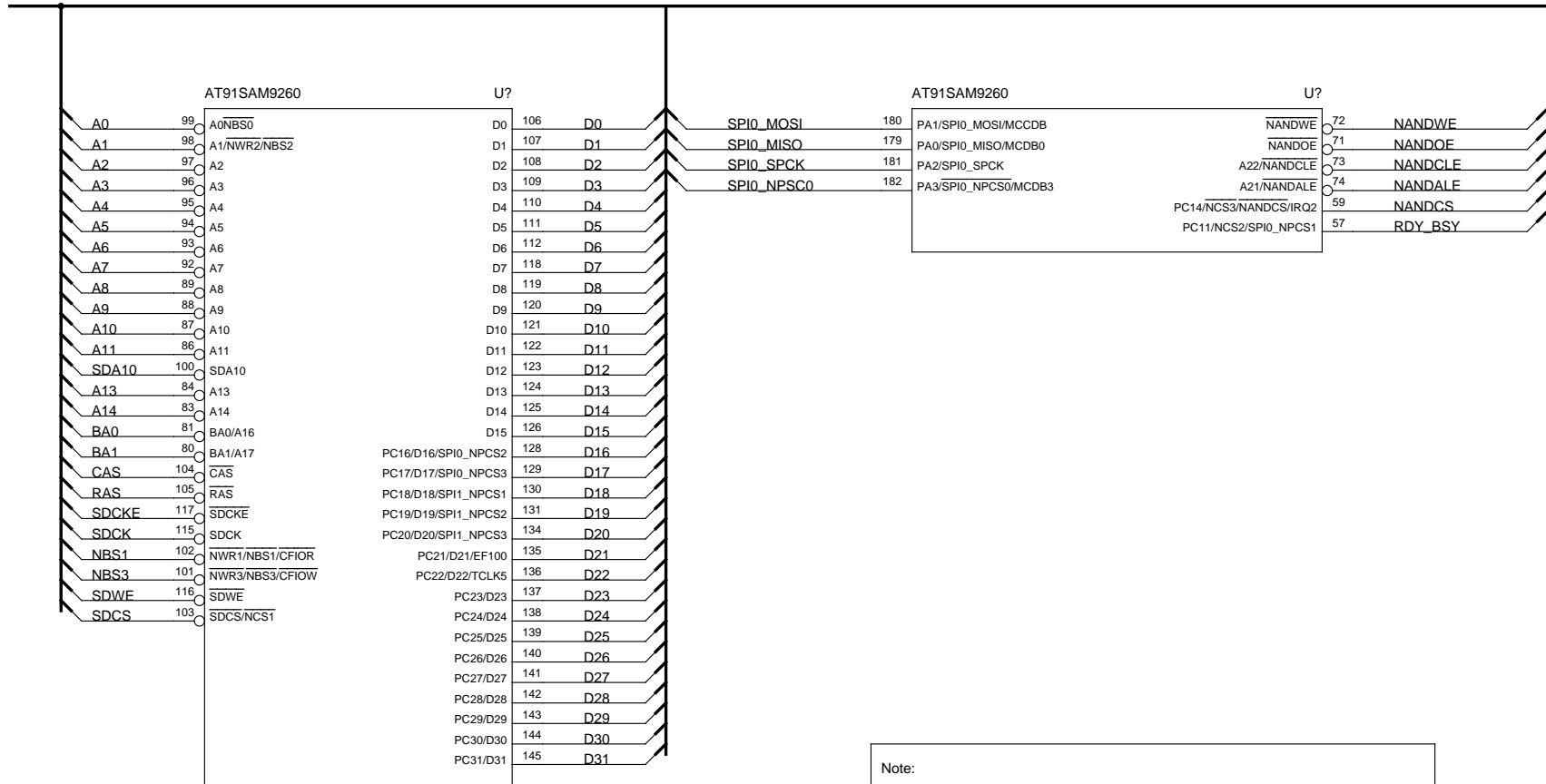
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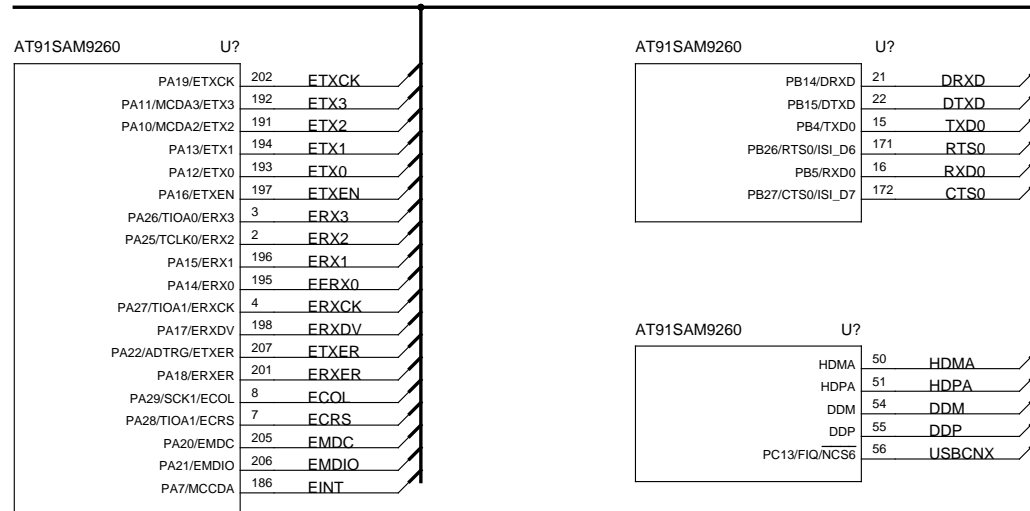
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Note:

PIO\_C11 is used for RDY\_BSY interrupt from the NAND flash.

SDRAM & Serial SPI Data flash & NAND flash			
GPLv3			
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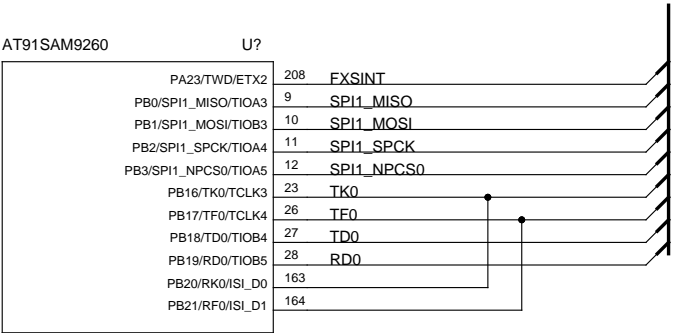


Note:

PIO\_A7 is used for PHY interrupt.

PIO\_C13 is used for 5V detection when plugged into a USB host port.

Ethernet 10/100 & DBGU/Serial & USB Host/Device	
TITLE GPLv3	
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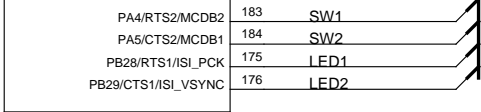
Note:

Clock: TK0 and RK0 are connected together, with RK0 configured as an input.  
FSYNC: TF0 and RF0 are connected together, with RF0 configured as an input.

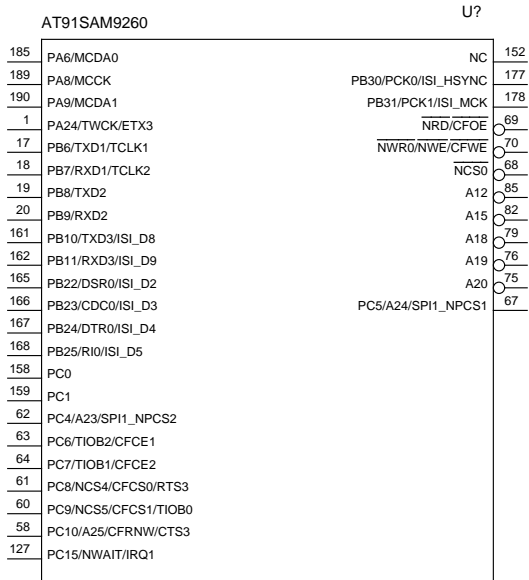
SSC interface (for FXS Si321x)			
GPLv3			
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AT91SAM9260

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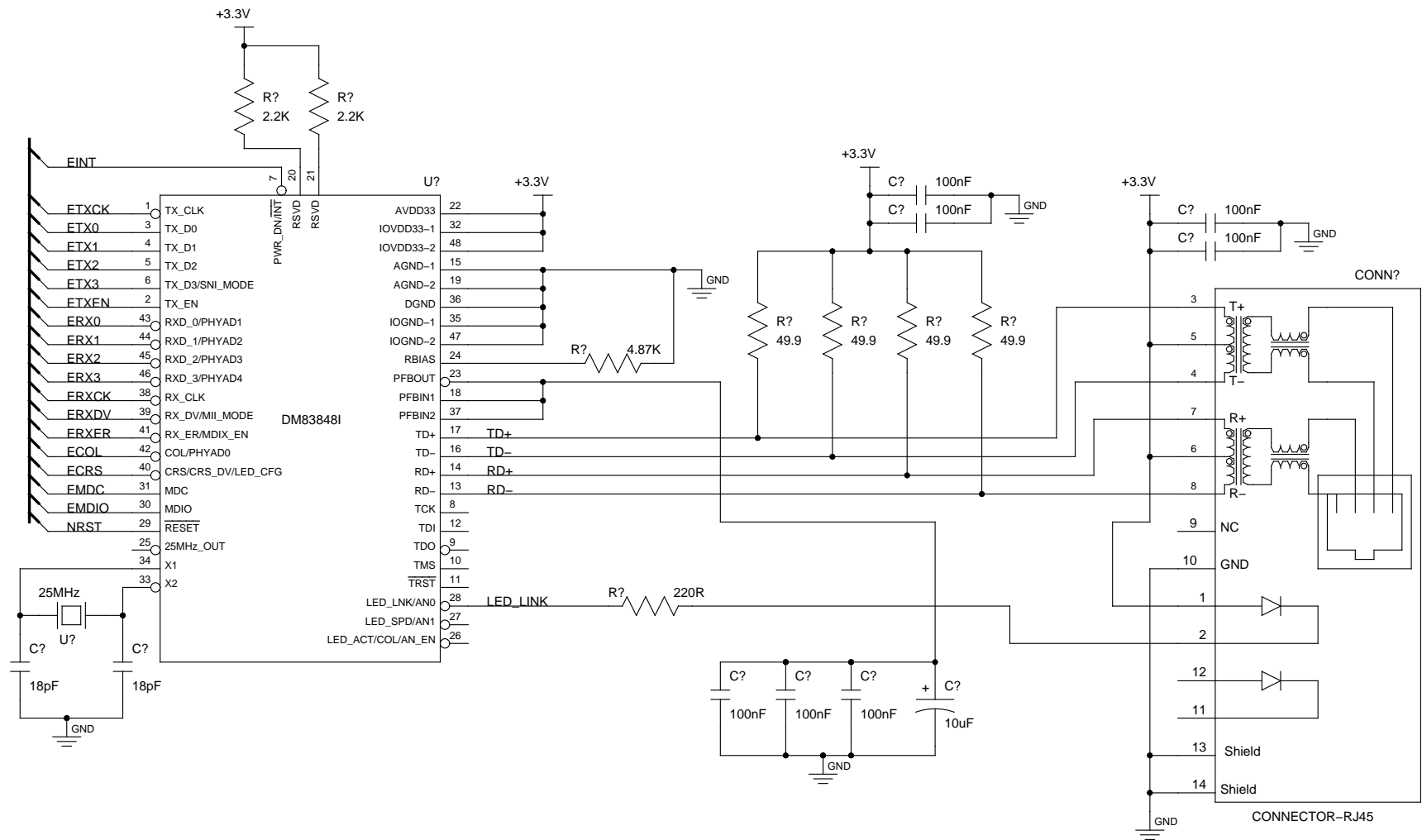


User Interface (LEDs and Interrupts)		
GPLv3		
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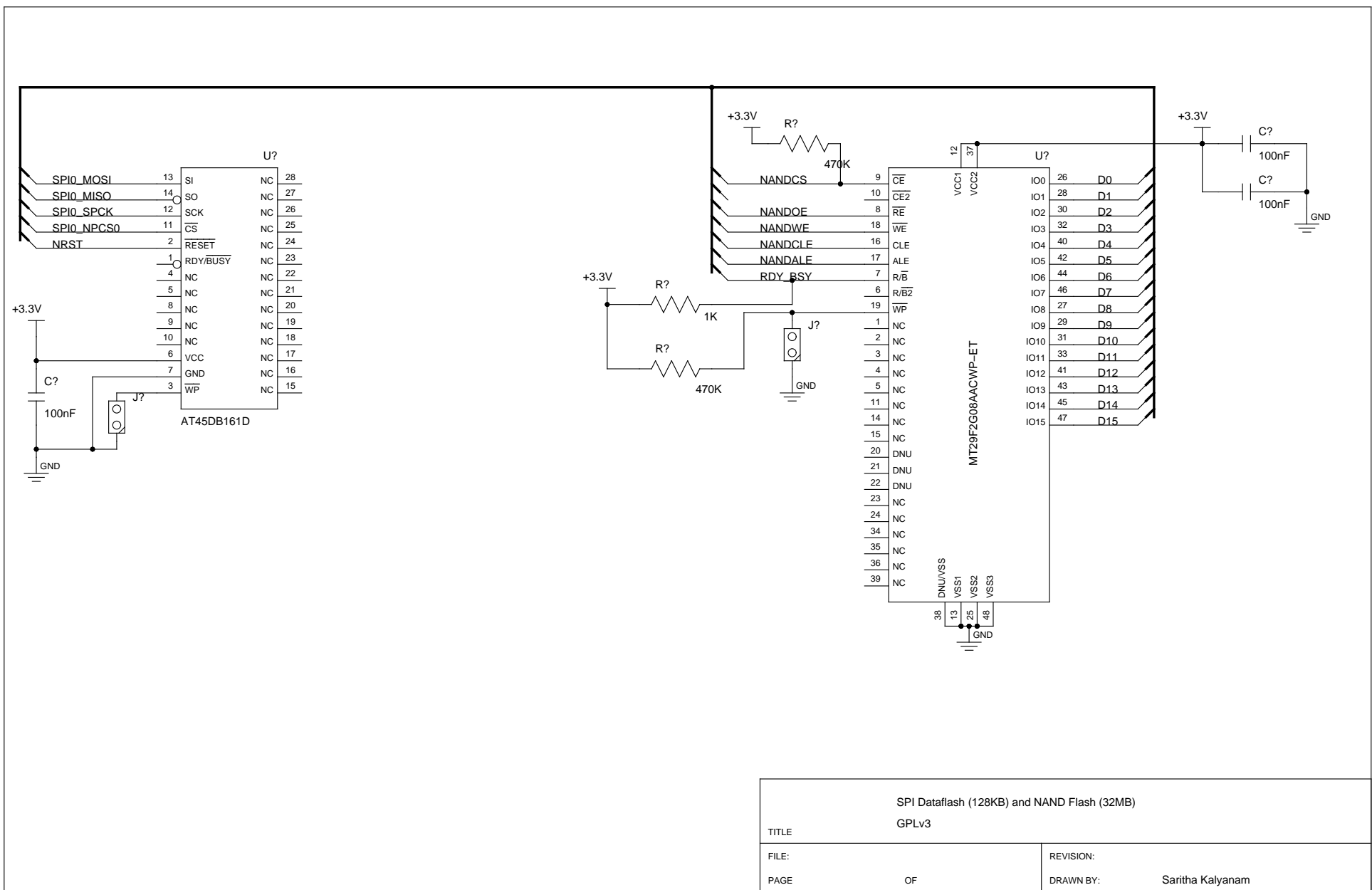


Unconnected pins	
GPLv3	
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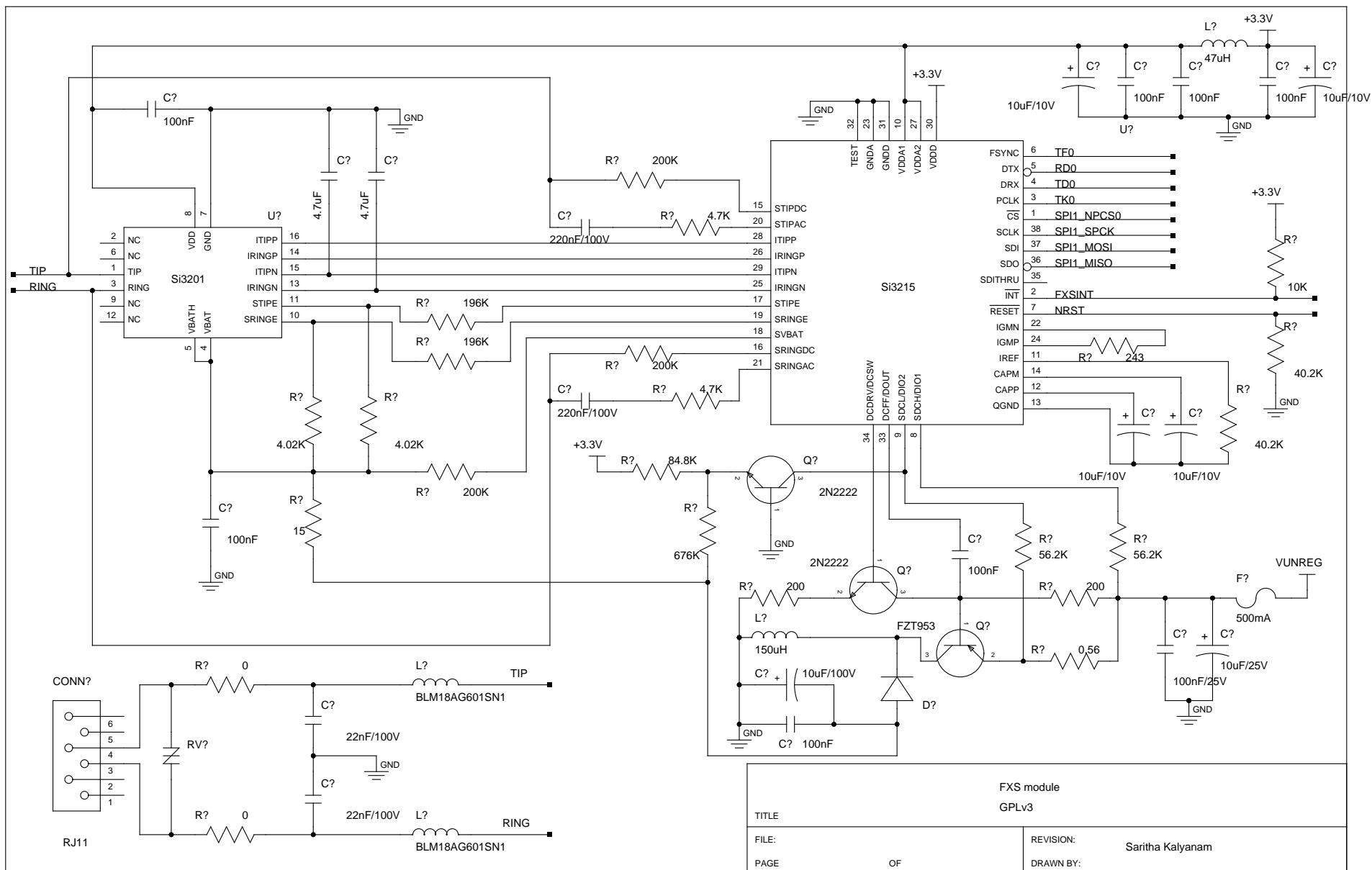


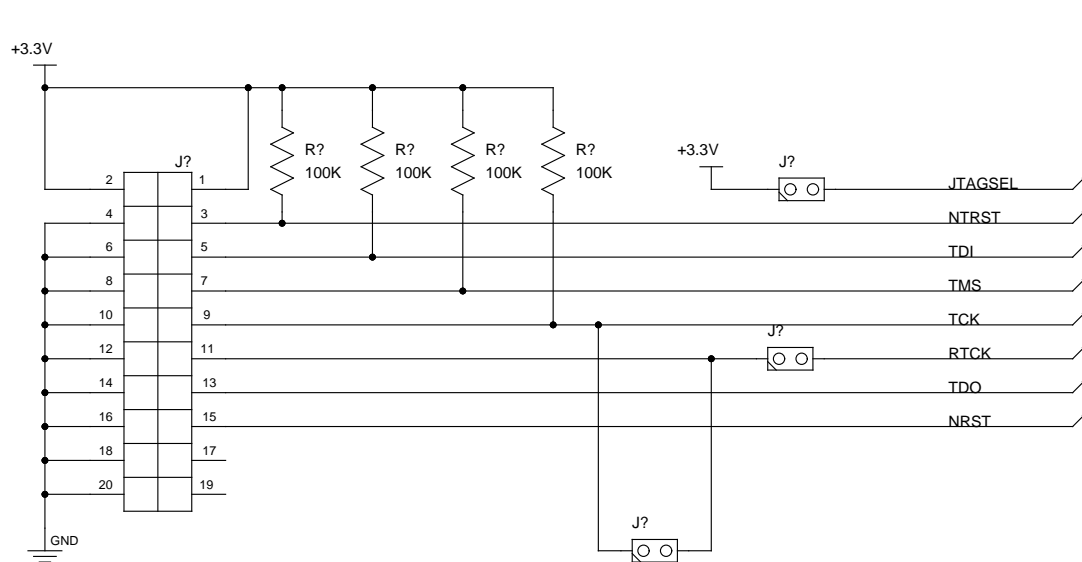


Ethernet 10/100		
GPLv3		
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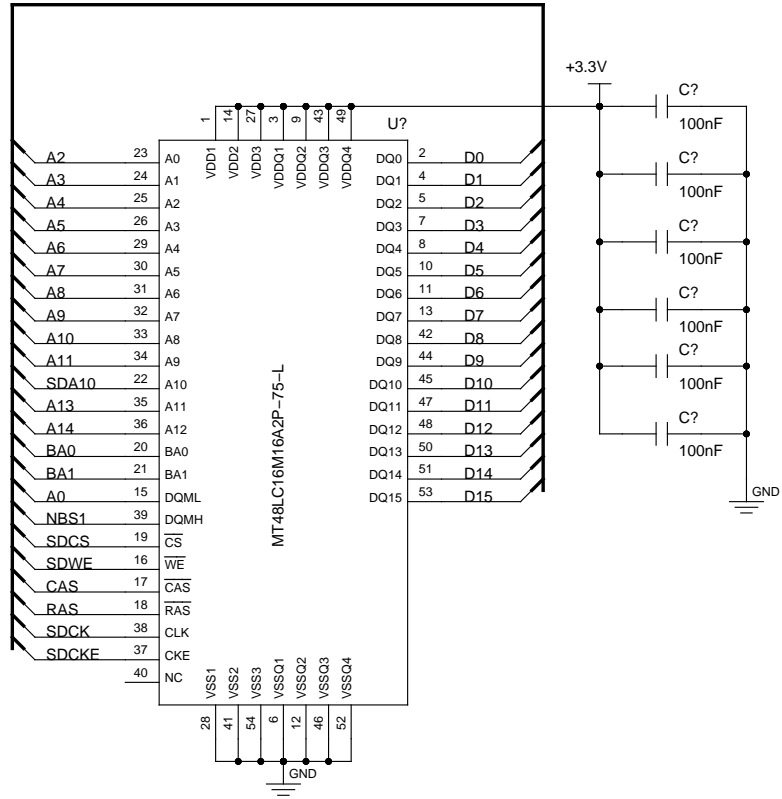


SPI Dataflash (128KB) and NAND Flash (32MB)		
GPLv3		
TITLE		
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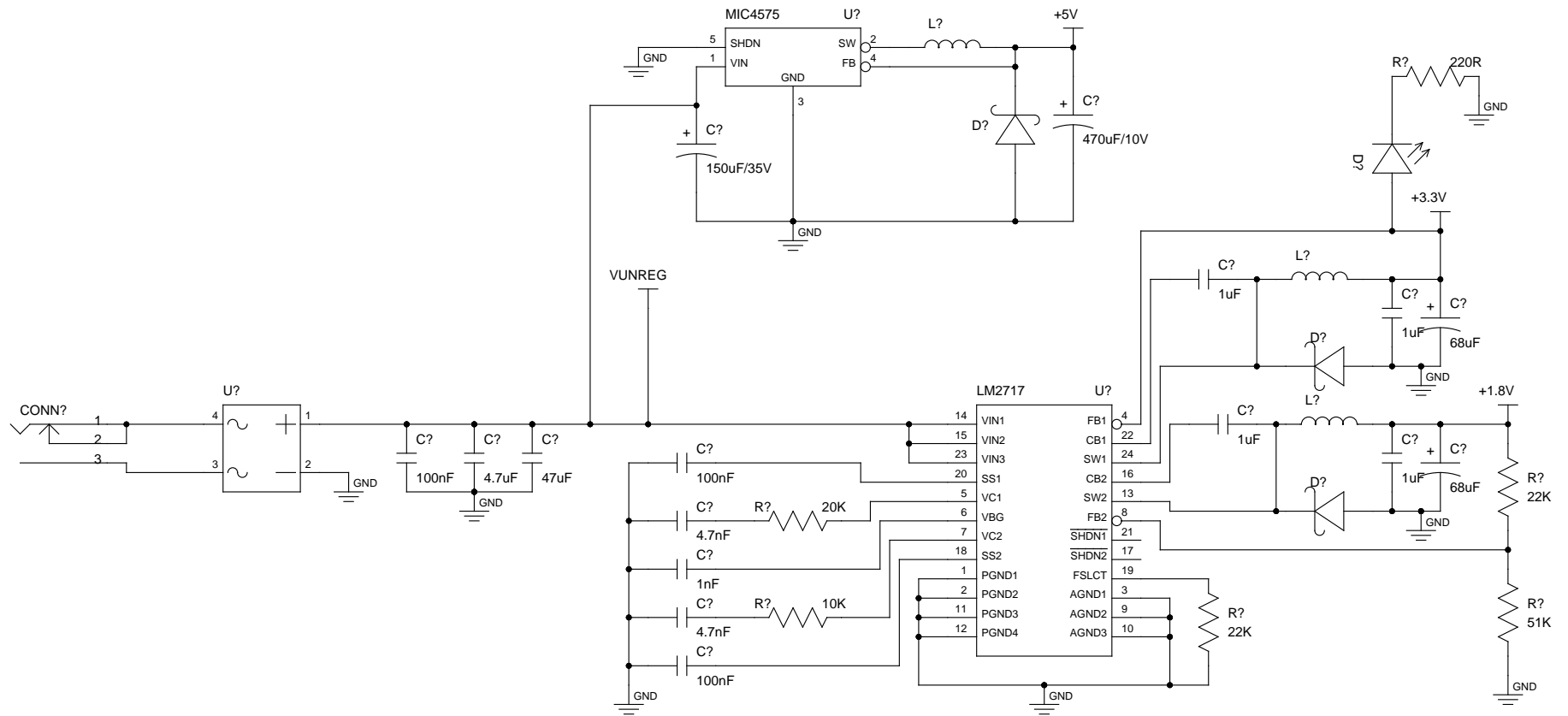




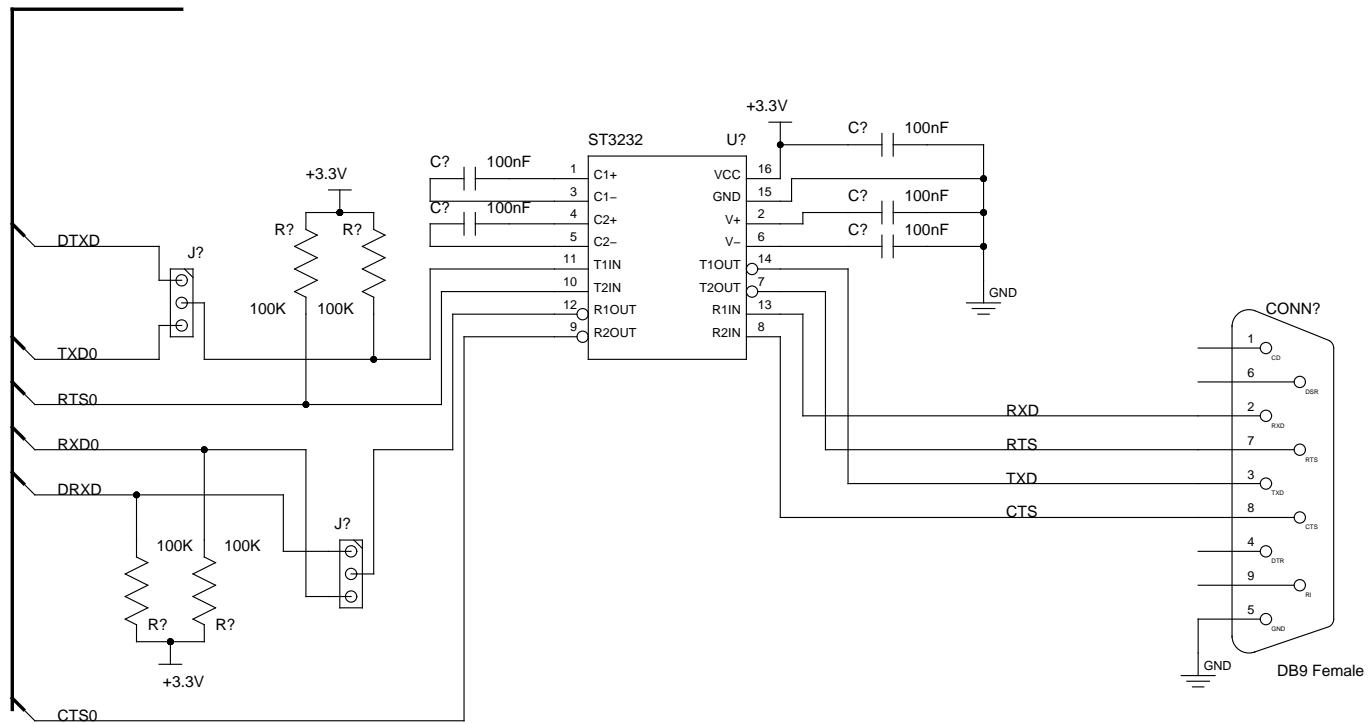
TITLE		JTAG/ICE interface GPLv3	
FILE:		REVISION:	
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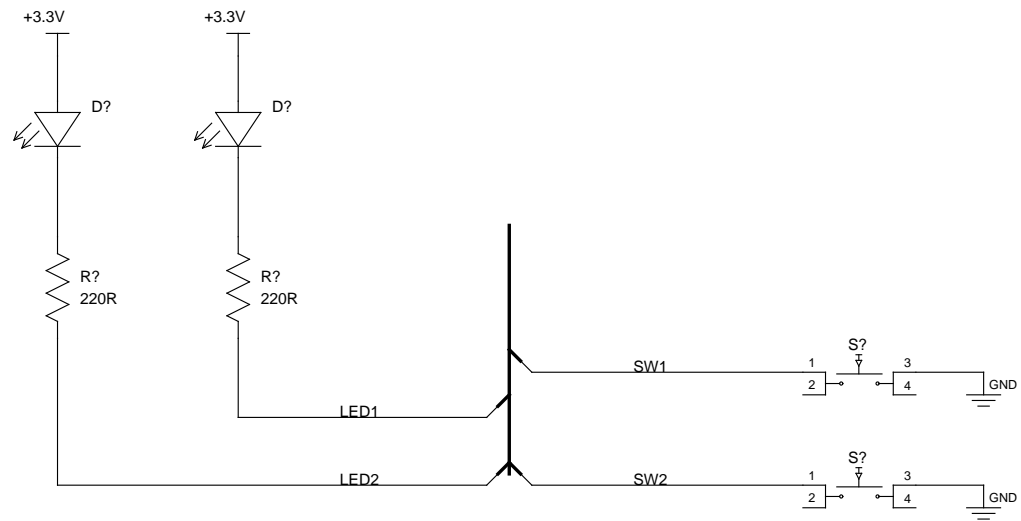
SDRAM	
32 MB (16-bit mode)	
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Power Supply		
GPLv3		
TITLE		
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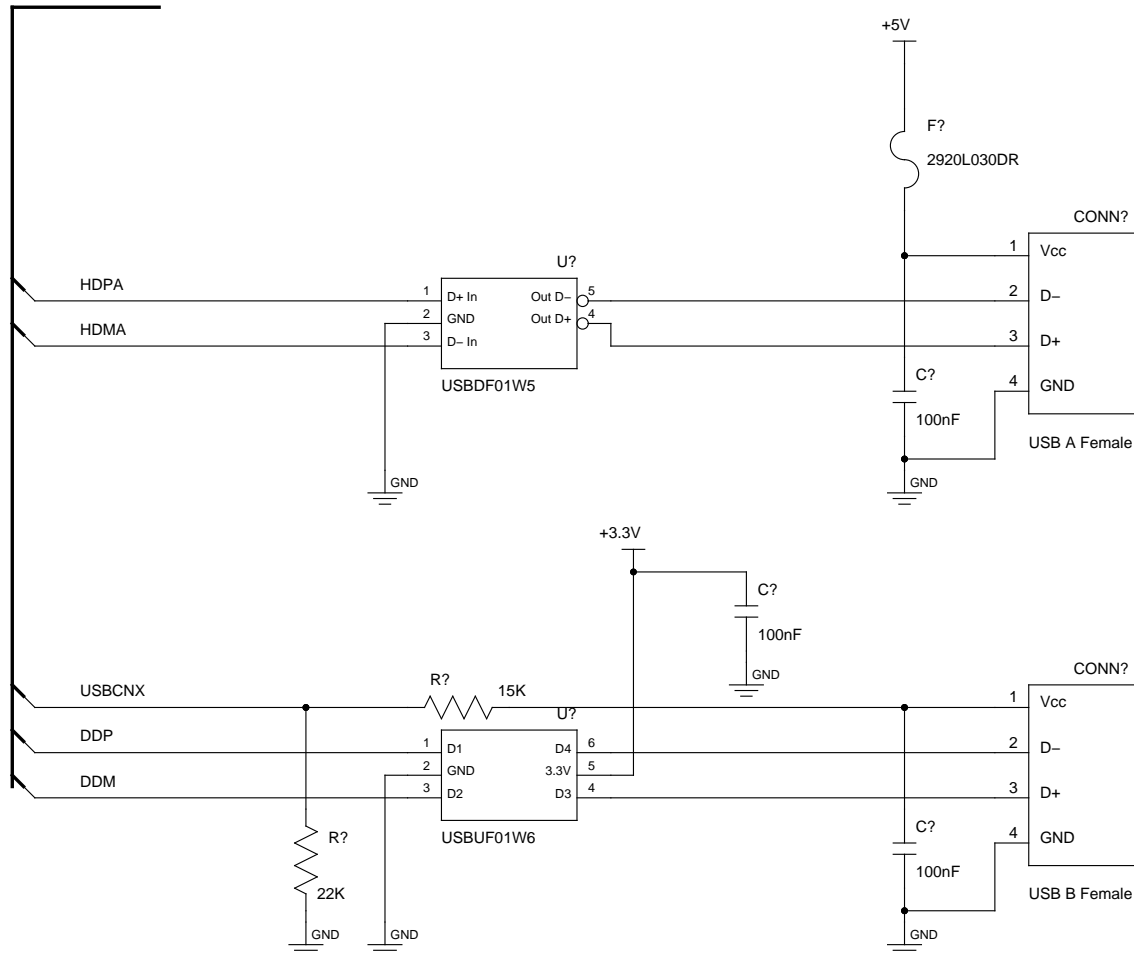


Serial Port (RS232 and DBGU)		
GPL v3		
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USB Host and Device	
GPLv3	
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