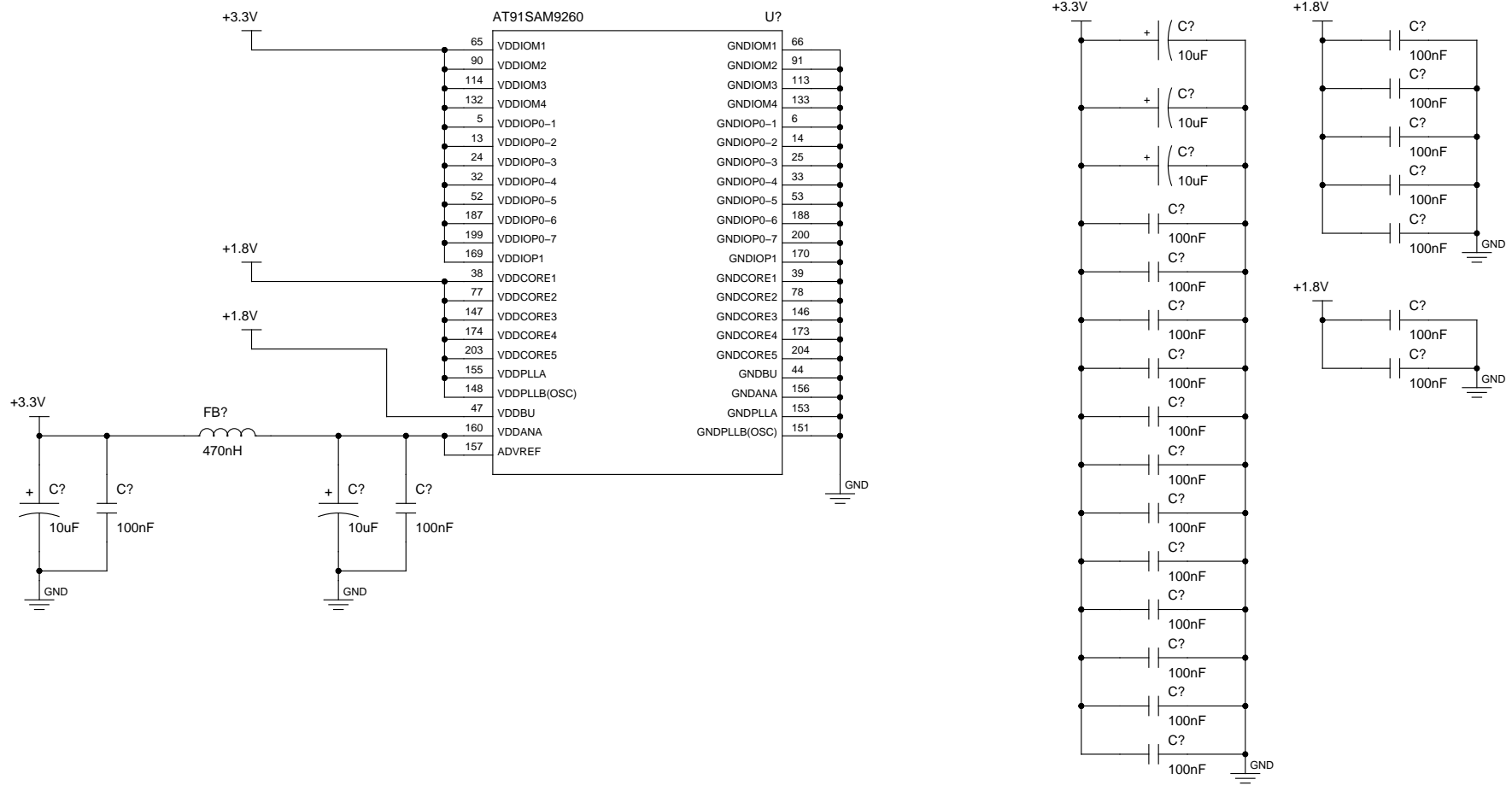
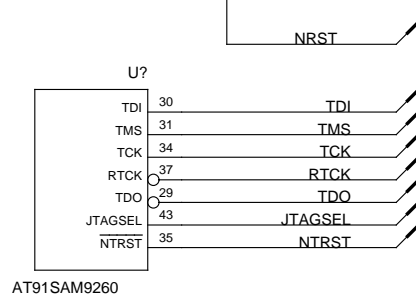
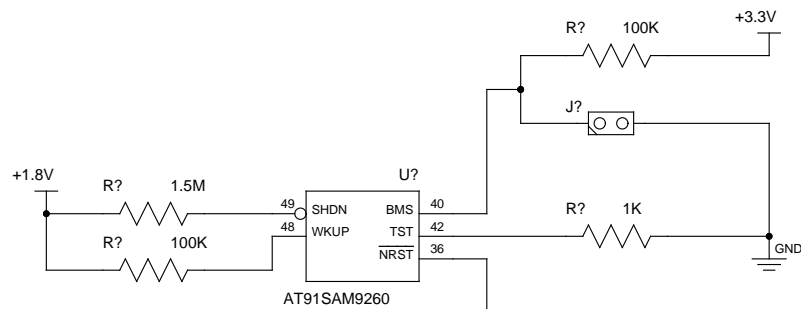
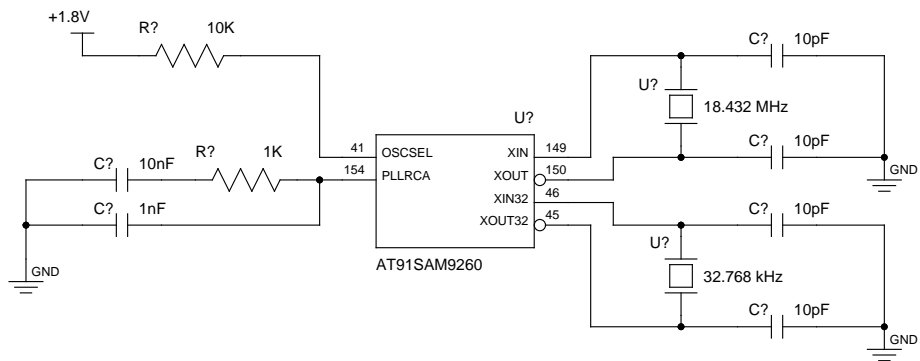




AT91SAM9260 based Telephone ATA Design (GPL v3)		
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Power		
GPLv3		
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Note:

REMAP=0, BMS=1 to boot using AT91SAM9260's internal ROM boot program.
OSCSEL=1, external slow clock (32.768 KHz)

Clock and JTAG/ICE
GPLv3

FILE:

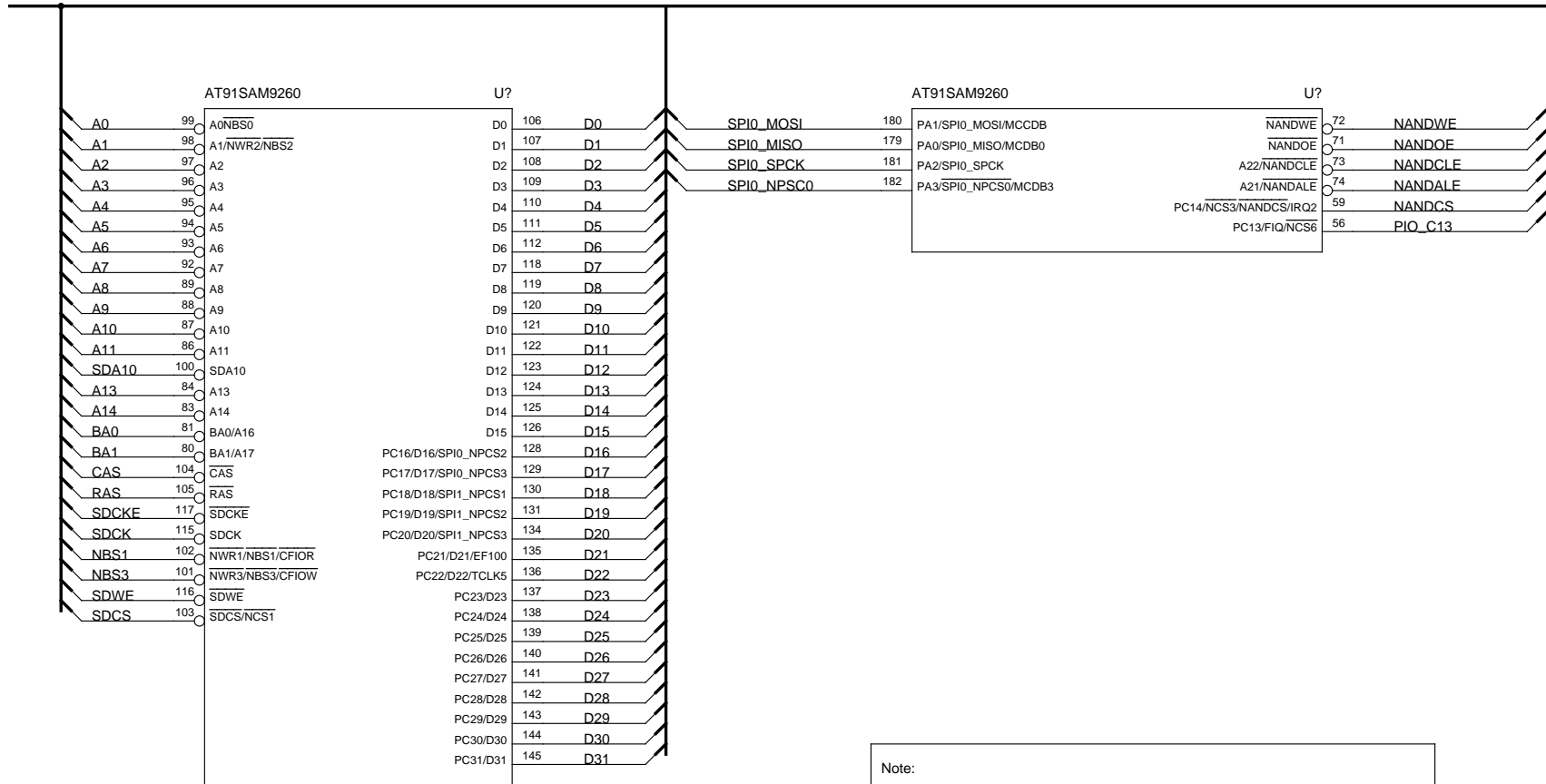
PAGE

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Note:

PIO_C13 is used for RDY_BSY interrupt from the NAND flash.

SDRAM & NAND flash

GPLv3

TITLE

FILE:

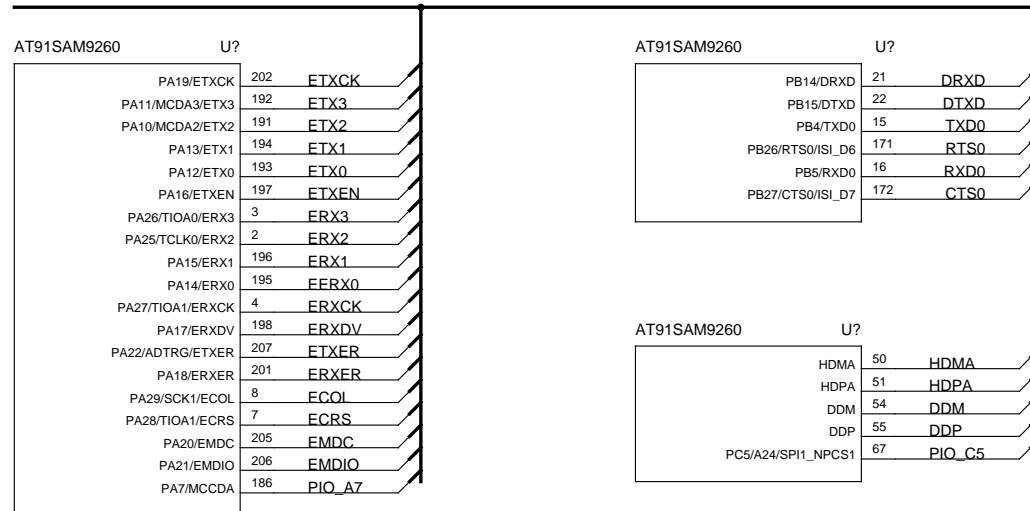
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Note:

PIO_A7 is used for PHY interrupt.

PIO_C5 is used for 5V detection when plugged into a USB host port.

Ethernet 10/100 & DBGU/Serial & USB Host/Device	
TITLE GPLv3	
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AT91SAM9260

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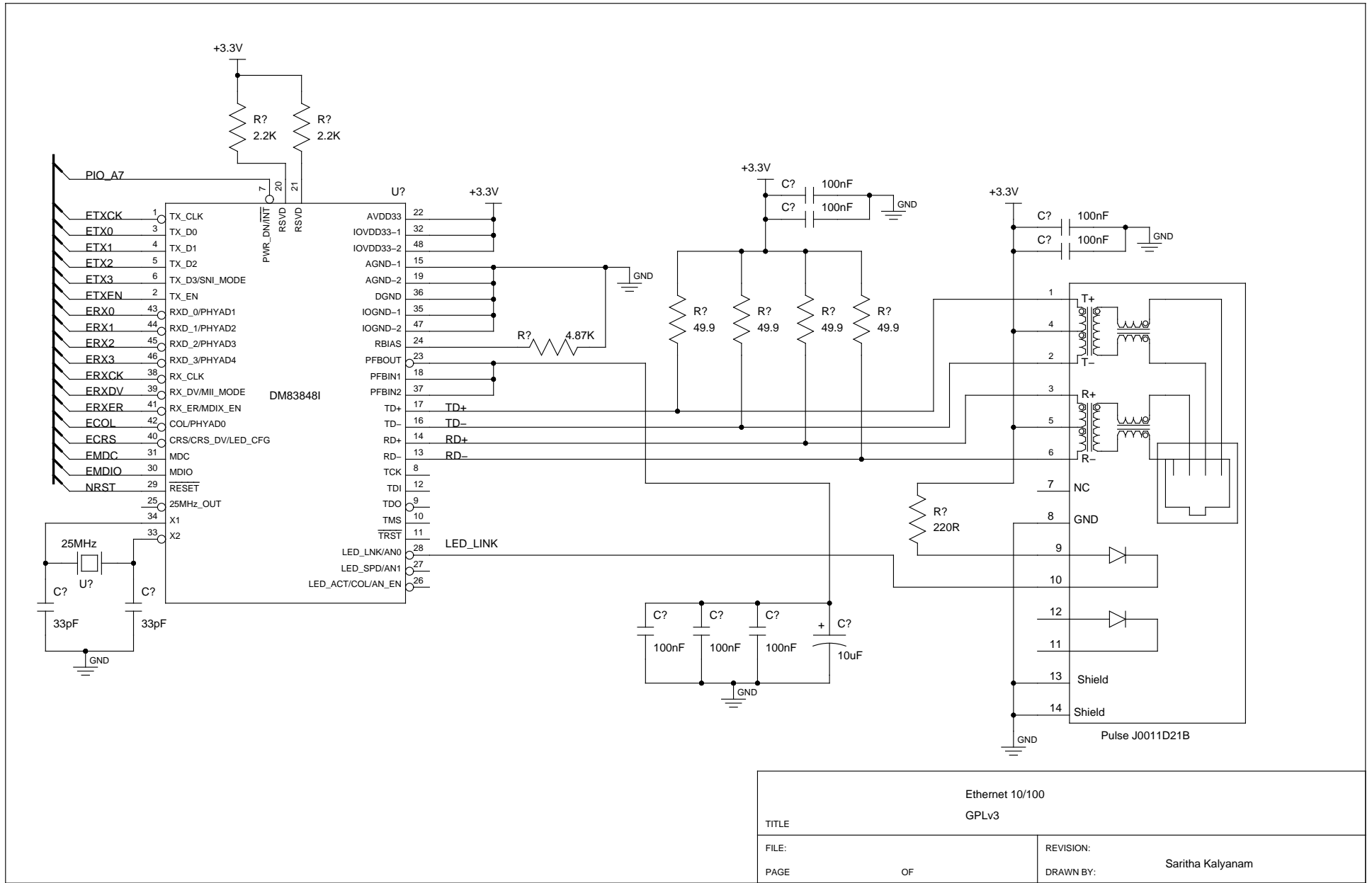
PA8/MCCK	189	PIO_A8
PA9/MCDA1	190	PIO_A9
PB30/PCK0/ISI_HSYNC	177	PIO_B30
PB31/PCK1/ISI_MCK	178	PIO_B31

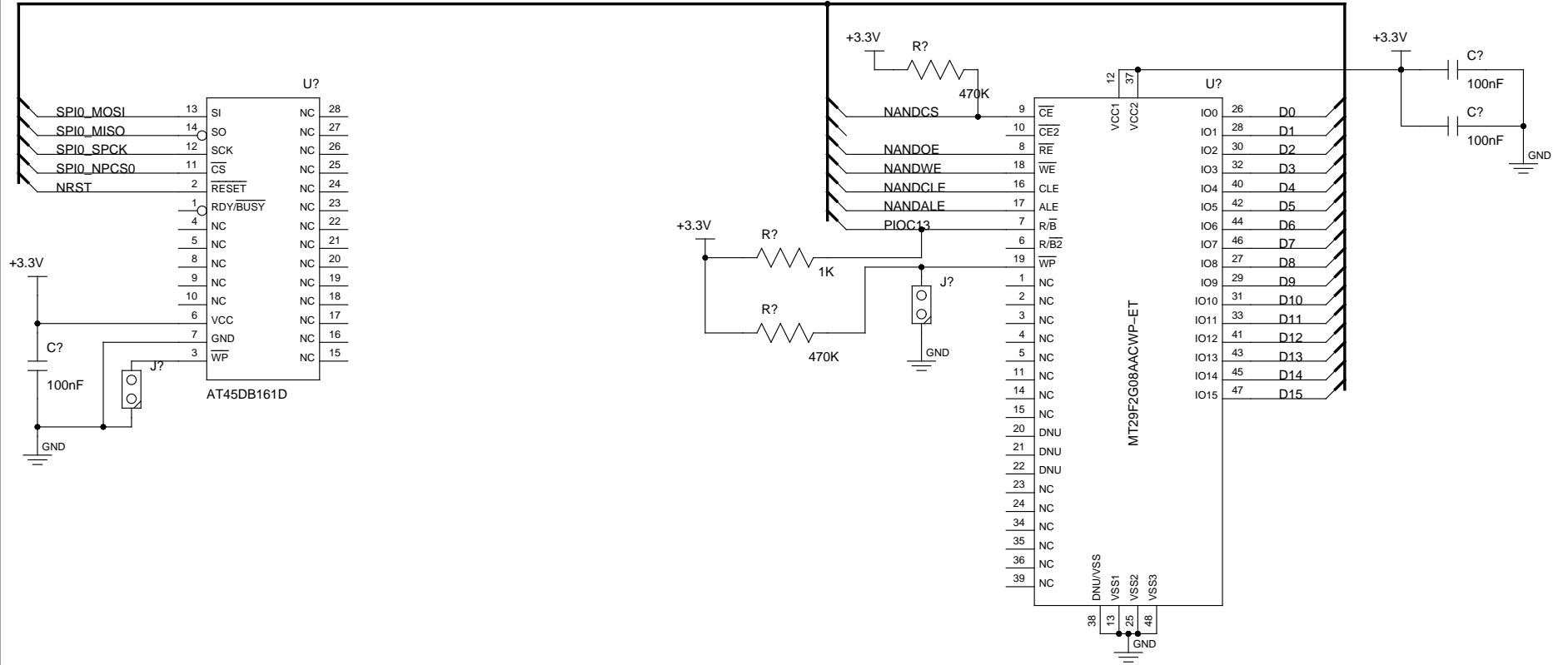
User Interface (LEDs and Interrupts)	
GPLv3	
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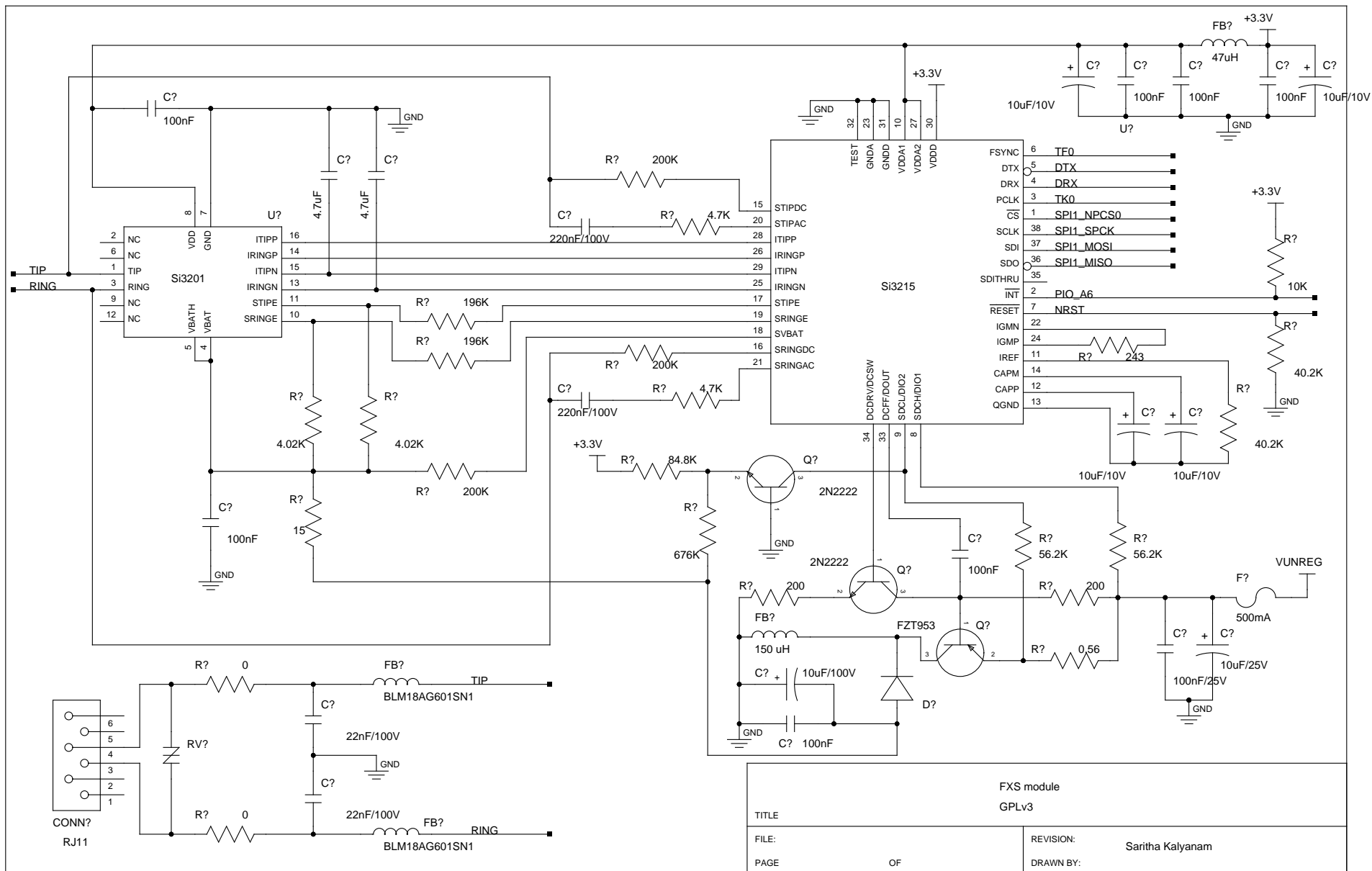
AT91SAM9260		
183	PA4/RTS2/MCDB2	NC 152
184	PA5/CTS2/MCDB1	NRD/CFOE 69
208	PA23/TWD/ETX2	NWR0/NWE/CFWE 70
1	PA24/TWCK/ETX3	NCS0 68
17	PB6/TXD1/TCLK1	A12 85
18	PB7/RXD1/TCLK2	A15 82
19	PB8/TXD2	A18 79
20	PB9/RXD2	A19 76
161	PB10/TXD3/ISL_D8	A20 75
162	PB11/RXD3/ISL_D9	
165	PB22/DSR0/ISL_D2	
166	PB23/CDC0/ISL_D3	
167	PB24/DTR0/ISL_D4	
168	PB25/RIO/ISL_D5	
175	PB28/RTS1/ISL_PCK	
176	PB29/CTS1/ISL_VSYNC	
158	PC0	
159	PC1	
62	PC4/A23/SPI1_NPCS2	
63	PC6/TIOB2/CFCE1	
64	PC7/TIOB1/CFCE2	
61	PC8/NCS4/CFCS0/RTS3	
60	PC9/NCS5/CFCS1/TIOB0	
58	PC10/A25/CFRNW/CTS3	
57	PC11/NCS2/SPI0_NPCS1	
127	PC15/NWAIT/IRQ1	

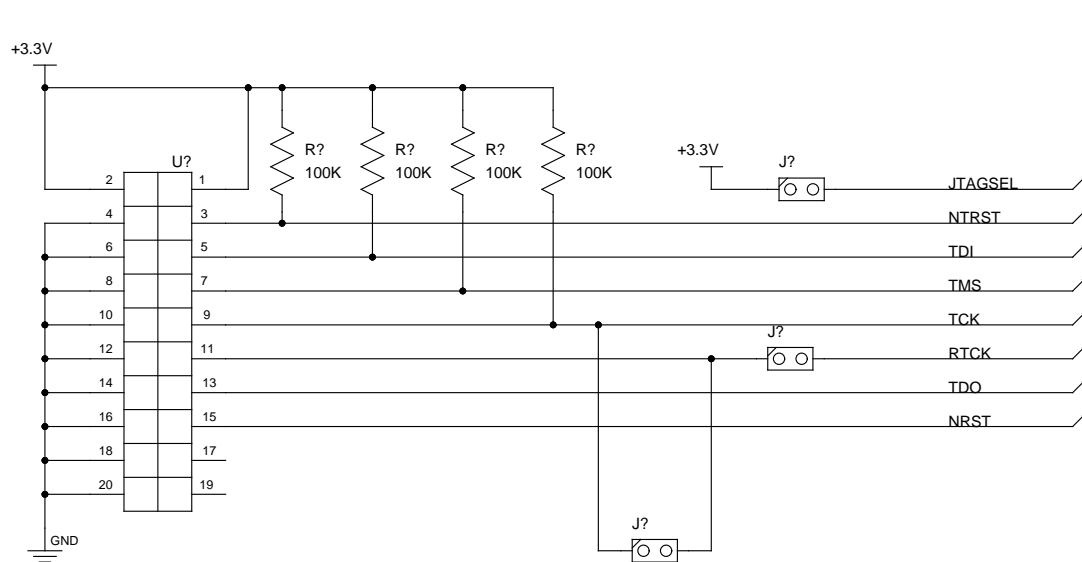
Unconnected pins		
GPLv3		
TITLE		
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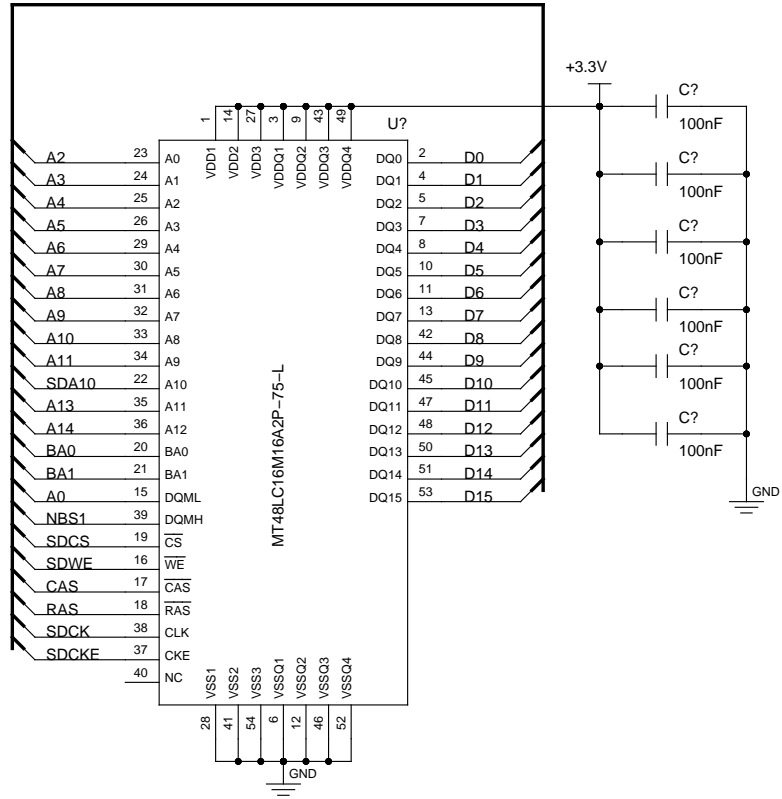


TITLE SPI Dataflash (128KB) and NAND Flash (32MB) GPLv3		
FILE:		REVISION:
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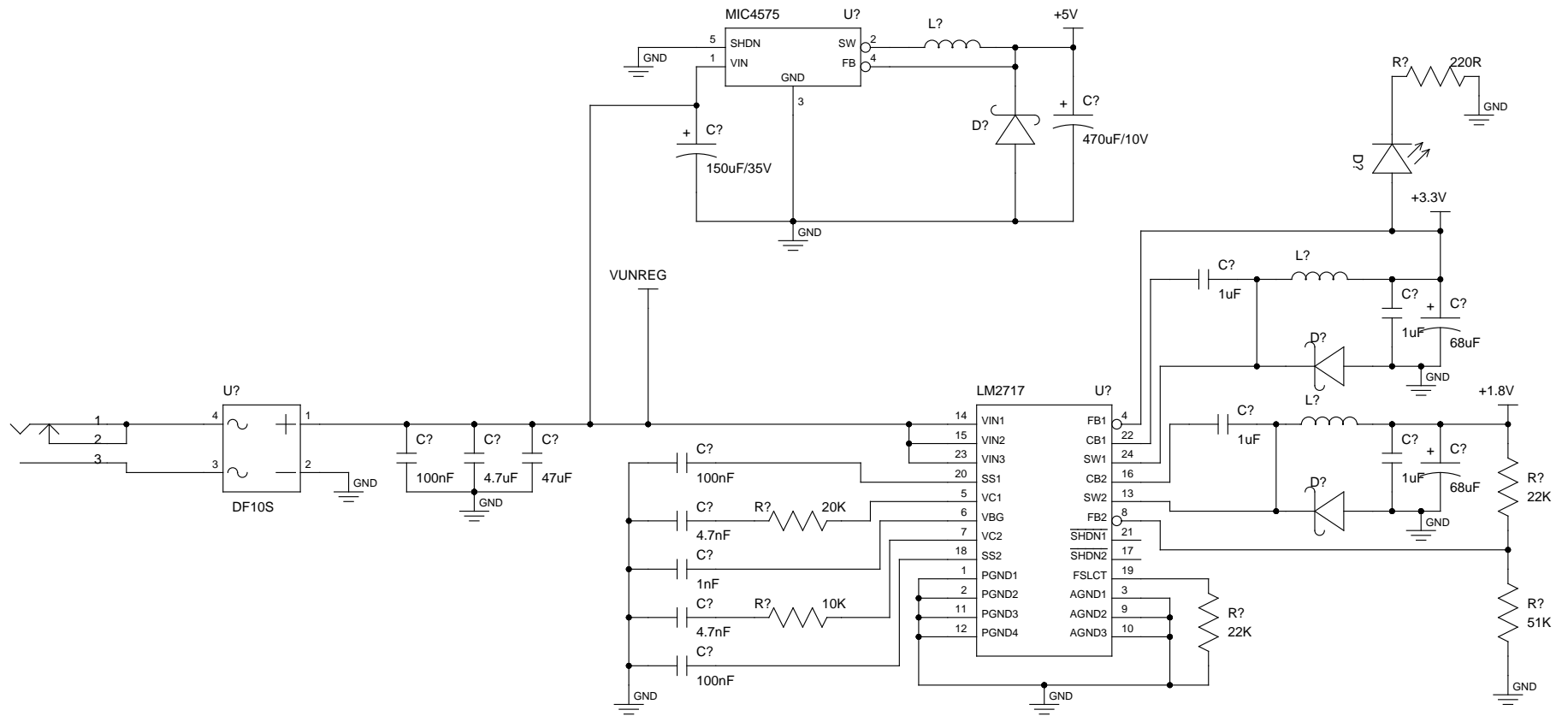




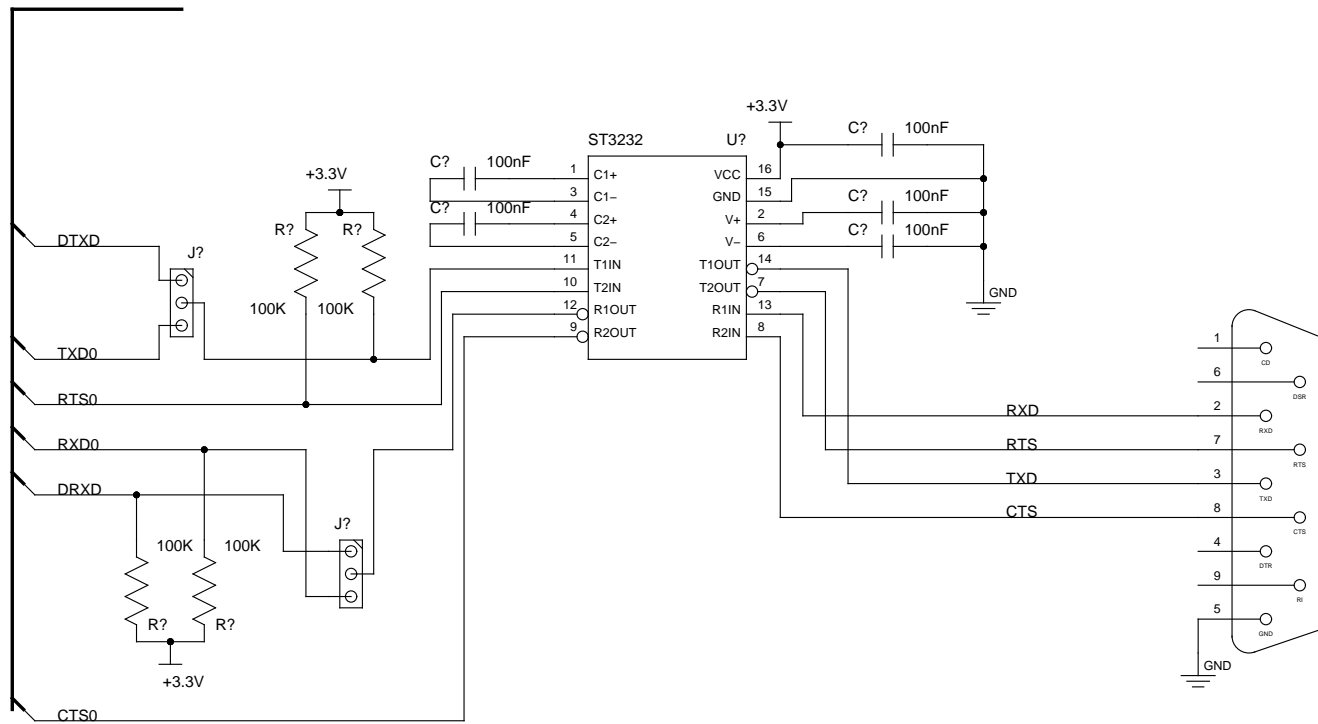
TITLE		JTAG/ICE interface GPLv3	
FILE:		REVISION:	
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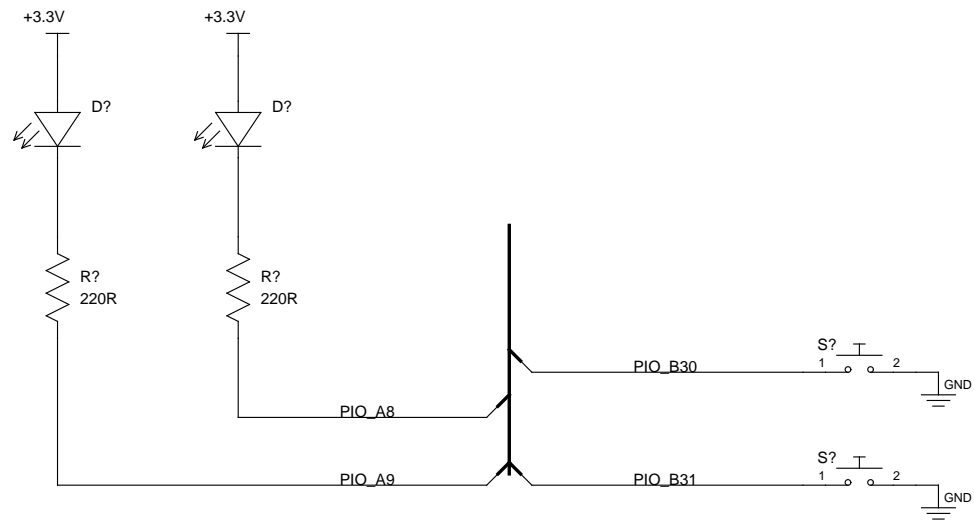
TITLE		SDRAM 32 MB (16-bit mode)	
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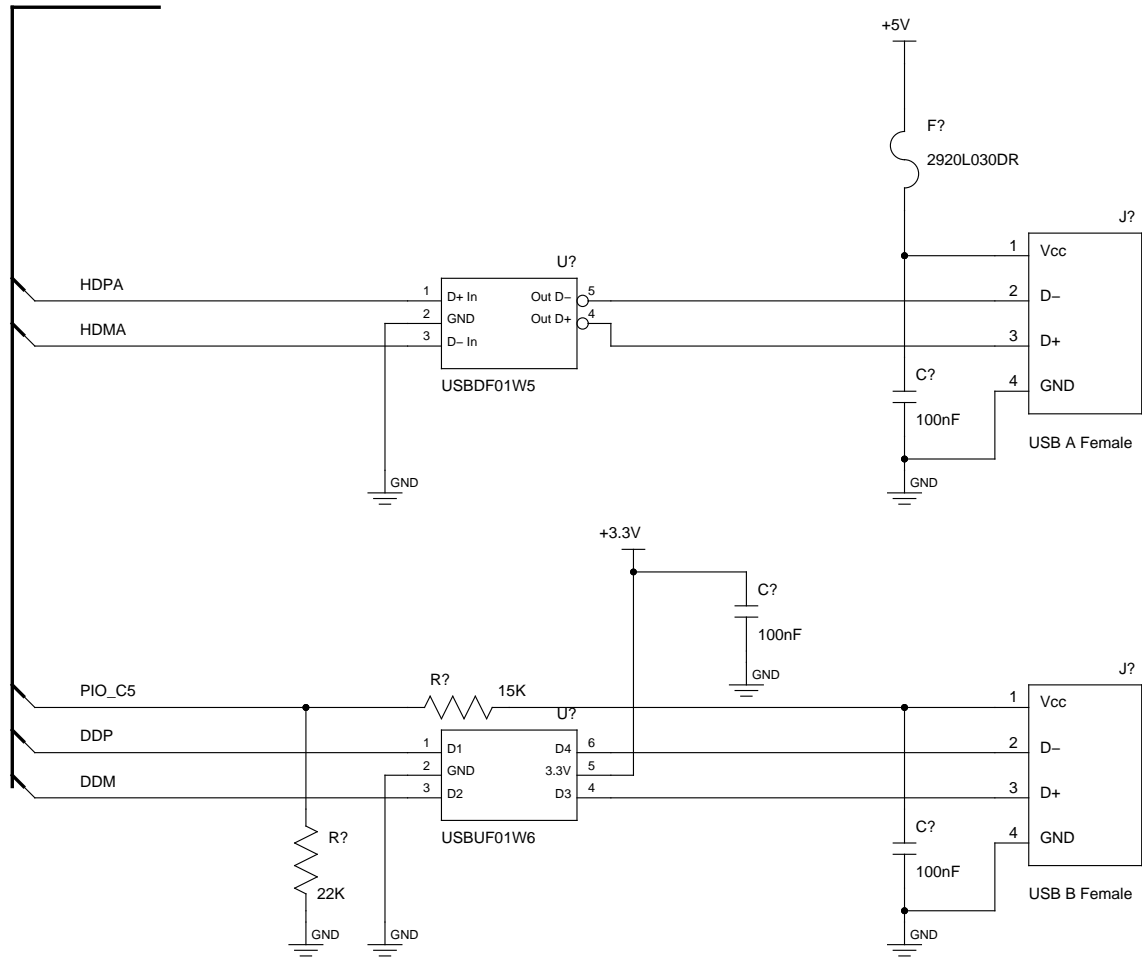
Power Supply		
GPLv3		
TITLE		
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Serial Port (RS232 and DBGU)			
GPL v3			
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USB Host and Device		
GPLv3		
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