In [1]:
```
! python --version
```

```
Python 3.6.9
```

In [2]:
```
! pip freeze | grep -P '(torch|numpy)'
```

```
numpy==1.19.2
torch==1.8.1+cu101
torchvision==0.9.1+cu101
```

In [3]:
```
! lscpu
```

```
Architecture:        x86_64
CPU op-mode(s):      32-bit, 64-bit
Byte Order:          Little Endian
CPU(s):              4
On-line CPU(s) list: 0-3
Thread(s) per core:  1
Core(s) per socket:  4
Socket(s):           1
NUMA node(s):        1
Vendor ID:           AuthenticAMD
CPU family:          23
Model:               49
Model name:          AMD EPYC 7V12 64-Core Processor
Stepping:            0
CPU MHz:             3217.623
BogoMIPS:            4890.81
Hypervisor vendor:   Microsoft
Virtualization type: full
L1d cache:           32K
L1i cache:           32K
L2 cache:            512K
L3 cache:            16384K
NUMA node0 CPU(s):   0-3
Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca
cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1g
b rdtscp lm constant_tsc rep_good nopl tsc_reliable nonstop_tsc cpuid extd_ap
icid aperfmperf pni pclmulqdq ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes x
save avx f16c rdrand hypervisor lahf_lm cmp_legacy cr8_legacy abm sse4a misal
ignsse 3dnowprefetch osvw topoext ssbd vmmcall fsgsbase bmi1 avx2 smep bmi2 r
dseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves clzero x
saveerptr arat umip rdpid
```

In [4]:
```
! nvcc --version
```

```
nvcc: NVIDIA (R) Cuda compiler driver
Copyright (c) 2005-2019 NVIDIA Corporation
Built on Sun_Jul_28_19:07:16_PDT_2019
Cuda compilation tools, release 10.1, V10.1.243
```

In [5]:
```
! nvidia-smi --query-gpu=name,driver_version --format=csv
```

```
name, driver_version
Tesla T4, 460.27.04
```

In [6]:
```
import torch
```

```
torch.set_printoptions(precision=11)
```

In [7]:
```
print(torch.__config__.show())
```

```
PyTorch built with:
  - GCC 7.3
  - C++ Version: 201402
  - Intel(R) Math Kernel Library Version 2020.0.0 Product Build 20191122 for
Intel(R) 64 architecture applications
  - Intel(R) MKL-DNN v1.7.0 (Git Hash 7aed236906b1f7a05c0917e5257a1af05e9ff68
3)
  - OpenMP 201511 (a.k.a. OpenMP 4.5)
  - NNPACK is enabled
  - CPU capability usage: AVX2
  - CUDA Runtime 10.1
  - NVCC architecture flags: -gencode;arch=compute_37,code=sm_37;-gencode;arc
h=compute_50,code=sm_50;-gencode;arch=compute_60,code=sm_60;-gencode;arch=com
pute_70,code=sm_70
  - CuDNN 7.6.3
  - Magma 2.5.2
  - Build settings: BLAS_INFO=mkl, BUILD_TYPE=Release, CUDA_VERSION=10.1, CUD
NN_VERSION=7.6.3, CXX_COMPILER=/opt/rh/devtoolset-7/root/usr/bin/c++, CXX_FLA
GS= -Wno-deprecated -fvisibility-inlines-hidden -DUSE_PTHREADPOOL -fopenmp -D
NDEBUG -DUSE_KINETO -DUSE_FBGEMM -DUSE_QNNPACK -DUSE_PYTORCH_QNNPACK -DUSE_XN
NPACK -O2 -fPIC -Wno-narrowing -Wall -Wextra -Werror=return-type -Wno-missing
-field-initializers -Wno-type-limits -Wno-array-bounds -Wno-unknown-pragmas -
Wno-sign-compare -Wno-unused-parameter -Wno-unused-variable -Wno-unused-funct
ion -Wno-unused-result -Wno-unused-local-typedefs -Wno-strict-overflow -Wno-s
trict-aliasing -Wno-error=deprecated-declarations -Wno-stringop-overflow -Wno
-psabi -Wno-error=pedantic -Wno-error=redundant-decls -Wno-error=old-style-ca
st -fdiagnostics-color=always -faligned-new -Wno-unused-but-set-variable -Wno
-maybe-uninitialized -fno-math-errno -fno-trapping-math -Werror=format -Wno-s
tringop-overflow, LAPACK_INFO=mkl, PERF_WITH_AVX=1, PERF_WITH_AVX2=1, PERF_WI
TH_AVX512=1, TORCH_VERSION=1.8.1, USE_CUDA=ON, USE_CUDNN=ON, USE_EXCEPTION_PT
R=1, USE_GFLAGS=OFF, USE_GLOG=OFF, USE_MKL=ON, USE_MKLDNN=ON, USE_MPI=OFF, US
E_NCCL=ON, USE_NNPACK=ON, USE_OPENMP=ON,
```

In [8]:
```
print('Cuda', torch.cuda.is_available())
```

```
Cuda True
```

In [9]:
```
def set_random_seed(seed_value: int, use_cuda: bool = False):
    import torch
    import numpy as np
    import random
    np.random.seed(seed_value)  # cpu vars
    torch.manual_seed(seed_value)  # cpu  vars
    random.seed(seed_value)  # Python
    torch.use_deterministic_algorithms(True)
    if use_cuda:
        torch.cuda.manual_seed(seed_value)
        torch.cuda.manual_seed_all(seed_value)  # gpu vars
        torch.backends.cudnn.deterministic = True  # needed
        torch.backends.cudnn.benchmark = True


def _run_module(layer_norm, dropout, x, use_cuda, train, seed):
    if use_cuda:
        device = torch.device('cuda:0')
    else:
        device = torch.device('cpu')
```

```
    set_random_seed(seed, use_cuda=use_cuda)
    layers = []
    if layer_norm:
        layers.append(torch.nn.LayerNorm(normalized_shape=(x.shape[-1],), eps
    if dropout:
        layers.append(torch.nn.Dropout(0.5, inplace=False))

    if not layers:
        raise ValueError('set `layer_norm` and/or `dropout` to True')

    model = torch.nn.Sequential(*layers)
    model.to(device)
    if train:
        model.zero_grad()
        model.train()
    else:
        model.eval()
    x = x.clone().to(device)
    if train:
        out = model(x)
    else:
        with torch.no_grad():
            out = model(x)
    return out


def run_layer_norm_module(seed):
    set_random_seed(seed, use_cuda=False)
    x = torch.rand(3, 3)
    return {
        'x': x,
        'cpu_t': _run_module(layer_norm=True, dropout=False, x=x, use_cuda=Fa
        'cpu_e': _run_module(layer_norm=True, dropout=False, x=x, use_cuda=Fa
        'gpu_t': _run_module(layer_norm=True, dropout=False, x=x, use_cuda=Tr
        'gpu_e': _run_module(layer_norm=True, dropout=False, x=x, use_cuda=Tr
    }



def run_dropout_module(seed):
    set_random_seed(seed, use_cuda=False)
    x = torch.rand(4, 256, 256)
    return {
        'x': x,
        'cpu_t': _run_module(layer_norm=False, dropout=True, x=x, use_cuda=Fa
        'gpu_t': _run_module(layer_norm=False, dropout=True, x=x, use_cuda=Tr
    }
```

In [10]:

```
runs = [run_dropout_module(seed=42) for i in range(5)]

for i in range(1, len(runs)):
    for k in runs[0]:
        assert torch.equal(runs[0][k], runs[i][k]), (i, k)

for k in runs[0]:
    if k != 'x':
        print(k, (runs[0][k].cpu() == 0.0).sum(axis=-1).tolist())
        print('=' * 100)
```

```
cpu_t [[124, 120, 133, 129, 128, 117, 135, 133, 143, 114, 132, 134, 134, 131,
127, 108, 128, 127, 125, 125, 124, 138, 134, 138, 130, 120, 137, 139, 127, 13
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```

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==========
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7, 139, 131, 120, 130, 130, 142, 124, 113, 123, 127, 116, 139, 128, 129, 134,
127, 139, 126, 131]]
================================================================================
=======================
```

In [11]:

```
runs = [run_layer_norm_module(seed=42) for i in range(5)]
```

```
for i in range(1, len(runs)):
    for k in runs[0]:
        assert torch.equal(runs[0][k], runs[i][k]), (i, k)

runs[0]
```

Out[11]:
```
{'x': tensor([[0.88226926327, 0.91500395536, 0.38286375999],
         [0.95930564404, 0.39044821262, 0.60089534521],
         [0.25657248497, 0.79364132881, 0.94077146053]]),
 'cpu_t': tensor([[ 0.63882547617,  0.77325701714, -1.41208255291],
         [ 1.31617474556, -1.10615468025, -0.21002103388],
         [-1.38439559937,  0.44202369452,  0.94237166643]],
       grad_fn=<NativeLayerNormBackward>),
 'cpu_e': tensor([[ 0.63882547617,  0.77325701714, -1.41208255291],
         [ 1.31617474556, -1.10615468025, -0.21002103388],
         [-1.38439559937,  0.44202369452,  0.94237166643]]),
 'gpu_t': tensor([[ 0.63882541656,  0.77325695753, -1.41208267212],
         [ 1.31617438793, -1.10615372658, -0.21002060175],
         [-1.38439559937,  0.44202369452,  0.94237166643]], device='cuda:0',
       grad_fn=<NativeLayerNormBackward>),
 'gpu_e': tensor([[ 0.63882541656,  0.77325695753, -1.41208267212],
         [ 1.31617438793, -1.10615372658, -0.21002060175],
         [-1.38439559937,  0.44202369452,  0.94237166643]], device='cuda:0')}
```