

# Chirag Shah

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Maharashtra

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## Career Objective

To obtain a niche position in the Electronics industry where I can utilize my passion of combining both my hardware and software skills together and create a meaningful product for the organization

## Education

Currently studying in second year, Electronics Engineering, Sardar Patel Institute of Technology, Mumbai

Degree	College/School	University	Passing Year	Pass %
HSC	PACE Junior Science College, Dadar	Maharashtra State Board	2015	82.31
SSC	St Xaviers High School, Fort	Maharashtra State Board	2013	87.5

## Projects and Competitions

- 1st position in **e-Yantra Robotics competition 2016**.  
Team was placed 1st out of 162 teams in the theme “Launch a Module” – Incharge of hardware design and embedded C programming  
<http://eycgen.e-yantra.org/index.php/validate/fff9370353355910a5556b2d87c8e4b04f50a00d>
- “**Formation Control of Multiple Swarm Robots**” project for eYantra Summer Internship 2017  
[https://github.com/eYSIP-2017/eYSIP-2017\\_Formation\\_Control\\_of\\_Multiple\\_Swarm\\_Robots](https://github.com/eYSIP-2017/eYSIP-2017_Formation_Control_of_Multiple_Swarm_Robots)
- My project **DIY Timelapse Dolly** was featured and won the first prize in **The Raspberry Pi contest 2016** contest on Instructables out of 27 finalists from 198 entries  
[www.instructables.com/id/DIY-Time-Lapse-Dolly-1/](http://www.instructables.com/id/DIY-Time-Lapse-Dolly-1/)
- Won Second prize in Troubleshooting Competition organized by SPIT - Department of Electronics Engineering Mumbai from 29th/Sep /2016 to 17th/Oct/2016

## Internships

- 7 weeks summer internship at IIT-Bombay under eYantra Summer Internship Program 2017

## Training

- 7 weeks summer internship at IIT-Bombay under the eYantra Summer Internship 2017
- 3 weeks summer training program on Embedded Systems System Design held from 13/June/2016 to 8/July/2016 at SPIT College Mumbai
- 2 days workshop on MSP-FPGA Software and Hardware co-design conducted on 16th and 18th Sep 2016 at SPIT, Mumbai

## Technical Skills

- Embedded C programming
- Project management using Git
- PCB designing and fabrication
- Good electronic hardware and circuit debugging skills
- Basic image processing using OpenCV and python

- Basic FPGA programming - Atlys Spartan-6 FPGA Trainer Board
- Document formatting using L<sup>A</sup>T<sub>E</sub>X

## Soft Skills

- Patient and persistent in completing my projects
- Willing to learn new skills as per the requirements of the project
- Good analytical and problem solving skills

## Other Interests

- PADI Level 2 certified open water scuba diver
- Sailing and Wind surfing
- Rubiks cube enthusiast
- Photography

## Co-Curricular Activities

- Class Representative: FY & SY (Electronics Engg)
- Core Team Member of SPOpen Mini - 2016 (Speed cubing competition) -Responsible for volunteer training.  
[www.worldcubeassociation.org/competitions/SPOpenMini2015](http://www.worldcubeassociation.org/competitions/SPOpenMini2015)

## References

1. Name: Dr. Rita Das  
 Desig: Dean, Students' Affairs  
 Associate Professor (Physics), Head of Applied Sciences & Humanites Department,  
 Sardar Patel Institute of Technology  
 Email: rita\_das@spit.ac.in
2. Name: Prof. Govind Haldankar  
 Desig: Assistant Professor, Electronics Engineering department,  
 Sardar Patel Institute of Technology  
 Email: g\_haldankar@spit.ac.in

## Declaration

I do hereby declare that the above given statements are true and correct to the best of my knowledge.

**Place:** Mumbai

**Date:** 24<sup>th</sup> April 2017

**Chirag Shah**