

Chirag Shah

16-Marina House,
5-Sir V.T Marg,
Opp Liberty Cinema,
Mumbai 400 020
Maharashtra

Contact: 9769168825
chirags1998@gmail.com



Career Objective

To work as a system/product design engineer in the electronics industry where I can develop and apply my hardware and software skills

Education

Degree	College/School	University	Passing Year	Pass %
SSC	St Xaviers High School, Fort	Maharashtra State Board	2013	87.5
HSC	PACE Junior Science College, Dadar	Maharashtra State Board	2015	82.31

Projects and Competitions

1. My project **DIY Timelapse Dolly** was featured and won the first prize in **The Raspberry Pi contest 2016** contest on Instructables.com
www.instructables.com/id/DIY-Time-Lapse-Dolly-1/
2. Member of the team which was selected as one of 5 finalists out of 162 teams in the Launch a Module theme and were placed at 1st position in the theme
<http://eycgen.e-yantra.org/index.php/validate/fff9370353355910a5556b2d87c8e4b04f50a00d>
3. Won Second prize in Troubleshooting Competition organized by Department of Electronics Engineering held at SPIT Mumbai from 29th/Sep /2016 to 17th/Oct/2016

Training and Internship

- 3 weeks summer training program on Embedded Systems System Design held from 13/June/2016 to 8/July/2016 at SPIT College Mumbai
- 2 days workshop on MSP-FPGA Software and Hardware co-design conducted on 16th and 18th Sep 2016 at SPIT, Mumbai

Research Publications

1. None

Technical Skills

- Embedded C programming
 - ATmega2560-FireBird V
 - ESP-8266
 - ATmega8535 dev-board
 - Arduino

- SMD soldering
- Basic PCB designing and fabrication
- Basic FPGA programming - Atlys Spartan-6 FPGA Trainer Board

Soft Skills

1. Patient and persistent in my projects
2. Good analytical and problem solving skills
3. Ready to adapt as needed

Extra Curricular Activities

- PADI Level 2 certified open water scuba diver
- Sailing and Wind surfing
- Photography
- Rubiks cube enthusiast

Co-Curricular Activities

1. Class Representative: FY & SY (Electronics Engg)
2. Core Team Member of SPOpen Mini - 2016 (Rubiks cube competition)
www.worldcubeassociation.org/competitions/SPOpenMini2015

Personal Details

Father's Name: Rasesh Shah
 Mother's Name: Reshma Shah
 Sex: Male
 Date of Birth: 18th April 1998
 Nationality: Indian
 Marital Status: Unmarried

References

1. Name: Dr. Rita Das
 Desig: Dean, Students' Affairs
 Associate Professor (Physics), Head of Applied Sciences & Humanites Department
 Email: rita_das@spit.ac.in
2. Name: Dr. Surendra S. Rathod
 Desig: Professor & Head of Electronics Engineering Department
 Dean, Students Affairs
 Associate Professor (Physics) & Head of Department,
 Email: surendra_rathod@spit.ac.in

Declaration

I do hereby declare that the above given statements are true and correct to the best of my knowledge.

Date

24th April 2017