

Chirag Shah

16-Marina House,
5-Sir V.T Marg,
Opp Liberty Cinema,
Mumbai 400 020
Maharashtra

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Career Objective

To obtain a niche position in the Electronics industry where I can utilize my passion of combining both my hardware and software skills together and create a meaningful product for the organization

Education

Currently studying in second year, Electronics Engineering, Sardar Patel Institute of Technology, Mumbai

Degree	College/School	University	Passing Year	Pass %
HSC	PACE Junior Science College, Dadar	Maharashtra State Board	2015	82.31
SSC	St Xaviers High School, Fort	Maharashtra State Board	2013	87.5

Projects and Competitions

1. Member of the team which participated in **e-Yantra Robotics competition 2016** and selected as one of 5 finalists out of 162 teams in the Launch a Module theme and were placed at 1st position in the theme - Incharge of hardware design and embedded C programming
<http://eycgen.e-yantra.org/index.php/validate/fff9370353355910a5556b2d87c8e4b04f50a00d>
2. My project **DIY Timelapse Dolly** was featured and won the first prize in **The Raspberry Pi contest 2016** contest on Instructables.com out of 198 entries and 27 finalists
www.instructables.com/id/DIY-Time-Lapse-Dolly-1/
3. Won Second prize in Troubleshooting Competition organized by SPIT - Department of Electronics Engineering Mumbai from 29th/Sep /2016 to 17th/Oct/2016

Training and Internship

- 3 weeks summer training program on Embedded Systems System Design held from 13/June/2016 to 8/July/2016 at SPIT College Mumbai
- 2 days workshop on MSP-FPGA Software and Hardware co-design conducted on 16th and 18th Sep 2016 at SPIT, Mumbai

Research Publications

1. None

Technical Skills

- Embedded C programming
 - ATmega2560-FireBird V
 - ESP-8266
 - ATmega8535 dev-board
 - Arduino
- SMD soldering
- Basic PCB designing and fabrication
- Basic FPGA programming - Atlys Spartan-6 FPGA Trainer Board

Soft Skills

1. Patient and persistent in completing my projects
2. Good analytical and problem solving skills
3. Ready to adapt as needed

Extra Curricular Activities

- PADI Level 2 certified open water scuba diver
- Sailing and Wind surfing
- Rubiks cube enthusiast
- Photography

Co-Curricular Activities

1. Class Representative: FY & SY (Electronics Engg)
2. Core Team Member of SPOpen Mini - 2016 (Speed cubing competition) -Responsible for volunteer training.

www.worldcubeassociation.org/competitions/SPOpenMini2015

Personal Details

Father's Name: Rasesh Shah
Mother's Name: Reshma Shah
Sex: Male
Date of Birth: 18th April 1998
Nationality: Indian
Marital Status: Unmarried

References

1. Name: Dr. Rita Das
Design: Dean, Students' Affairs
Associate Professor (Physics), Head of Applied Sciences & Humanities Department,
Sardar Patel Institute of Technology
Email: rita_das@spit.ac.in
2. Name: Prof. Govind Haldankar
Design: Assistant Professor, Electronics Engineering department,
Sardar Patel Institute of Technology
Email: g_haldankar@spit.ac.in

Declaration

I do hereby declare that the above given statements are true and correct to the best of my knowledge.

Place: Mumbai

Date: 24th April 2017

Chirag Shah