Chirag Shah

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Career Objective

To work as a system/product design engineer in the electronics industry where I can develop and apply my hardware and software skills

Education

Degree	College/School	University	Passing Year	Pass %
SSC	St Xaviers High School, Fort	Maharashtra State Board	2013	87.5
HSC	PACE Junior Science College, Dadar	Maharashtra State Board	2015	82.31

Projects and Competitions

1. My project **DIY Timelapse Dolly** was featured and won the first prize in **The Raspberry Pi contest 2016** contest on Instructables.com
   www.instructables.com/id/DIY-Time-Lapse-Dolly-1/
2. Member of the team which was selected as one of 5 finalists out of 162 teams in the Launch a Module theme and were placed at 1st position in the theme
   http://eycgen.e-yantra.org/index.php/validate/fff9370353355910a5556b2d87c8e4b04f50a00d
3. Won Second prize in Troubleshooting Competition organized by Department of Electronics Engineering held at SPIT Mumbai from 29th/Sep /2016 to 17th/Oct/2016

Training and Internship

- 3 weeks summer training program on Embedded Systems System Design held from 13/June/2016 to 8/July/2016 at SPIT College Mumbai
- 2 days workshop on MSP-FPGA Software and Hardware co-design conducted on 16th and 18th Sep 2016 at SPIT, Mumbai

Research Publications

1. None

Technical Skills

- Embedded C programming
  - ATmega2560-FireBird V
  - ESP-8266
  - ATmege8535 dev-board
  - Arduino

- SMD soldering
- Basic PCB designing and fabrication
- Basic FPGA programming - Atlys Spartan-6 FPGA Trainer Board