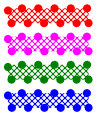
CMN2015-1 schematic

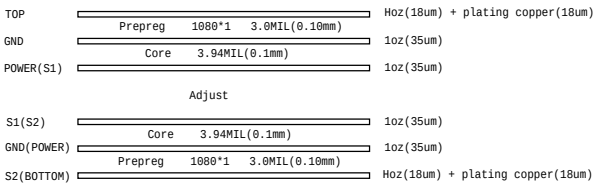
CONTENT INDEXING

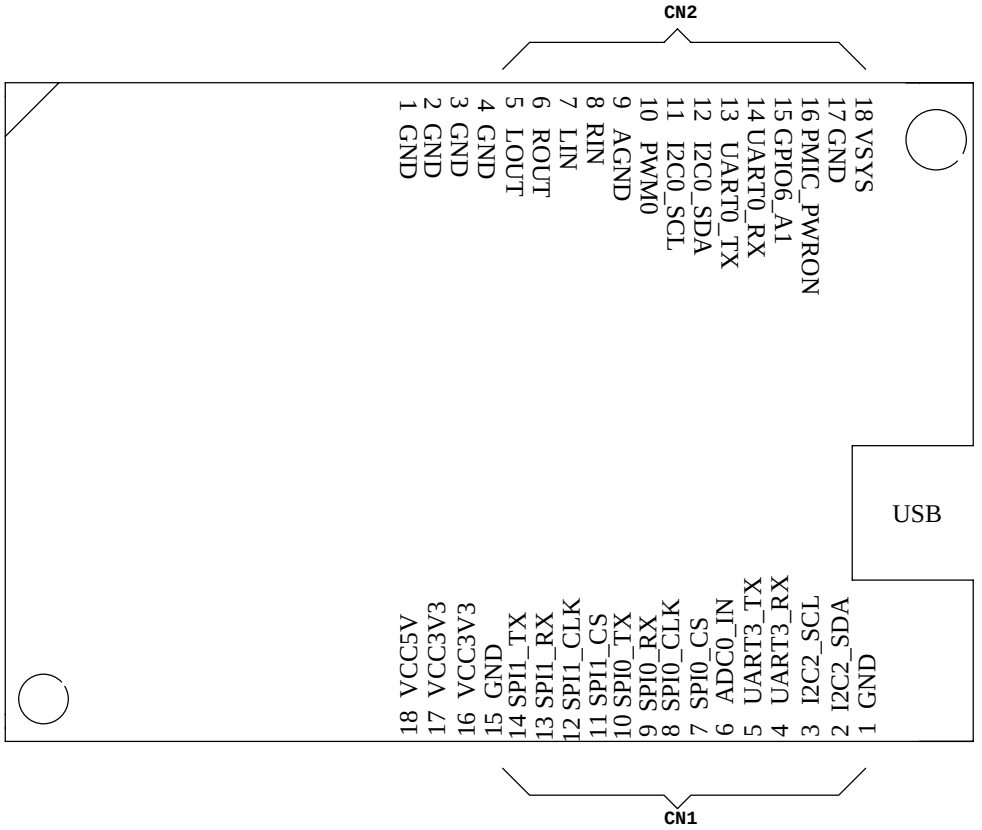
PCB POWER WIRE WIDTH INDICATE



above 80 miles
above 50 miles
above 30 miles
above 16 miles
No indicate
Under needs

6 LAYERS PCB STACK





This is the schematic of CMN2015-1 which is the first board computer design that the CHIRIMEN Open Hardware project developed.

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Version	Date	Author	Change Note	Note
V1	20150108	Dawen	First edictor	
V2	20150302	Dawen	INARI to CHIRIMEN, Add Power&Ground PIN	

CHIRIMEN Open Hardware Project

Title: ***Modify note***

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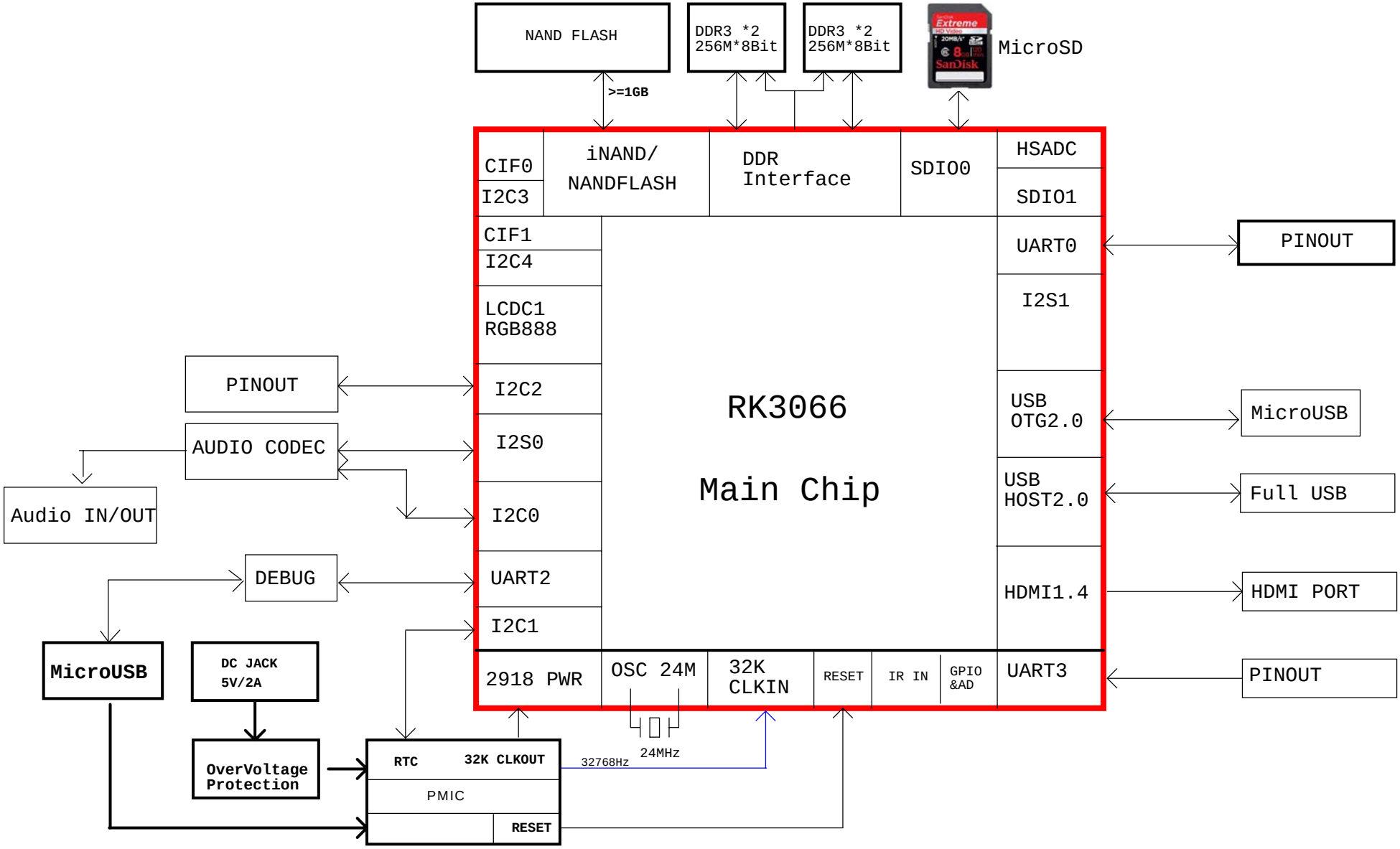

CHIRIMEN Open Hardware Project

Title: *Block Diagram*

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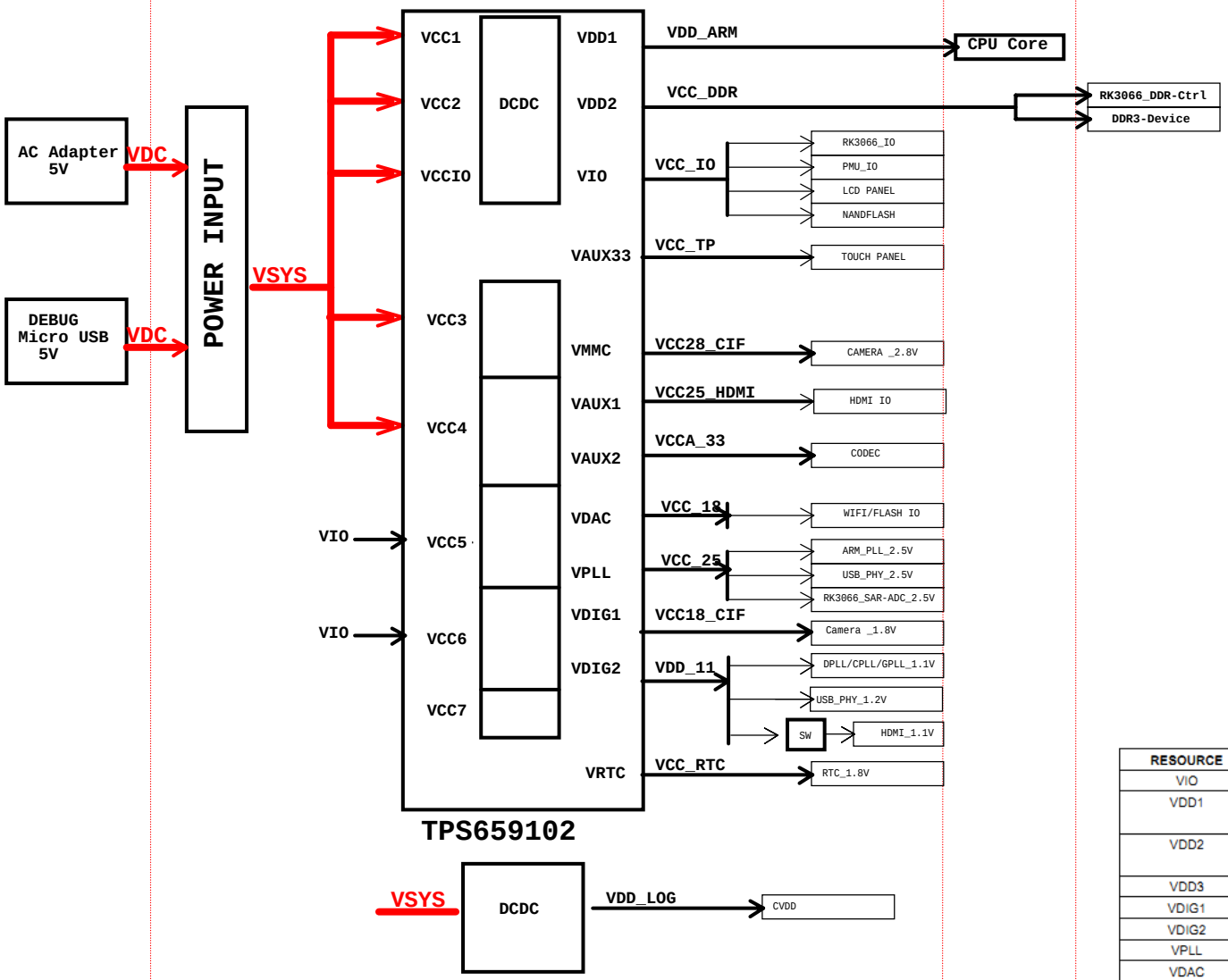

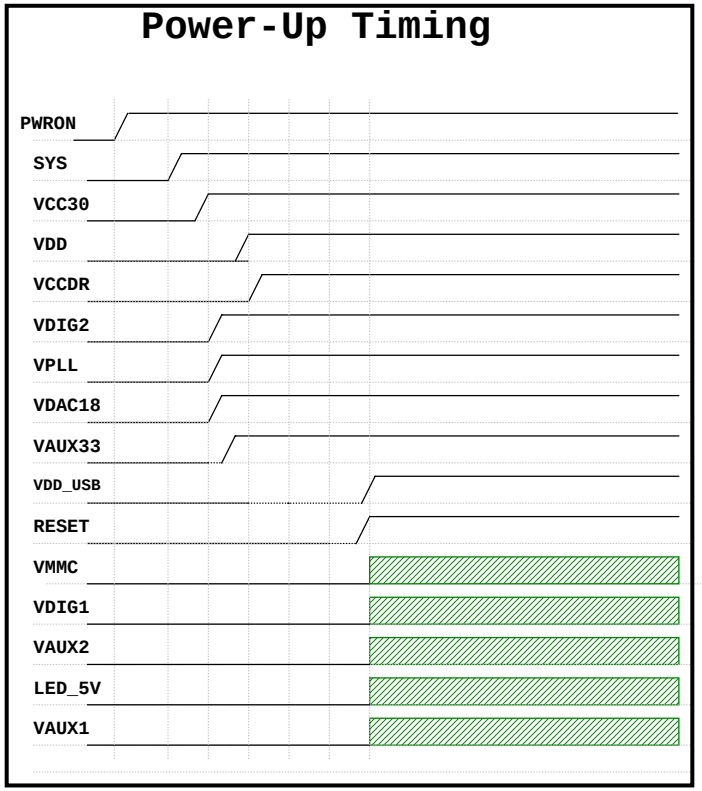

Table 12. Power Sources

RESOURCE	TYPE	VOLTAGES	POWER
VIO	SMPS	1.5 V / 1.8 V / 2.5 V / 3.3 V	1000 mA
VDD1	SMPS	0.6 ... 1.5 in 12.5-mV steps Programmable multiplication factor: x2, x3	1500 mA
VDD2	SMPS	0.6 ... 1.5 in 12.5-mV steps Programmable multiplication factor: x2, x3	1500 mA
VDD3	SMPS	5 V	100 mA
VDIG1	LDO	1.2 V, 1.5 V, 1.8 V, 2.7 V	300 mA
VDIG2	LDO	1 V, 1.1 V, 1.2 V, 1.8 V	300 mA
VPLL	LDO	1.0 V, 1.1 V, 1.8 V, 2.5 V	50 mA
VDAC	LDO	1.8 V, 2.6 V, 2.8 V, 2.85 V	150 mA
VAUX1	LDO	1.8 V, 2.5 V, 2.8 V, 2.85 V	300 mA
VAUX2	LDO	1.8 V, 2.8 V, 2.9 V, 3.3 V	150 mA
VAUX33	LDO	1.8 V, 2.0 V, 2.8 V, 3.3 V	150 mA
VMMC	LDO	1.8 V, 2.8 V, 3.0 V, 3.3 V	300 mA

POWER DIAGRAM

CHIRIMEN Open Hardware Project

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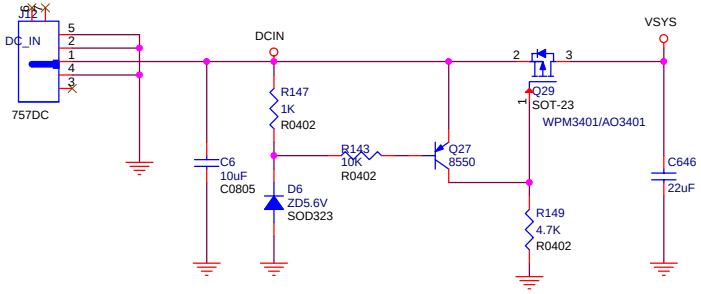
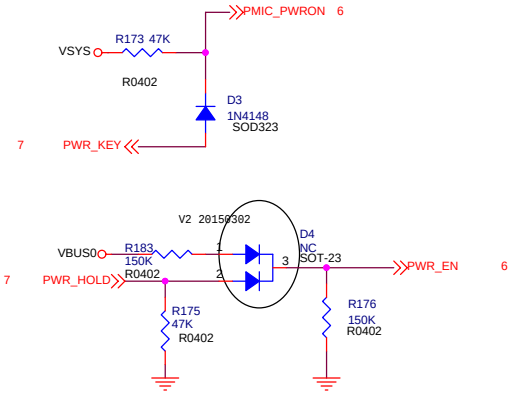


DC IN

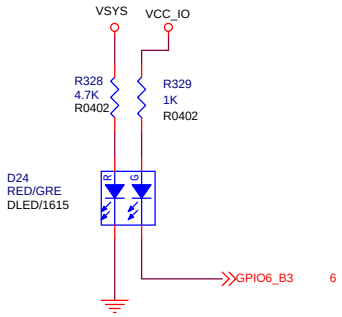
Note:
Please use a 5V DC.

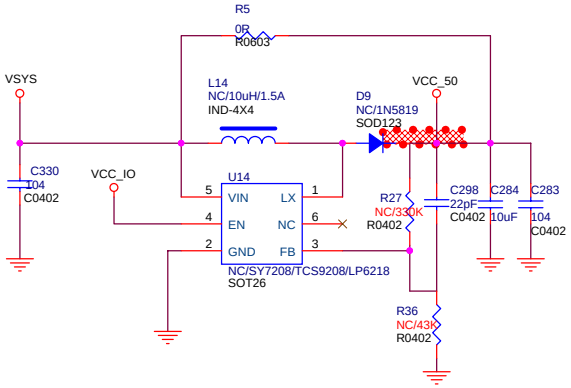


POWER CONTROL

PCB POWER WIRE WIDTH INDICATE

	above 80 miles
	above 50 miles
	above 30 miles
	above 12 miles
No indicate	Under needs





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Title: *5V DC in/LED*		
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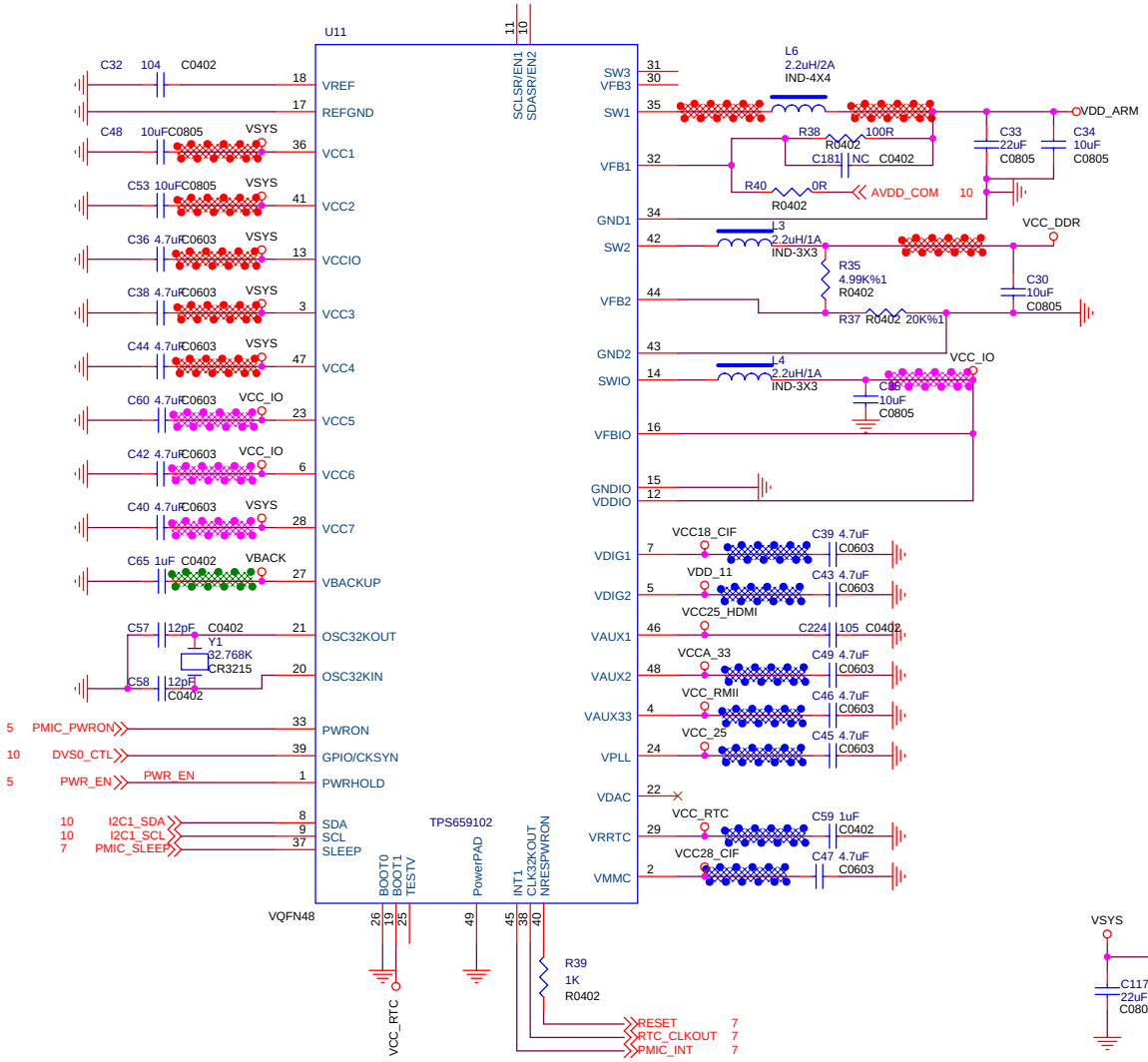
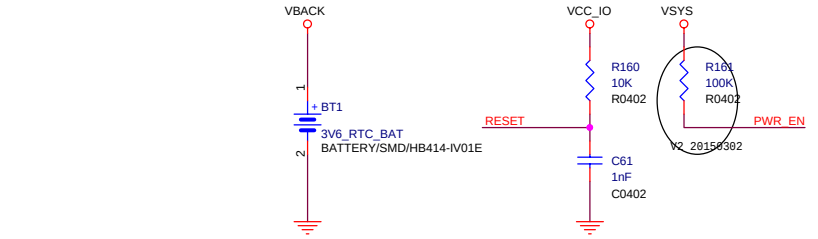
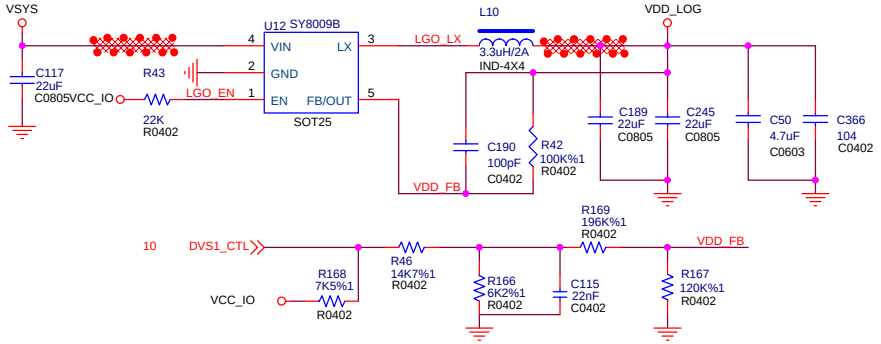
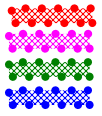






PCB POWER WIRE WIDTH INDICATE



above 80 miles
above 50 miles
above 30 miles
above 12 miles
No indicate
Under needs

SYSTEM POWER

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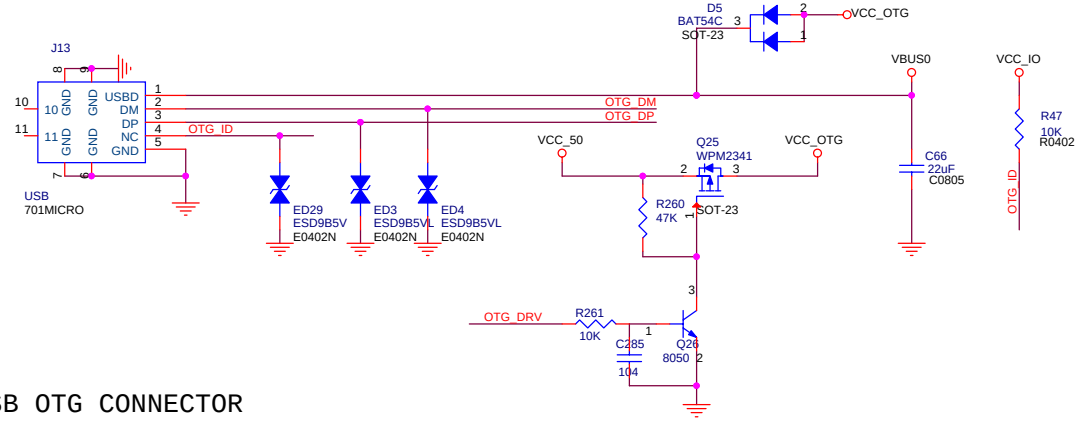
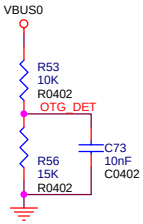
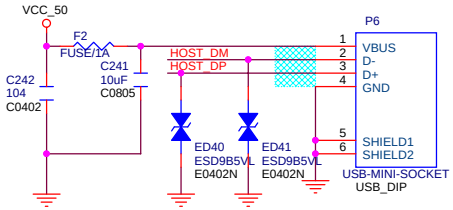


USB OTG CONNECTOR



USB_DET



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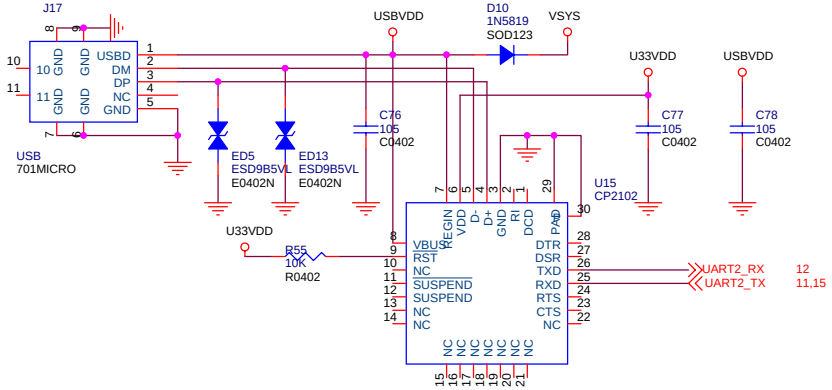
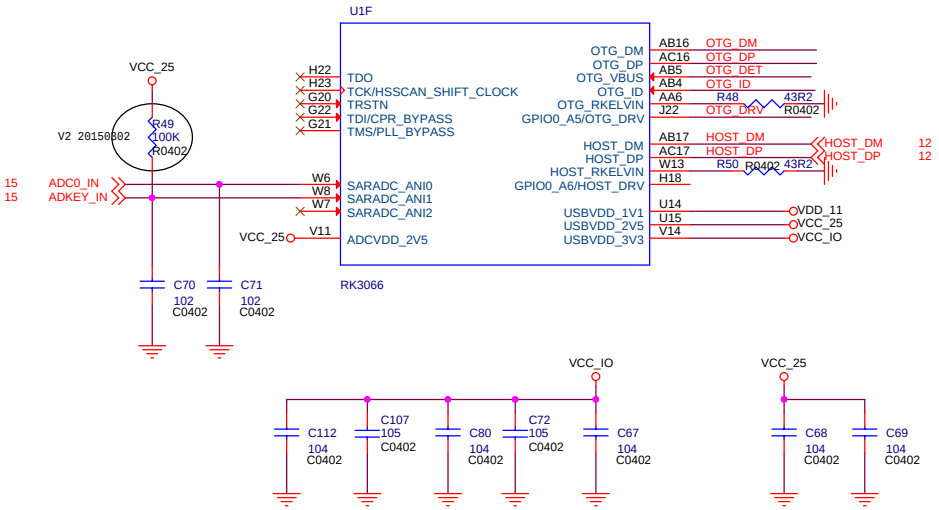
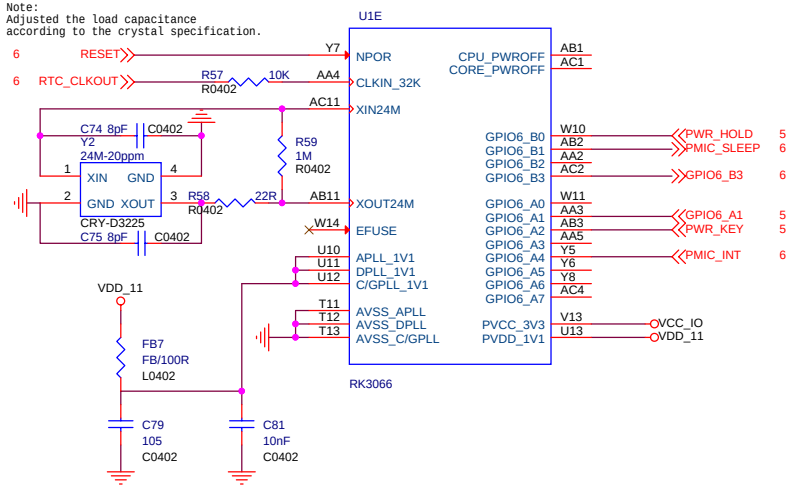


USB_DEBUG

When the system power on, the Adkey_in level is 0V,
RK30 enter into recover mode.



RK3066-F



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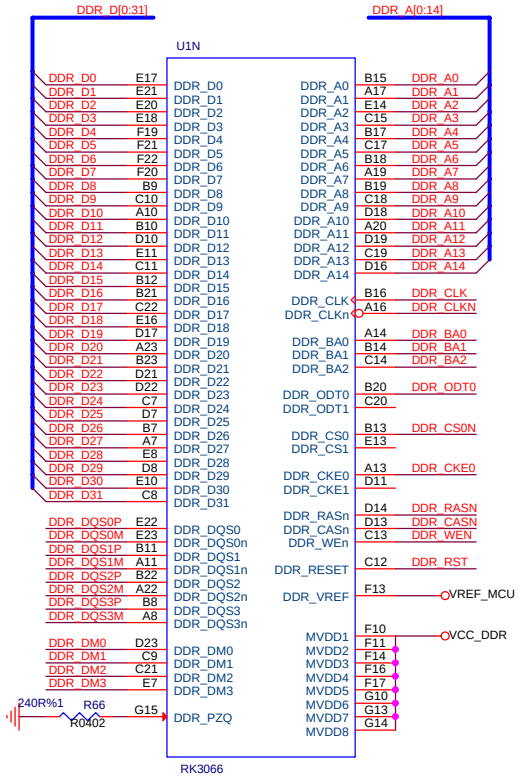
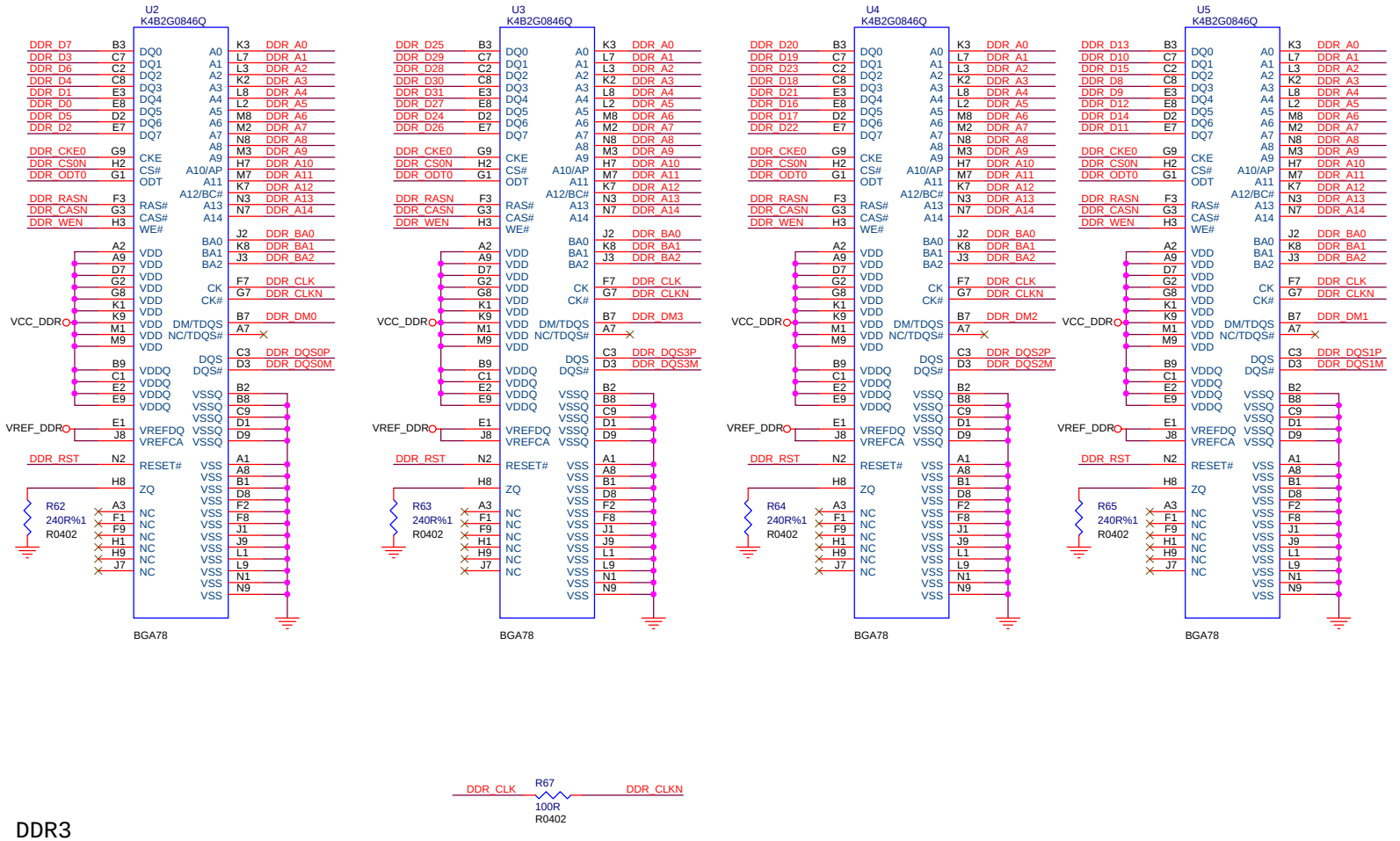
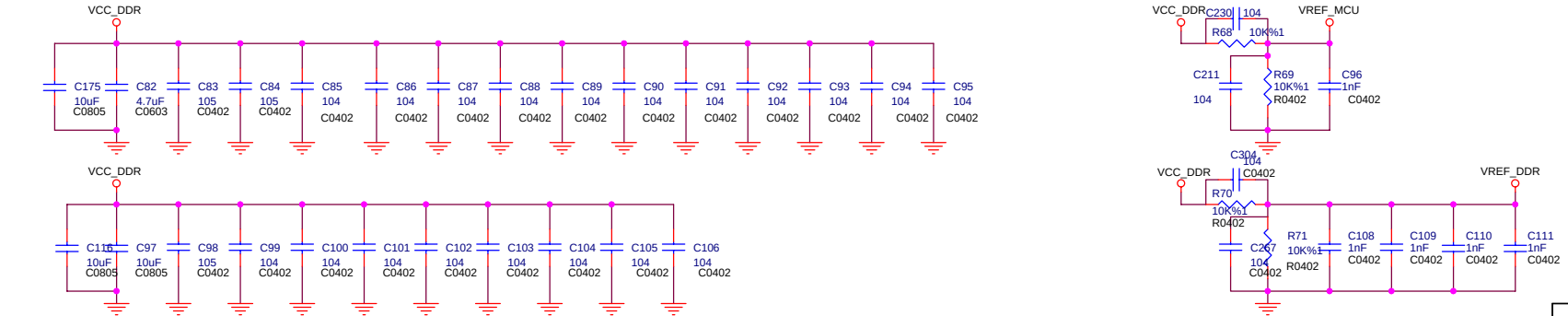


RK3066-N



DDR3



DDR FILTER

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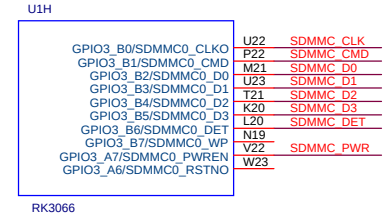
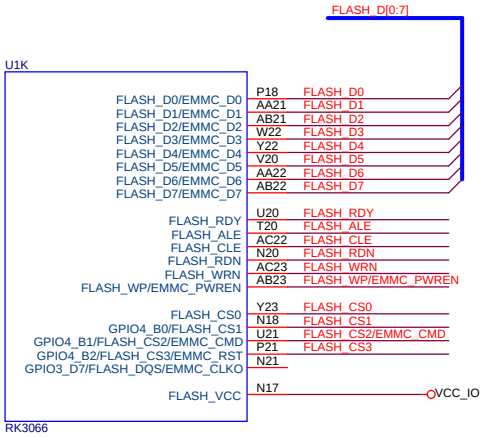
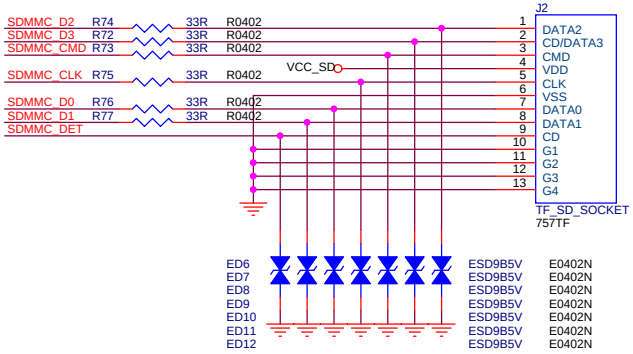


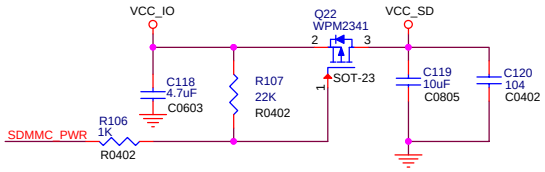
RK3066-H

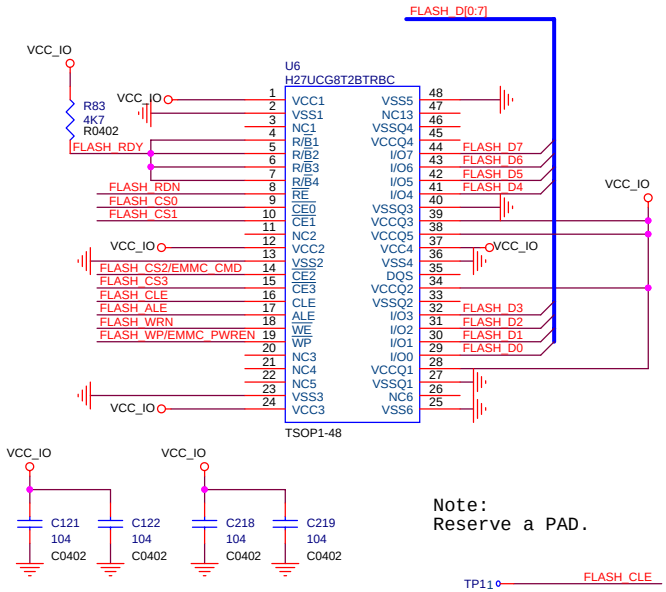


Note:
Select the power type
follow FLASH IO need.

RK3066-K







Note:
Reserve a PAD.

NAND FLASH

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Title: ***Flash/MicroSD***		
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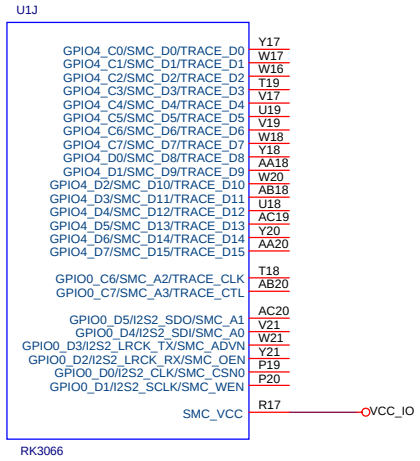

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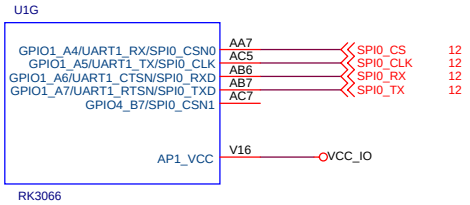
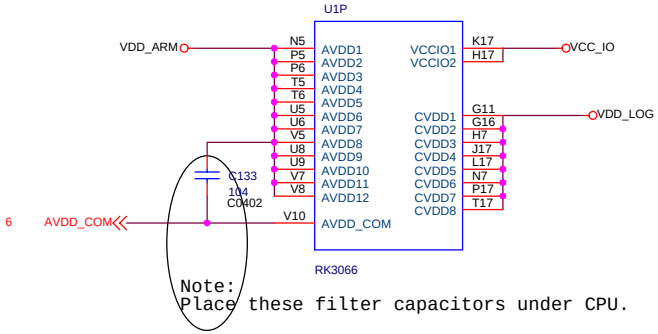


RK3066-G



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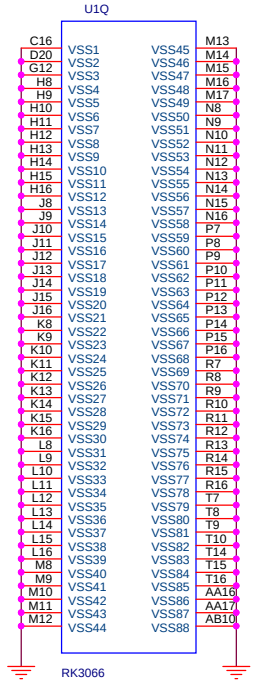

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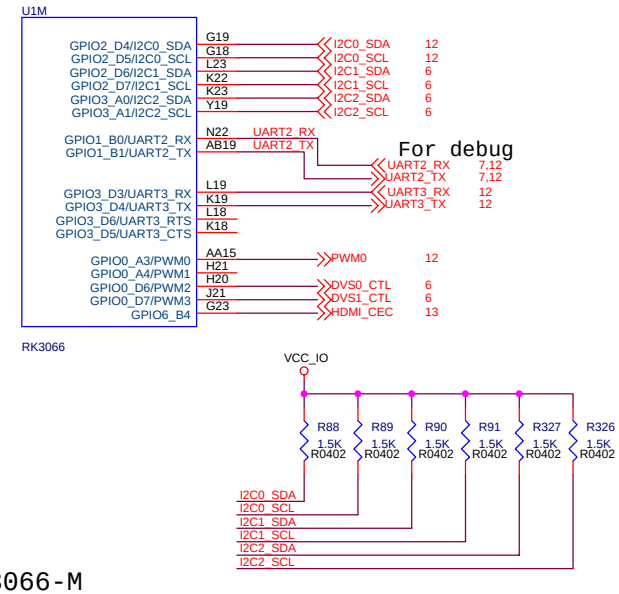
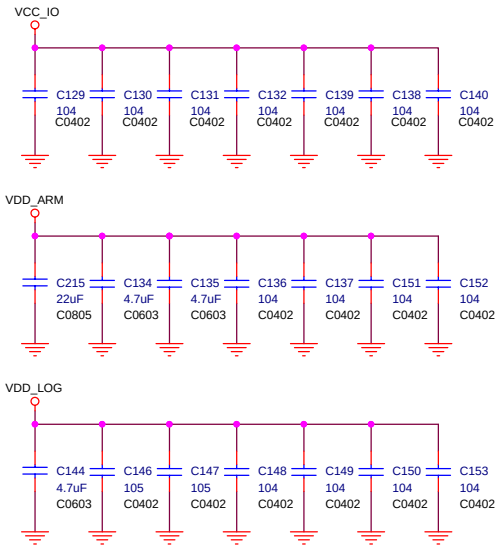


RK3066-M



RK3066 CORE POWER FILTER

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Title: *RK_Vss/Bypass CAP/GPIO*

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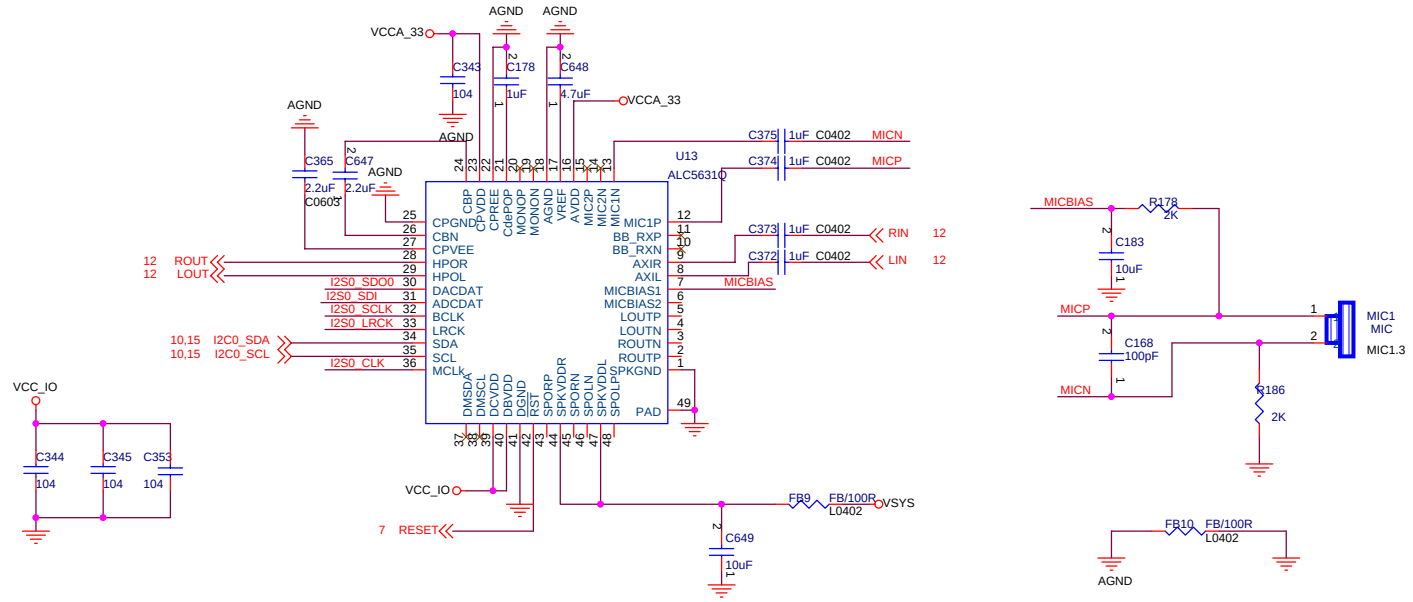

Audio CODEC

RK3066-L

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Title: ***Audio/Codec***		
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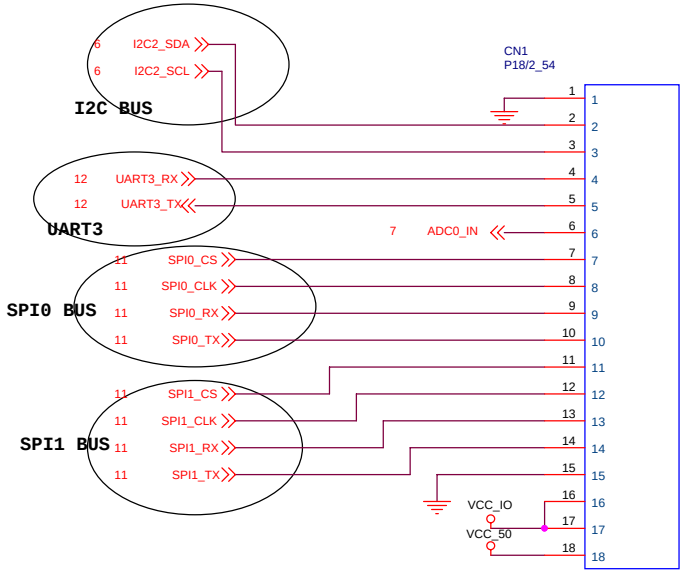
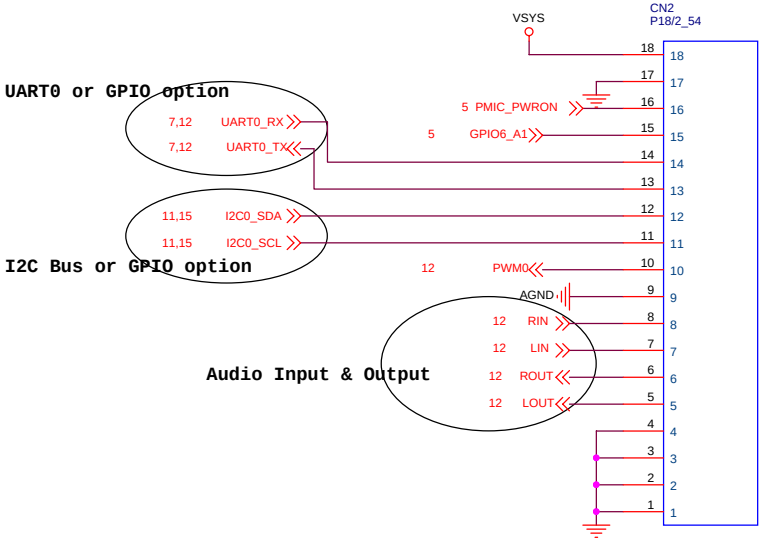
DIP Connector





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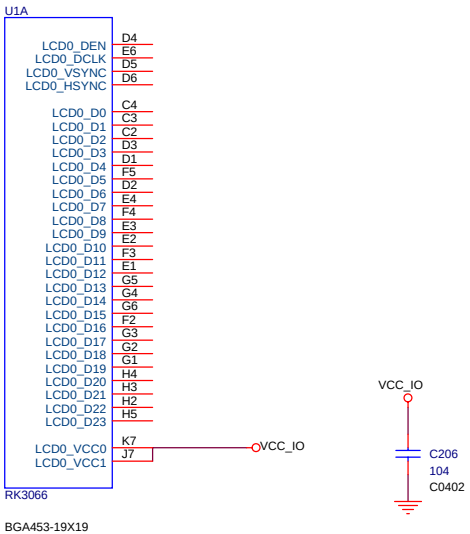

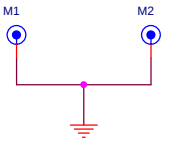

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Title: ***Connector/PINOUT***		
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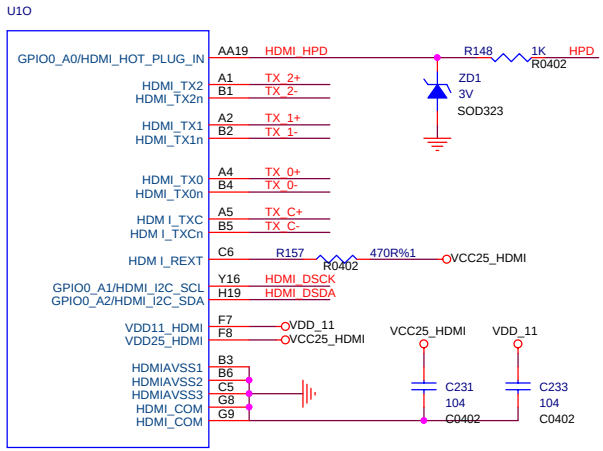
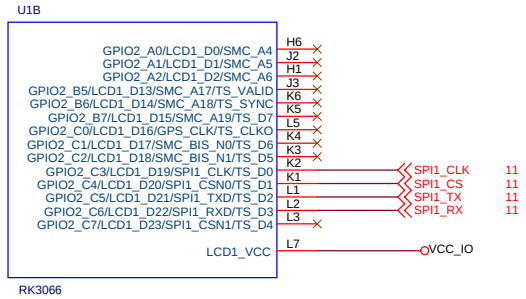
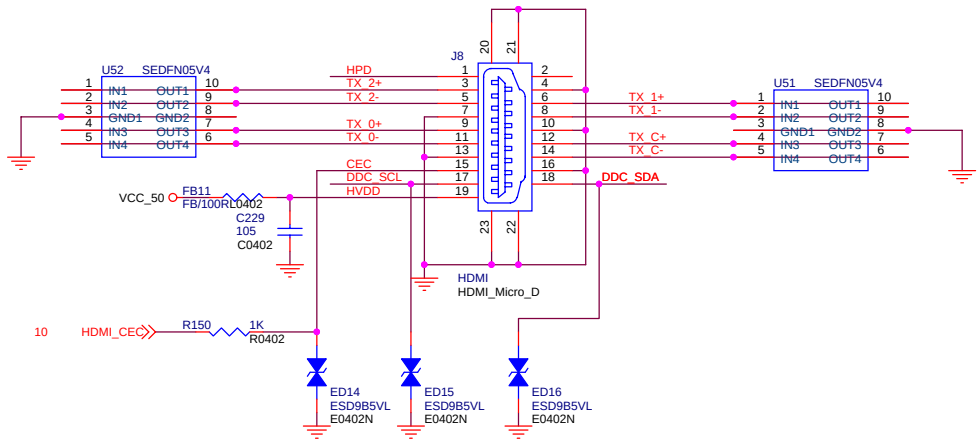
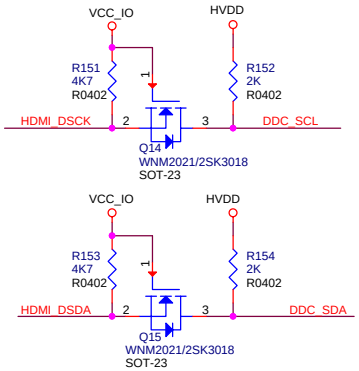


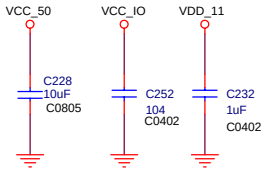
RK3066-0



RK3066-B







CHIRIMEN Open Hardware Project		
Title: ***HDMI Output***		
Size: A3	Document Number: CMN2015-1	Rev: 2
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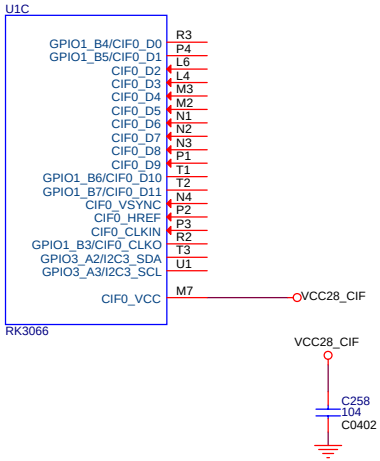
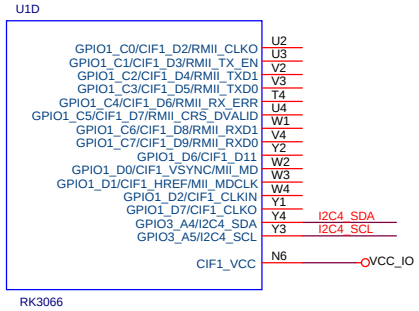
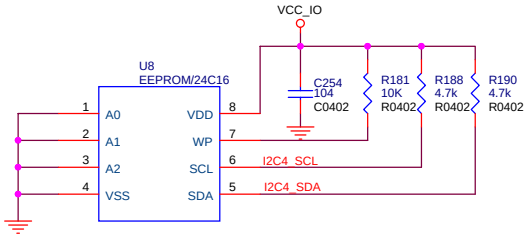


RK3066-C



RK3066-D



V2 20150302

DEL POWER ON KEY Button

KEY Button

When the system power on, the Adkey_in level is 0V, RK30 enter into recover mode.

CHIRIMEN Open Hardware Project		
Title: ***Debug Key/E2PROM***		
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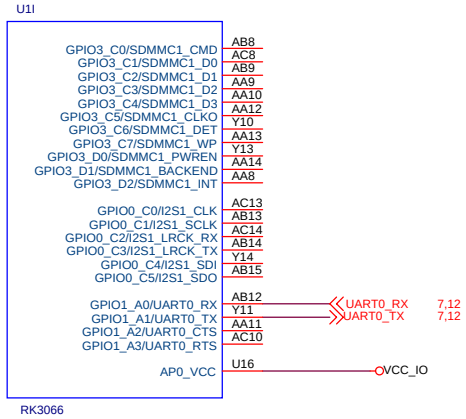

RK3066-I

CHIRIMEN Open Hardware Project		
Title: ***UART0***		
Size: A3	Document Number: CMN2015-1	Rev: 2
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