



Python Productivity for Zynq

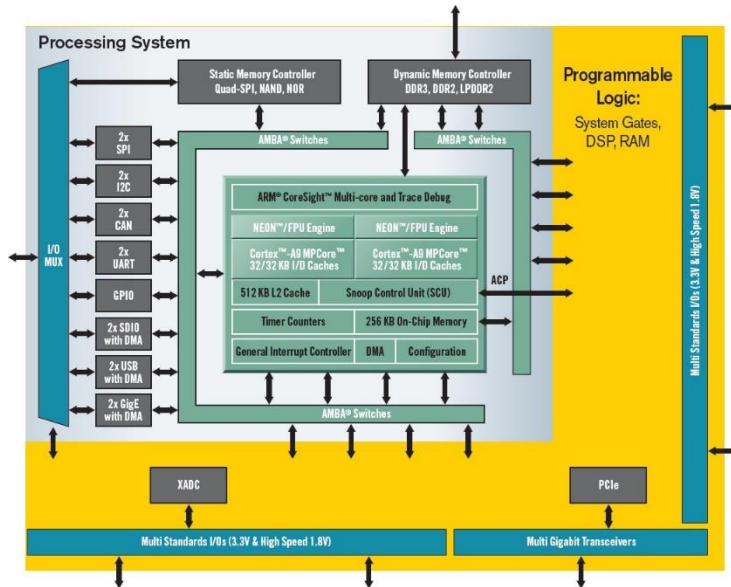


- > Zynq & Zynq Ultrascale+
- > PYNQ Framework
- > Technologies
- > Community

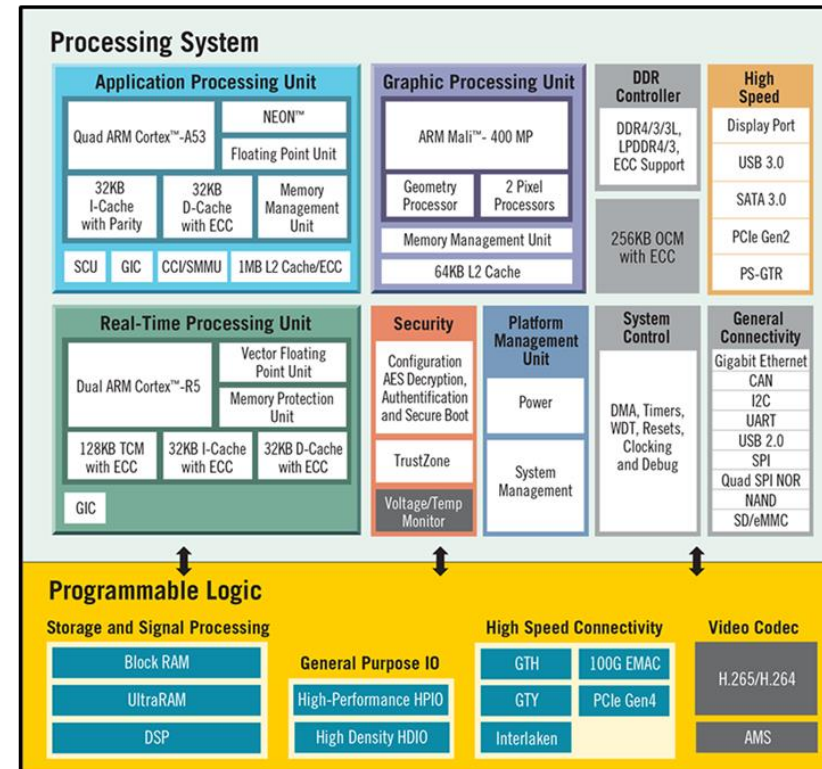
ZYNQ and ZYNQ UltraSCALE+

Best-in-class, SoC and MPSoCs

ZYNQ 7000



ZYNQ UltraSCALE+



FPGAs and tightly-integrated CPUs enable entirely new opportunities

Zynq applications

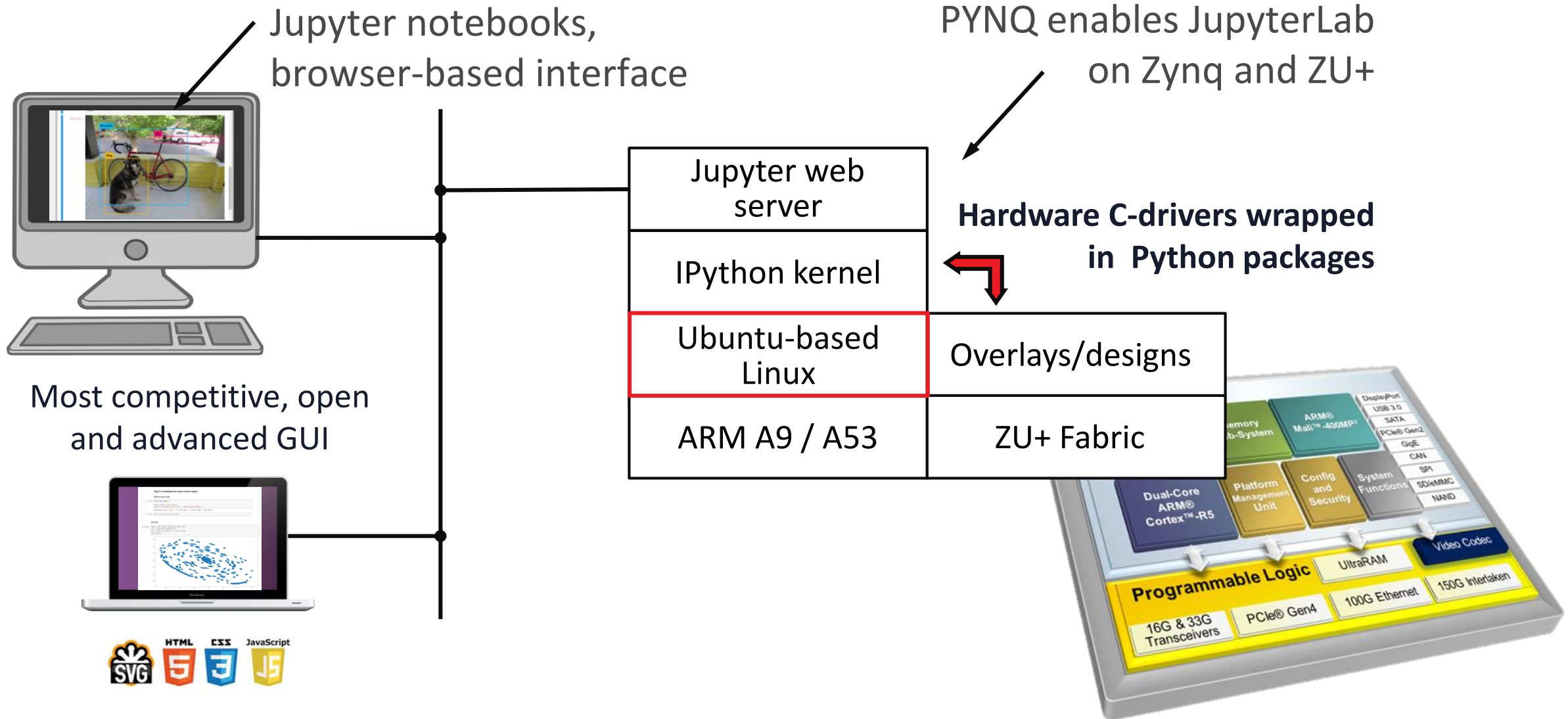


- > Make Zynq so easy-to-use that programmers can access the benefits without learning advanced digital design skills**



PYNQ framework





Ubuntu-based Linux versus embedded Linux

Ubuntu-based Linux



➤ Optimized for developer productivity

- > All the Linux libraries and drivers you expect
- > Pre-built SD card image
- > Ubuntu/Debian ecosystem & community

>>145,000,000 Google hits

Embedded Linux



➤ Optimized for deployment efficiency

- > Selective Linux libraries and drivers
- > Commonly delivered in flash memory on board
- > PetaLinux ecosystem:

>> 143,000 Google hits

3 orders of magnitude difference

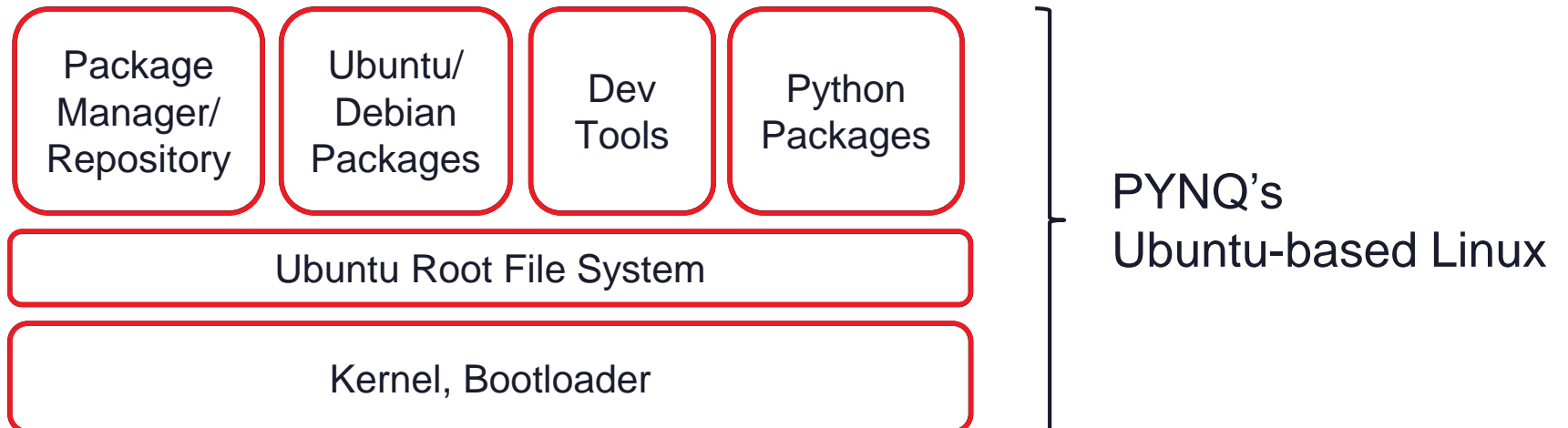
PYNQ's Ubuntu-based Linux

PYNQ uses Ubuntu's:

- Root file system (RFS)
- Package manager (*apt-get*)
- Repositories

PYNQ bundles :

- Development tools
 - Cross-compilers
- Latest Python packages



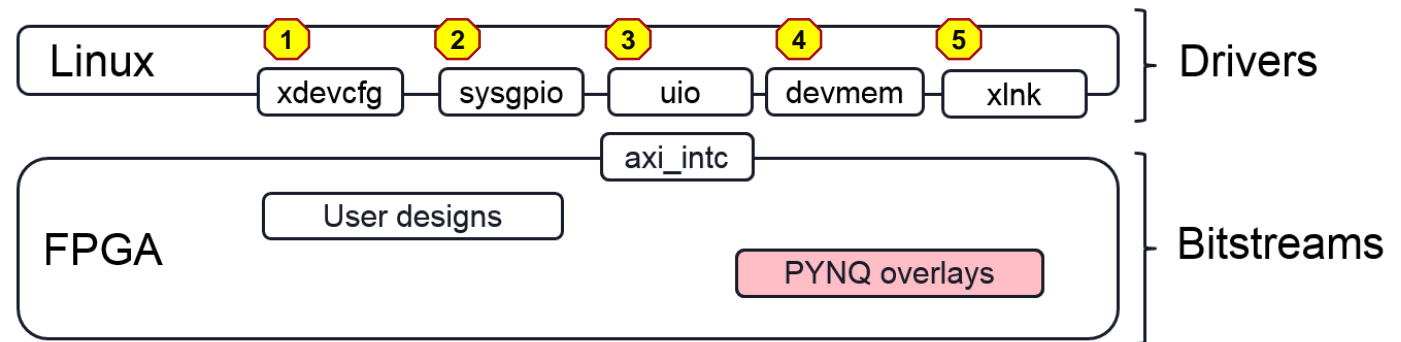
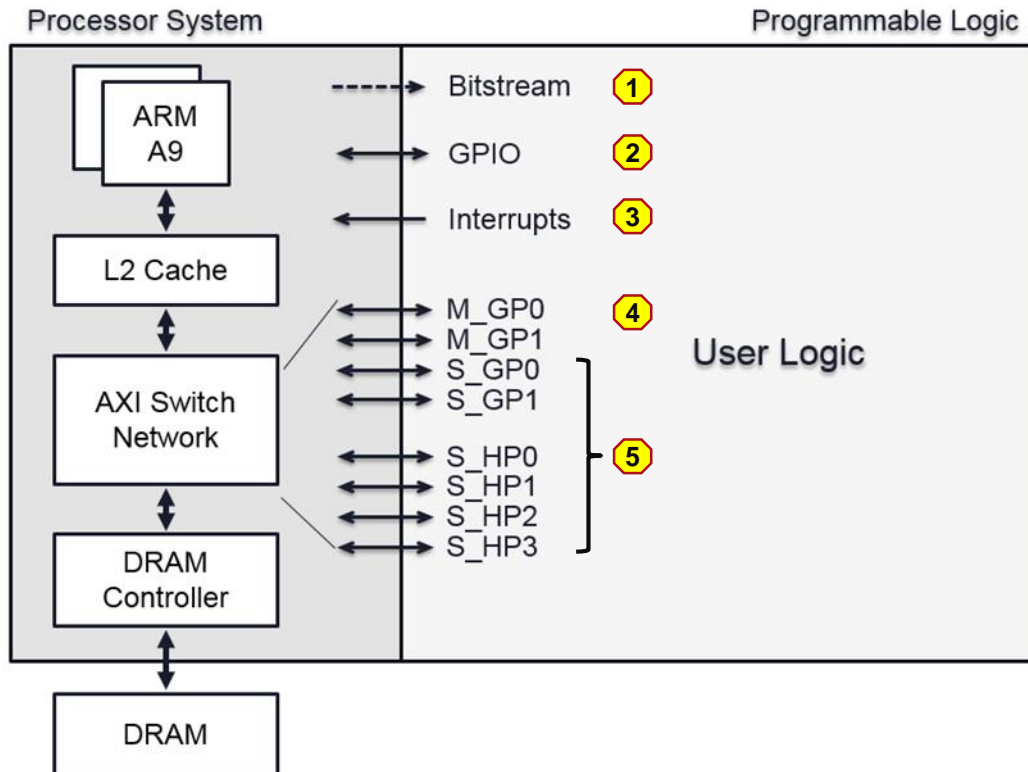
PYNQ uses the PetaLinux build flow and board support package:

- Access to all Xilinx kernel patches
- Works with any Xilinx supported board
- Configured with additional drivers for PS-PL interfaces

PYNQ provides Linux Drivers for PS-PL Interfaces ...

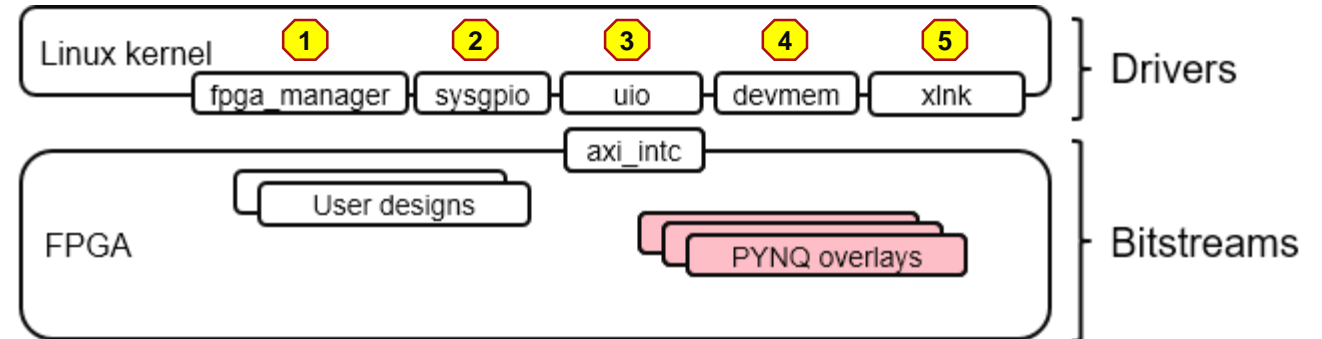
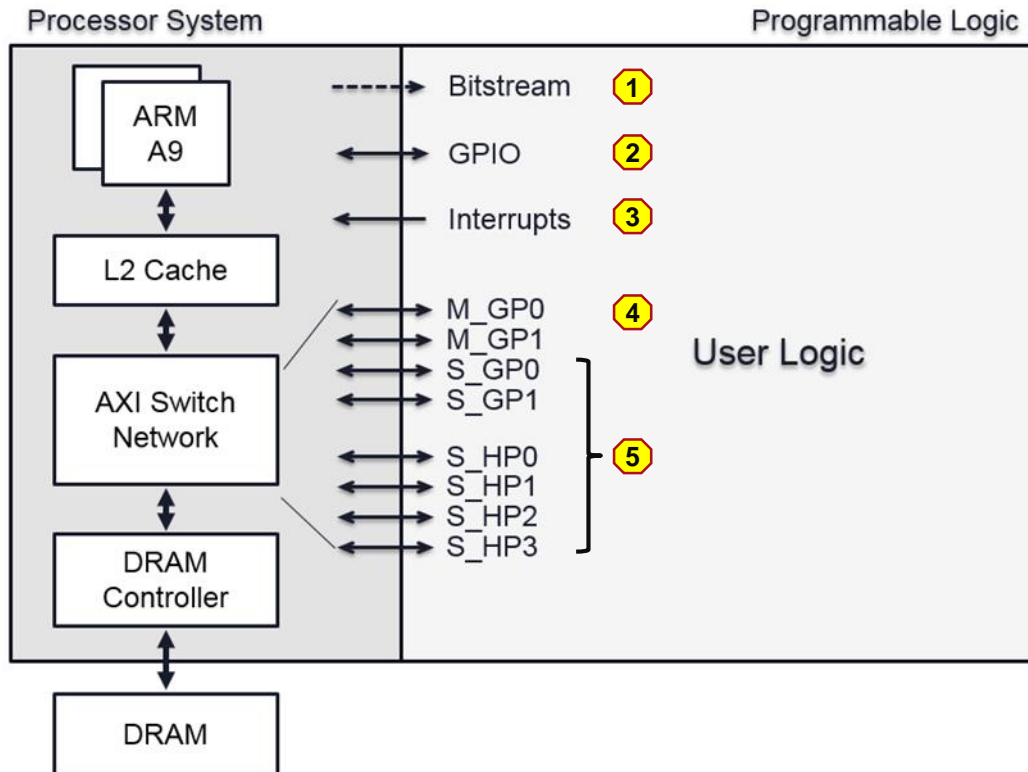
wrapped in Python Libraries

Zynq

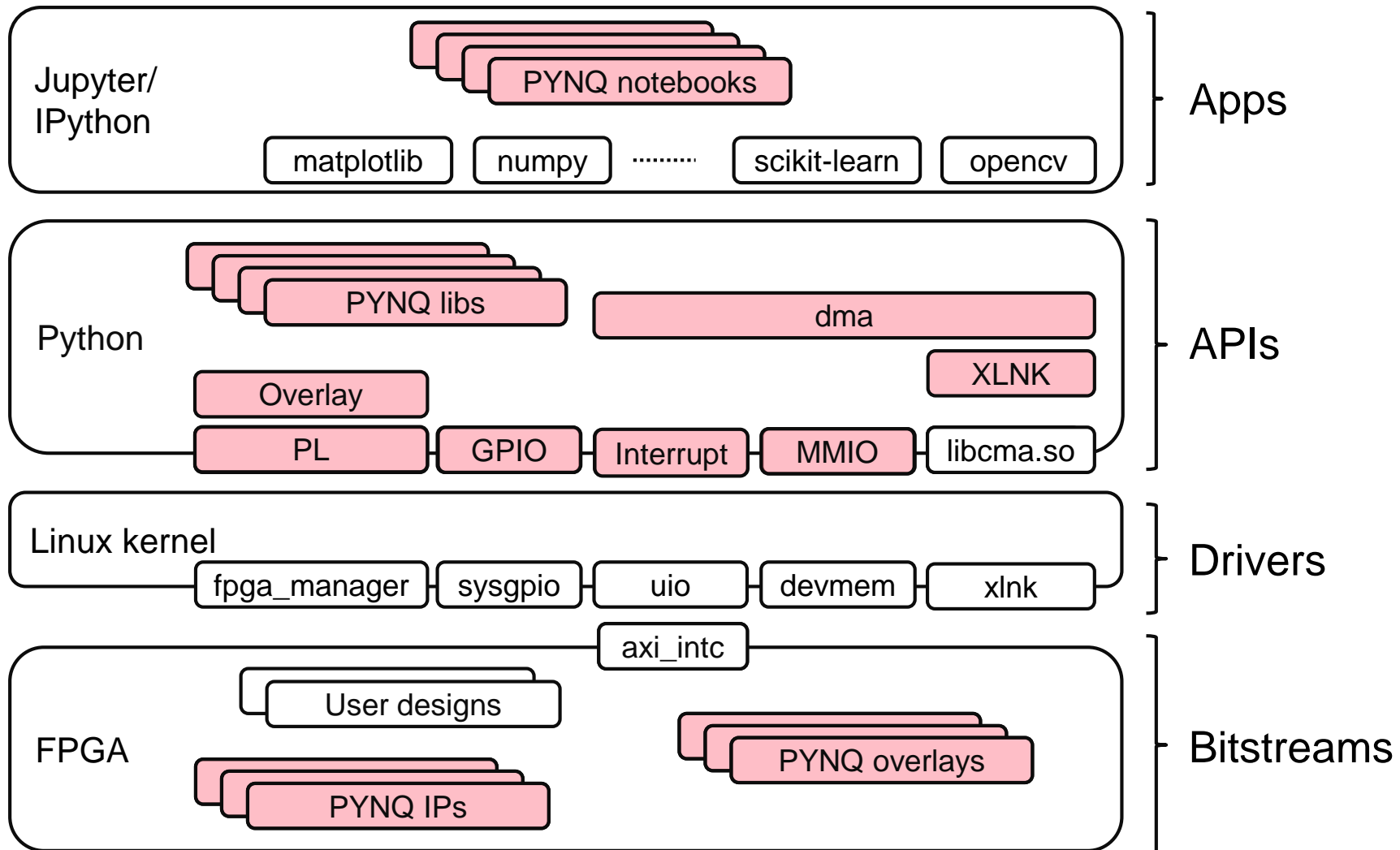
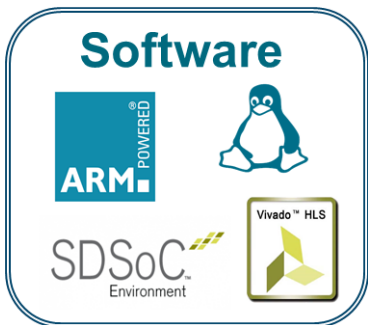
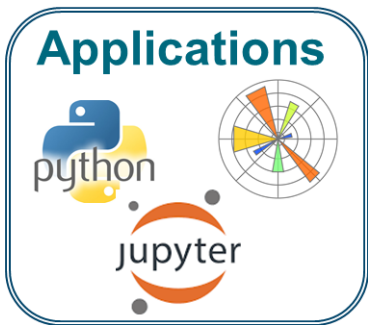


PYNQ provides Linux Drivers for PS-PL Interfaces ... wrapped in Python Libraries

Zynq



PYNQ™ is a Framework



Vivado Design Metadata available from Python

- > **PYNQ passes the Vivado metadata file to the target platform**
 - >> Initially Vivado TCL file
 - >> Now moving to Hardware Handoff (HWH) file
- > **It then parses the Vivado metadata file to:**
 - >> Set Zynq clock frequencies automatically
 - >> Assign memory-map attributes for every IP
 - >> Assign default MMIO drivers to IP, when no drivers are specified
- > **Creates a Python dictionary for the IP in the bitstream from the metadata file**
 - >> Enables bitstream metadata to be queried and modified in Python at runtime

Hybrid Packages

- > **New *hybrid packages* are created by extending Python packages with additional files:**
 - >> Design Bitstream
 - >> Design metadata file
 - >> C drivers
 - >> Jupyter notebooks
- > **Hybrid packages enable software-style packaging and distribution of designs**
- > **Use the Python package installer, PIP to install a hybrid package just like any regular Python (software only) package**
 - >> Delivers package's files to target board
- > **Uses Python standard `setup.py` script for installation**

Software-style Packaging & Distribution of Designs

Enabled by new *hybrid packages*

The image displays four GitHub repository pages from Xilinx, each showing a different PYNQ notebook design:

- QNN-MO-PYNQ**: Shows a notebook titled "3. Open image to be classified" with Python code for image processing and a photo of a beagle.
- SPYN**: Shows a notebook titled "SPYN - III phase AC motor control" with objectives, a step-by-step guide, and a diagram of a motor control system.
- PYNQ-DL**: Shows a notebook titled "Resizing an image" with a hardware block diagram illustrating the data flow between PS (ARM), PL (Resize IP), and DRAM/DMA components.
- PYNQ-ComputerVision**: Shows a notebook titled "OpenCV Overlay: Filter2D and Dilate" with code for image processing and a diagram of a programmable logic device.

Download a design from GitHub with a single Python command:























```
pip3.6 install git+https://github.com/Xilinx/pynqDL.git
```

PYNQ enabling technologies



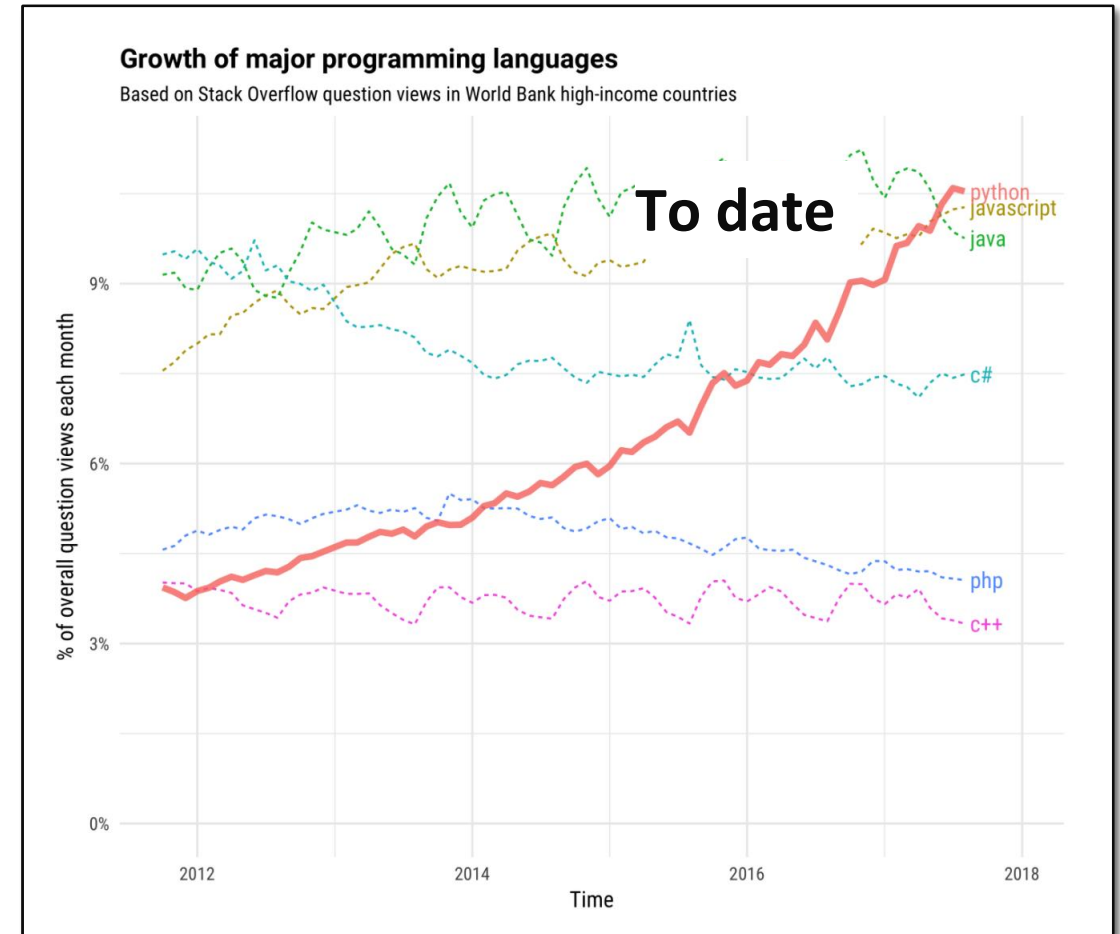
Python is increasingly the Language of Choice

Top Programming Languages, IEEE Spectrum, July'18

Language Rank	Types	Spectrum Ranking
1. Python	  	100.0
2. C++	  	98.4
3. C	  	98.2
4. Java	  	97.5
5. C#	  	89.8
6. PHP		85.4
7. R		83.3
8. JavaScript	 	82.8
9. Go	 	76.7
10. Assembly		74.5

Python is listed as an embedded language for the first time

<https://spectrum.ieee.org/at-work/innovation/the-2018-top-programming-languages>

























<https://stackoverflow.blog/2017/09/06/incredible-growth-python/>

Python is the fastest growing language: driven by data science, AI, ML and academia

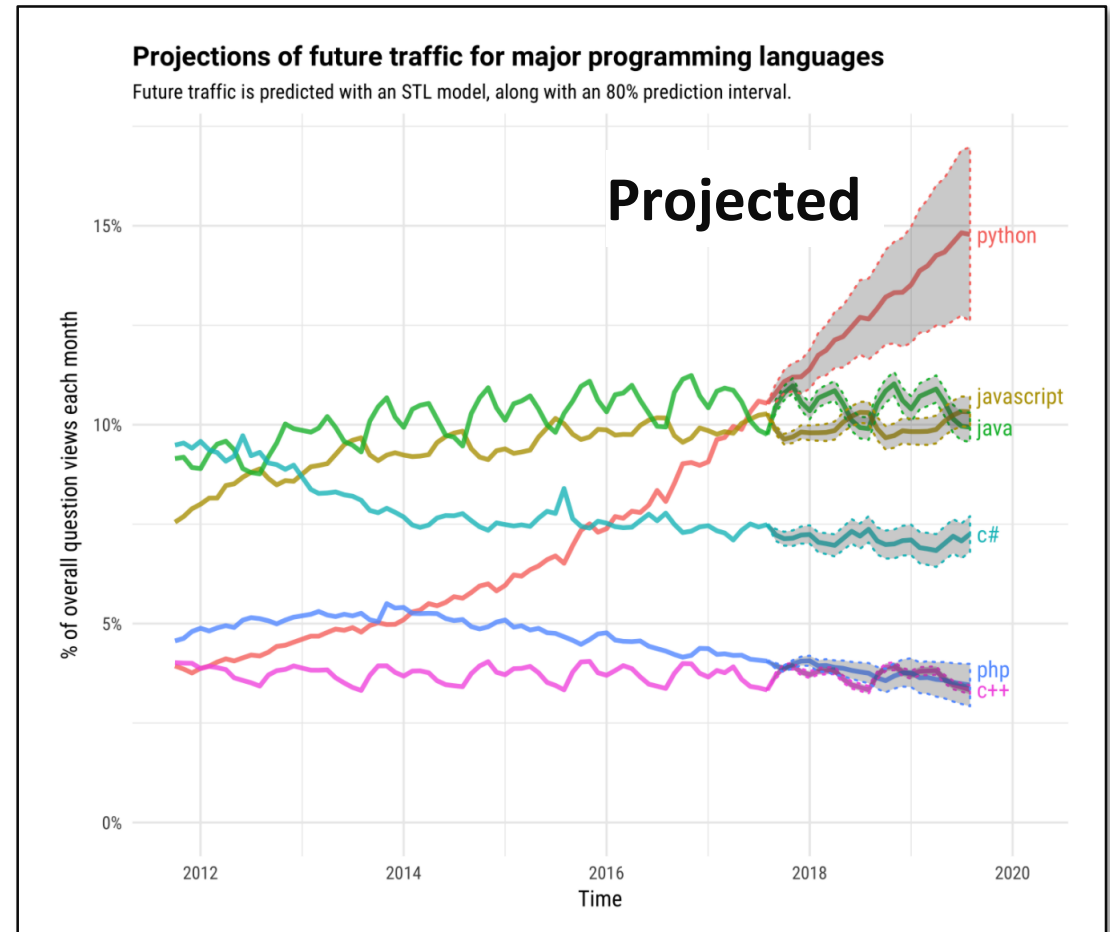
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Python is the fastest growing language: driven by data science, AI, ML and academia

Ecosystem advantage: there's a Python library for that...

200,00+ projects

1.6M+ releases

2.4M+ files

390,000+ users



The Python Package Index (PyPI) is a repository of software for the Python programming language.

PyPI helps you find and install software developed and shared by the Python community. [Learn about installing packages.](#)

Package authors use PyPI to distribute their software. [Learn how to package your Python code for PyPI.](#)

<https://pypi.org> retrieved 19 Apr 2019

CPython is written in C ... and most popular C/C++ frameworks have Python libraries

Jupyter Notebooks ... the engine of data science

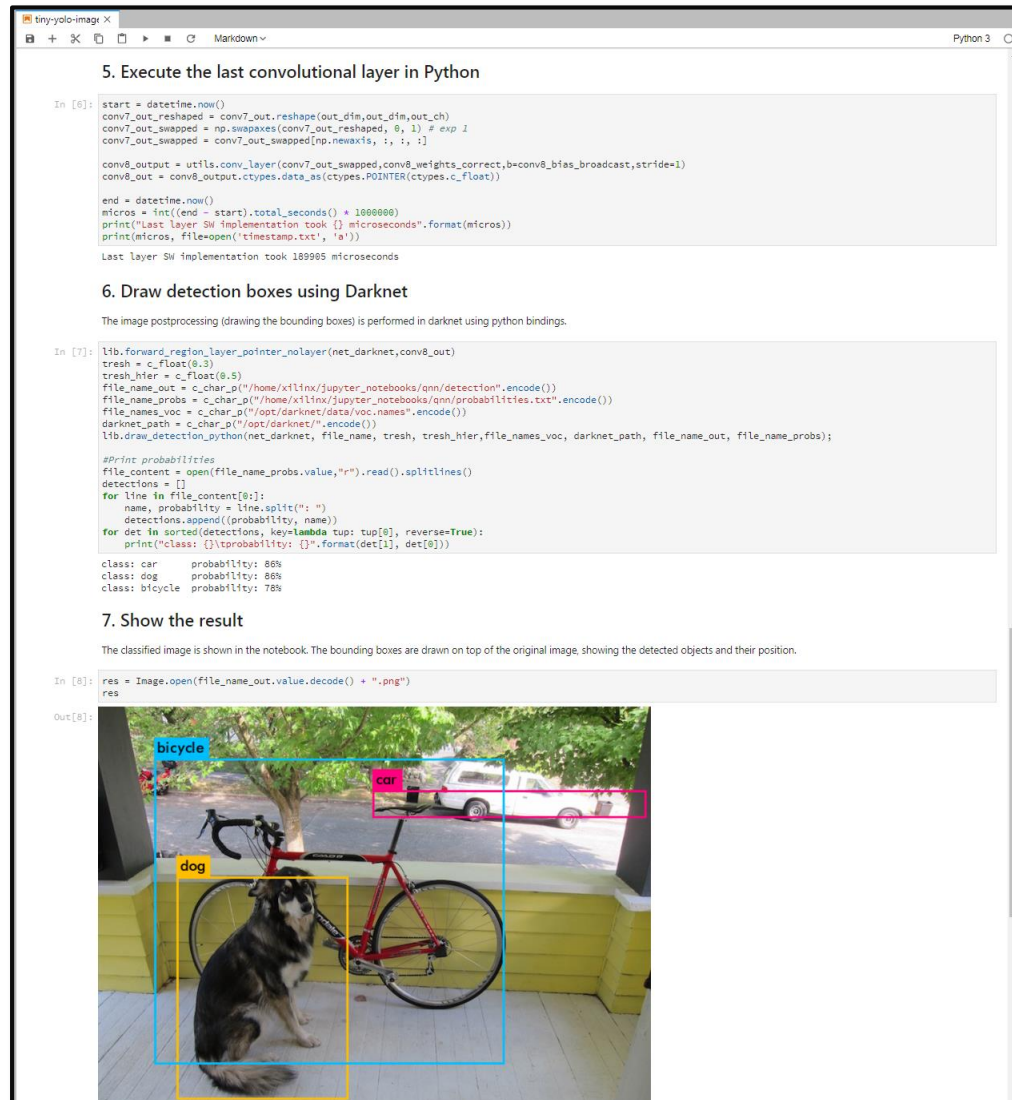


Open source browser-based, executable documents

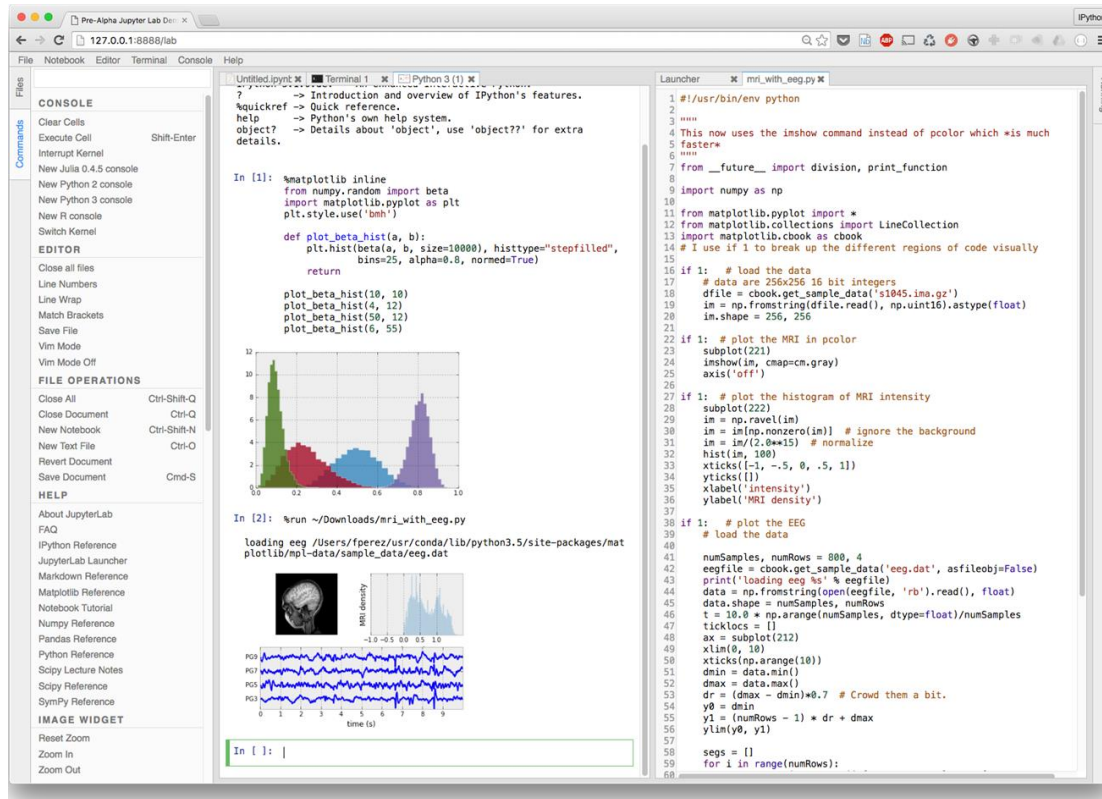
Live code, text, multimedia, graphics,
equations, widgets ...

1.7 million notebooks on GitHub

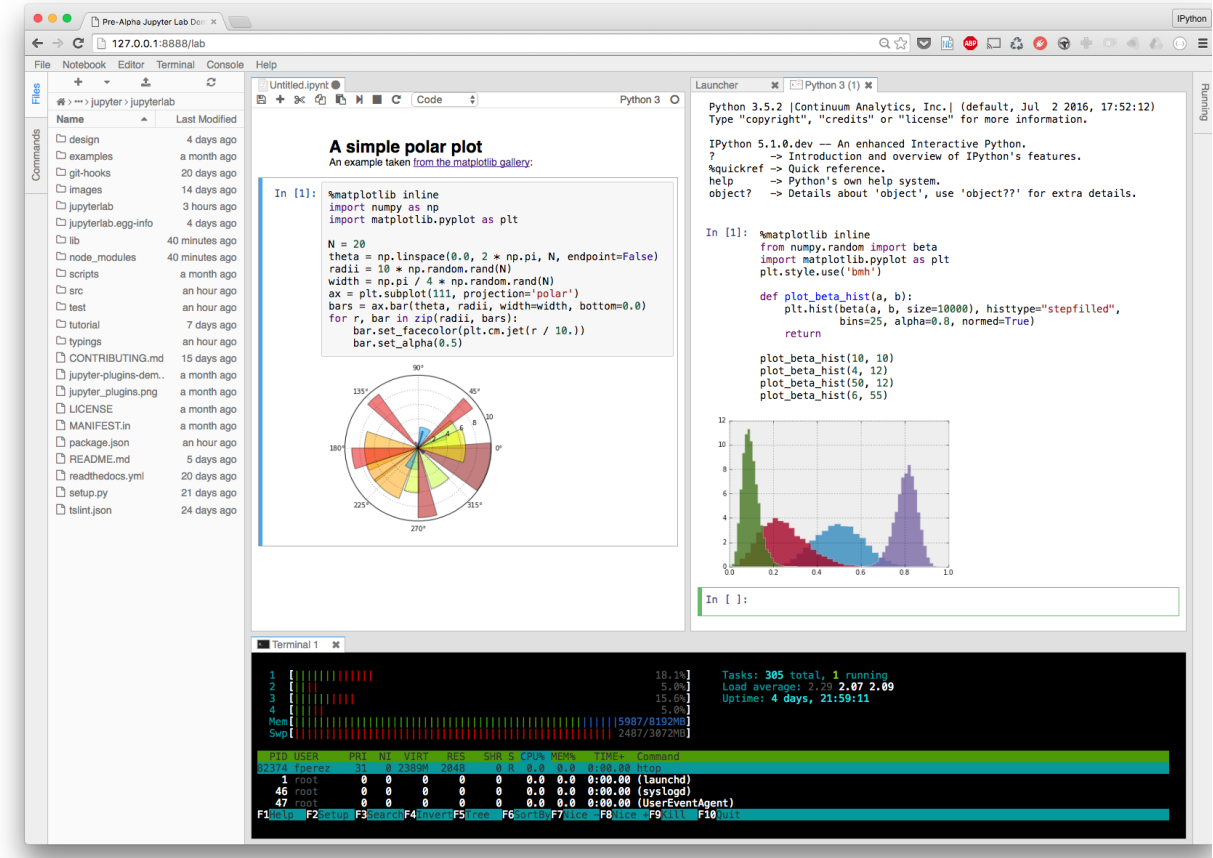
Taught to 1,000+ Berkeley data science students



JupyterLab: web-based IDE incl. Notebooks



Jupyter Notebook is now one of many plug-ins within the JupyterLab integrated development environment



JupyterLab - an open-source, extensible IDE in a browser

PYNQ enabled boards



PYNQ-enabled boards

> Python productivity for Zynq

- >> Open source
- >> Build image for other Zynq boards

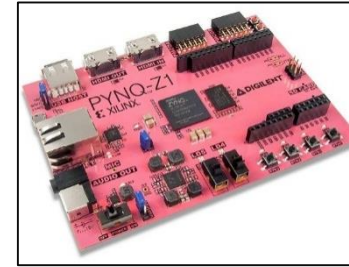
> Downloadable SD card image

- >> Zynq 7000
 - PYNQ-Z1 (Digilent)
 - PYNQ-Z2 (TUL)
- >> Zynq MPSoC
 - Ultra96 (Avnet)
 - ZCU104 (Xilinx)
- >> Zynq RFSoc
 - ZCU111 RFSoc (Xilinx)

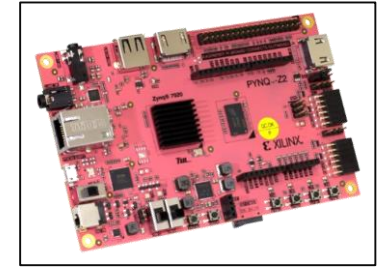
ZYNQ™

ZYNQ[®]
MPSoC

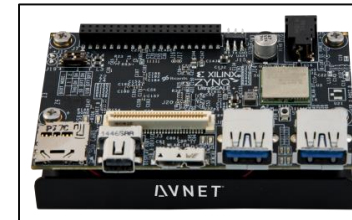
ZYNQ[®]
RFSoc



PYNQ-Z1



PYNQ-Z2



Ultra96

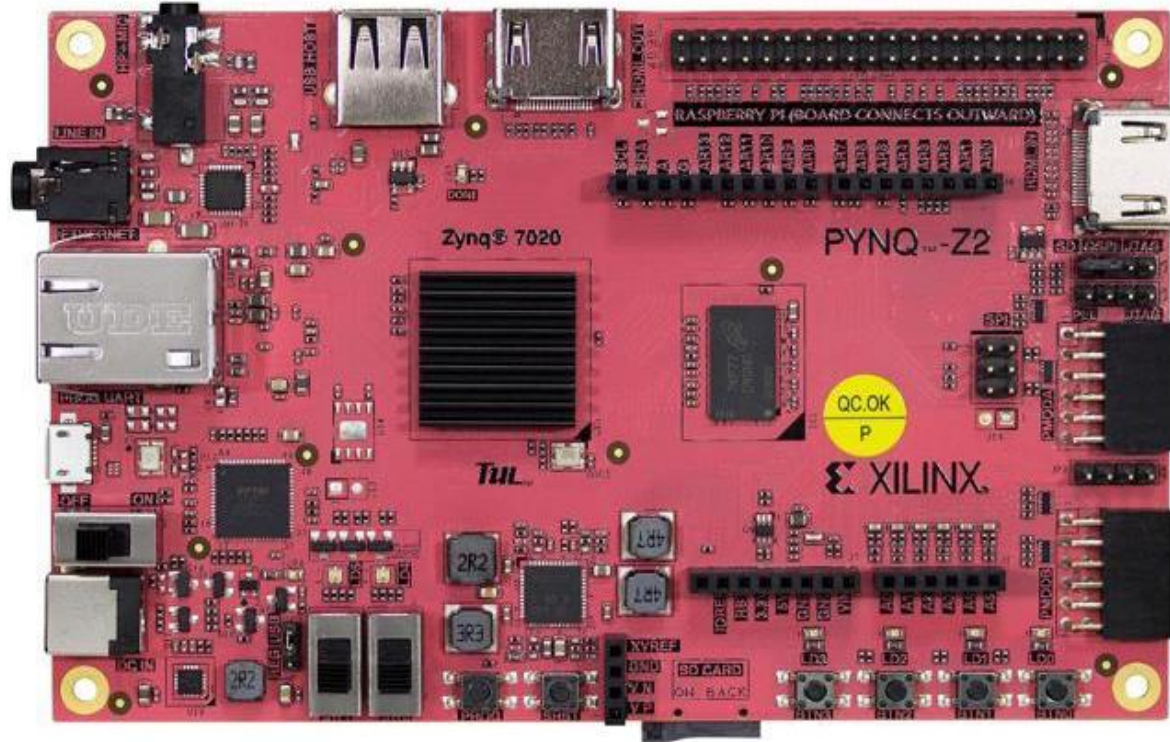


ZCU104



ZCU111

New PYNQ-Z2 Board



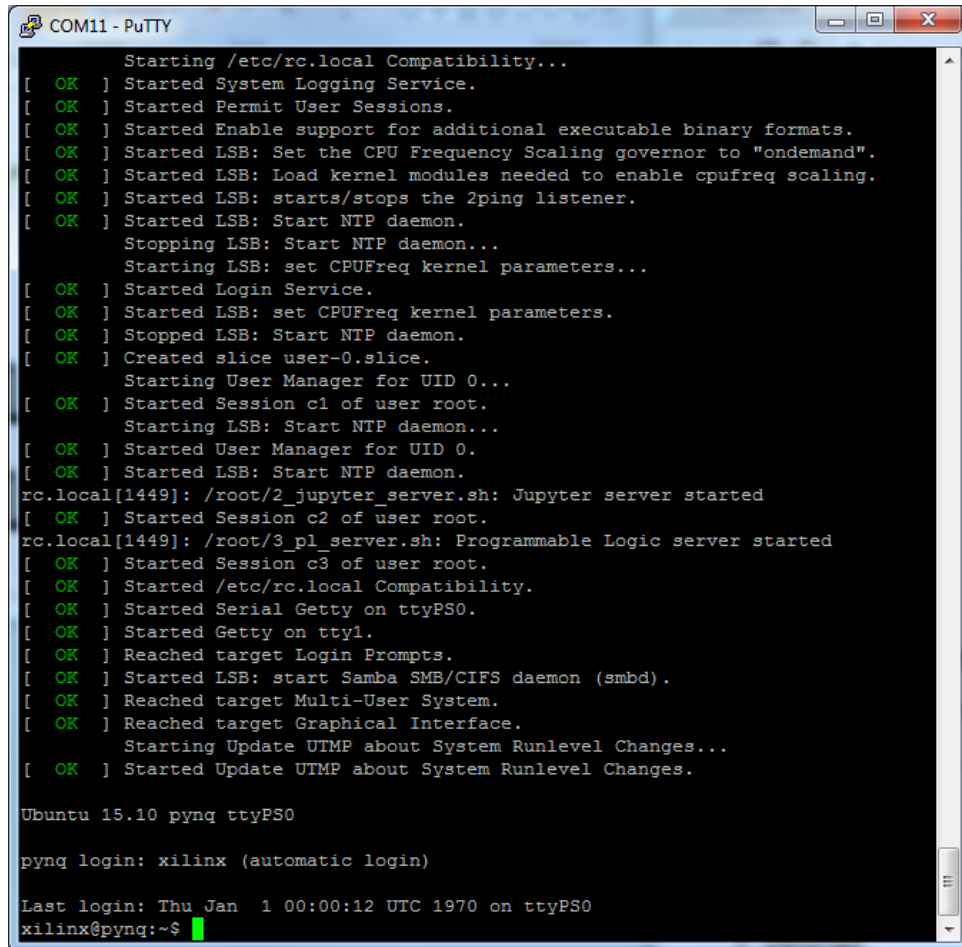
\$119/€119 or equivalent

- New PYNQ reference platform
- New stereo audio with on-board codec
- New Raspberry Pi connector
- Open source design
- Z2 manufactured in Taiwan by TUL
- Distributed globally by Premier Farnell
- Also Newegg in US
- Academic discounts & donations available

Benefits of PYNQ



Start using PYNQ out-of-the-box



```
COM11 - PuTTY
Starting /etc/rc.local Compatibility...
[ OK ] Started System Logging Service.
[ OK ] Started Permit User Sessions.
[ OK ] Started Enable support for additional executable binary formats.
[ OK ] Started LSB: Set the CPU Frequency Scaling governor to "ondemand".
[ OK ] Started LSB: Load kernel modules needed to enable cpufreq scaling.
[ OK ] Started LSB: starts/stops the 2ping listener.
[ OK ] Started LSB: Start NTP daemon.
Stopping LSB: Start NTP daemon...
Starting LSB: set CPUFreq kernel parameters...
[ OK ] Started Login Service.
[ OK ] Started LSB: set CPUFreq kernel parameters.
[ OK ] Stopped LSB: Start NTP daemon.
[ OK ] Created slice user-0.slice.
Starting User Manager for UID 0...
[ OK ] Started Session c1 of user root.
Starting LSB: Start NTP daemon...
[ OK ] Started User Manager for UID 0.
[ OK ] Started LSB: Start NTP daemon.
rc.local[1449]: /root/2_jupyter_server.sh: Jupyter server started
[ OK ] Started Session c2 of user root.
rc.local[1449]: /root/3_pl_server.sh: Programmable Logic server started
[ OK ] Started Session c3 of user root.
[ OK ] Started /etc/rc.local Compatibility.
[ OK ] Started Serial Getty on ttyPS0.
[ OK ] Started Getty on tty1.
[ OK ] Reached target Login Prompts.
[ OK ] Started LSB: start Samba SMB/CIFS daemon (smbd).
[ OK ] Reached target Multi-User System.
[ OK ] Reached target Graphical Interface.
Starting Update UTMP about System Runlevel Changes...
[ OK ] Started Update UTMP about System Runlevel Changes.

Ubuntu 15.10 pynq ttyPS0

pynq login: xilinx (automatic login)

Last login: Thu Jan  1 00:00:12 UTC 1970 on ttyPS0
xilinx@pynq:~$
```

> PYNQ delivered as downloadable SD card image

>> Linux preconfigured

> Additional packages and drivers pre-installed

>> USB peripheral drivers: webcams, wifi modules ...

> PYNQ is for Zynq

>> PYNQ image is portable to other Zynq boards

Start using Zynq out of the box ✓

Desktop Linux

> Network/Internet access

- >> “apt-get” to install packages from Ubuntu universe
- >> Samba(Network drive)
- >> Web services

> Git directly on board



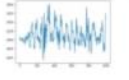
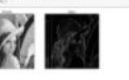




> Compilers and other development tools

- >> Gcc,, MicroBlaze, RISC-V

> Python packages

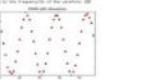
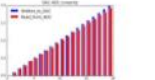


- >> “pip install”
- >> PYNQ Community examples

A selection of projects from the PYNQ community is shown below. Note that some examples are built on different versions of the PYNQ image.

Binary Neural Network Xilinx Labs, NTNU, University Sydney 	SPynq: NTUA Greece 	Processing noisy filters PYNQ Japan user group 	Soft GPU for ZC706 Ruhr University Bochum 
Video Processing Vectorblox 	FIR filter example CU Boulder 	DMA and stream Tutorial 	CNN Example Imperial College London 

Example Notebooks

A selection of notebook examples are shown below that are included in the PYNQ image. The notebooks contain live code, and generated output from the code can be saved in the notebook. Notebooks can be viewed as webpages, or opened on a Pynq enabled board where the code cells in a notebook can be executed.

ADC waveforms 	DAC ADC example 	Downloading overlays 	Grove ADC 
---	---	--	---

Simplify downloading bitstreams to PL

> PYNQ 'Overlay' class

- >> Simplifies downloading bitstream
- >> two lines of code
- >> No Xilinx tools required

> Maintain many bitstreams on the SD card

- >> E.g. multiple different demos

> Can execute Python in browser, or from command line

```
from pynq import Overlay  
ol = Overlay('gray.bit')
```

Simply and fast way to configure Programmable Logic ✓

Simplify IP debug and prototyping

- > Debug of IP typically uses C/C++
- > SDK tools used to:
 - >> Compile test application
 - >> Download application to board
 - >> Step through code
- > PYNQ MMIO class allows peek/poke of IP registers from Python
 - >> Python executes directly on the board
 - >> No offline compilation, download loop
 - >> No SDK tools

C code – compile, debug from host

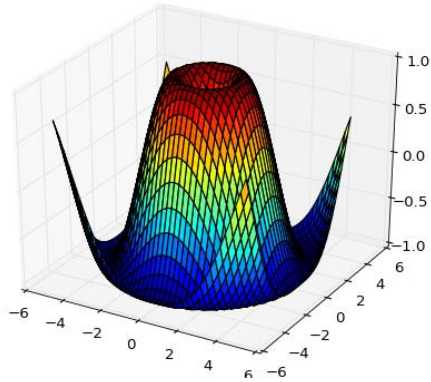
```
/* *****  
 * This function does a selftest on the IIC device and XIic driver as an  
 * example.  
 * @param DeviceId is the XPAR_IIC_instance>_DEVICE_ID value from  
 * xparameters.h.  
 * *****  
int IicSelfTestExample(u16 DeviceId)  
{  
    Status = XIic_CfgInitialize(&Iic, ConfigPtr, ConfigPtr->BaseAddress);  
    if (Status != XST_SUCCESS) {  
        return XST_FAILURE;  
    }  
  
    /* Perform a self-test to ensure that the hardware was built */  
    Status = XIic_SelfTest(&Iic);  
    if (Status != XST_SUCCESS) {  
        return XST_FAILURE;  
    }  
}
```

Python executes directly on target

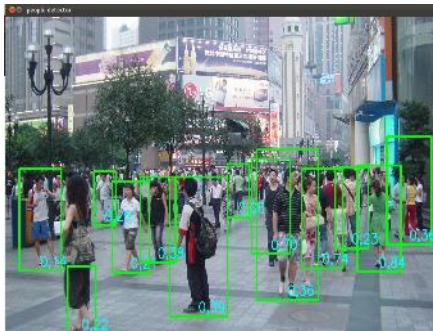
```
from pynq import MMIO  
  
# Map registers to MMIO instance  
my_ip = MMIO(my_ip_addr, LENGTH)  
  
# Write 0x1 to start IP  
my_ip.write(CONTROL_REGISTER, 0x1)  
# Check status register  
my_ip.read(STATUS_REGISTER)
```

Rapid testing and prototyping ✓

Python packages for data analysis and visualisation



Matplotlib



> Take advantage of Python for data analysis and processing

- >> NumPy
 - Scientific computing package for Python
- >> Matplotlib
 - Python 2D plotting library
- >> Pandas
 - Data analysis tools for Python
- >> OpenCV
 - Computer Vision and machine learning software

Optimized open-source software libraries ✓

Example

6. Detailed Classification Information

In addition to highest ranked class, it is possible to get the ranked couple of images of a car, an airplane, and a bird and place

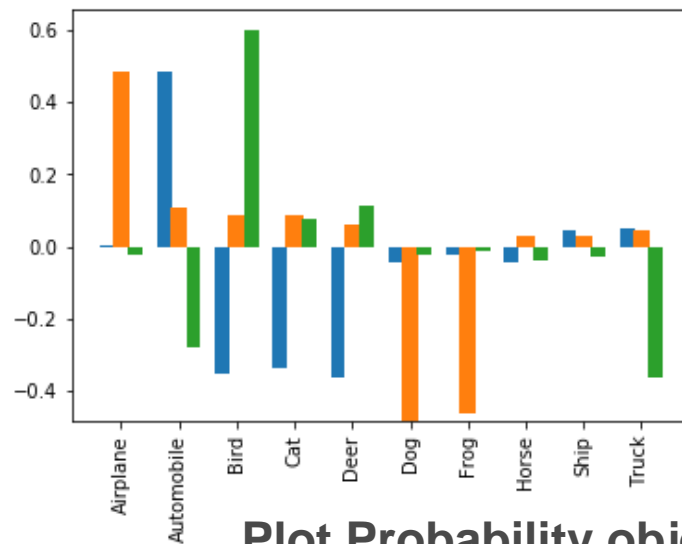


Probability object belongs to class

```
In [8]: from IPython.display import display
```

```
%matplotlib inline
import matplotlib.pyplot as plt

x_pos = np.arange(len(car_class))
fig, ax = plt.subplots()
ax.bar(x_pos - 0.25, (car_class/255)-1, 0.25)
ax.bar(x_pos, (air_class/255)-1, 0.3)
ax.bar(x_pos + 0.25, (bird_class/255)-1, 0.25)
ax.set_xticklabels(classifier.bnn.classes, rotation='vertical')
ax.set_xticks(x_pos)
ax.set
plt.show()
```



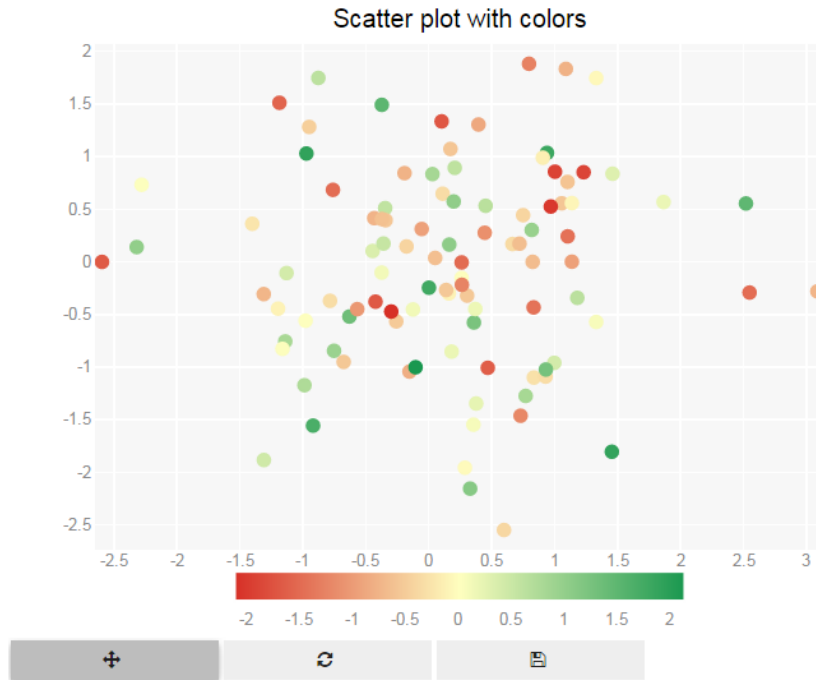
Plot Probability object belongs to class

```
oseconds
images per second
69 163 244 249 244 267 268]
```

```
oseconds
images per second
277 277 271 132 137 262 263 266]
```

```
oseconds
images per second
274 284 249 252 245 248 163]
```

Take advantage of interactive widgets

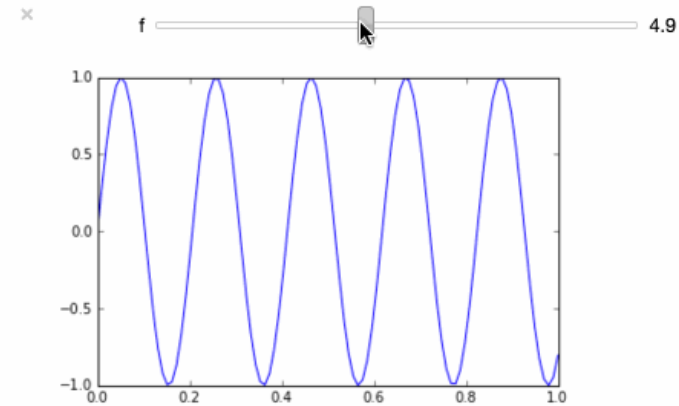


<http://jupyter.org/widgets.html>

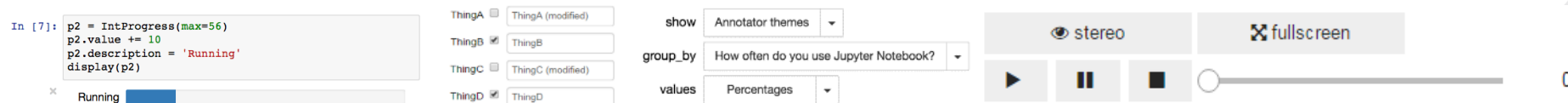
```
In [22]: from IPython.html.widgets import *
t = arange(0.0, 1.0, 0.01)

def pltsin(f):
    plt.plot(x, sin(2*pi*t*f))
    plt.show()

interact(pltsin, f=(1,10,0.1))
```



<https://blog.dominodatalab.com/interactive-dashboards-in-jupyter/>



Add intuitive graphical interfaces ✓

Why PYNQ is a Game-changer!

- > **PYNQ makes Zynq/ZynqU+ accessible to non-traditional customers**
- > **PYNQ delivers open source benefits**
 - >> Huge ecosystem
 - >> Extensive knowledge base
 - >> Amazing community support
- > **PYNQ enables highly-productive ...**
 - >> Prototyping
 - >> Debug
 - >> Verification
 - >> Evaluation
- > **PYNQ powers awesome demonstrators**
- > **PYNQ documentation flows are amazing**
 - >> Capture your own work
 - >> Capture work you want to re-use
- > **PYNQ designs can be ...**
 - >> Packaged, published and distributed

just like software
- > **“PYNQ makes FPGAs FUN again!”**,
J. Gray, Xilinx Power User

Community

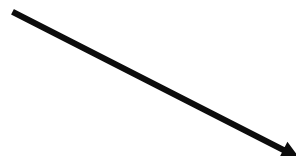


PYNQ: PYTHON PRODUCTIVITY ON ZYNQ



Home Get Started PYNQ-Z1 Bo

Community Projects



Selection of projects and notebooks

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Binary Neural Network

Xilinx Labs, NTNU, University Sydney



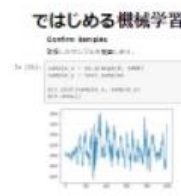
SPynq:

NTUA Greece



Processing noisy filters

PYNQ Japan user group



Soft GPU for ZC706

Ruhr University Bochum



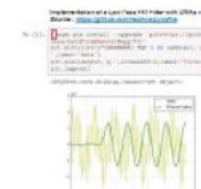
Video Processing

Vectorblox



FIR filter example

CU Boulder



DMA and stream

Tutorial



CNN Example

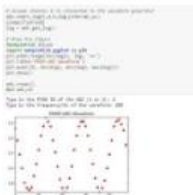
Imperial College London



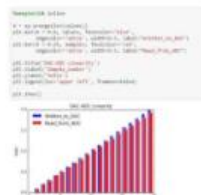
Example Notebooks

A selection of notebook examples are shown below that are included in the PYNQ image. The notebooks contain live code, and generated output from the code can be saved in the notebook. Notebooks can be viewed as webpages, or opened on a Pynq enabled board where the code cells in a notebook can be executed.

ADC waveforms



DAC ADC example



Downloading overlays



Grove ADC



All Feedback helps

Contribute

If you like it, star it!

The screenshot shows the GitHub repository page for Xilinx / PYNQ. The repository has 81 Watchers, 395 Stars, and 245 Forks. The navigation bar includes links for Features, Business, Explore, Marketplace, and Pricing, along with a search bar and Sign in / Sign up buttons. The repository tabs show Code, Issues (12), Pull requests (2), Projects (0), and Insights. A 'Join GitHub today' banner is displayed, stating that GitHub is home to over 28 million developers working together to host and review code, manage projects, and build software together. A green 'Sign up' button is visible. Below the banner, the text 'Python Productivity for ZYNQ' is followed by the URL <http://www.pynq.io/> and a 'pynq' badge.

Annotations on the image:

- A red arrow points from the word "Contribute" to the "Issues" tab, which is labeled "Issues, feature requests".
- A red arrow points from the text "If you like it, star it!" to the "Star" button.

Summary



- > PYNQ is Python productivity for Zynq
- > Everything runs on Zynq, access via a browser
- > Support for Zynq Ultrascale+
- > Overlays are hardware libraries and enable software developers to use Zynq
- > Provides a rapid prototyping framework for hardware developers



pynq.io

pynq.readthedocs.org

github.com/Xilinx/PYNQ

tul.com.tw/ProductsPYNQ-Z2.html

pynq.io/support

Adaptable.
Intelligent.

