MICROCHIP

To whom it may concern

September 28, 2020

Subject: Letter of interest for DTU Compute project: Software-Defined Hardware (SDH) w.Martin Schoeberl

Microchip is a semiconductor company and the Denmark design center excels in developing high speed Ethernet swich ASICs. For these ASIC designs all digital hardware is described using description languages such as VHDL, Verilog and System Verilog. These languages do in general not provide a larger abstraction, which in terms cause long development times and with the larger number of code lines also more errors we need to make sure are found and corrected prior ASIC production.

For future development we are very interested in the attempts to improve the design throughput and the project from prof. Martin Schoeberl on Software-Defined Hardware addresses exactly this.

If you should have any questions or require any additional information, please feel free to contact me at + 45-4485-5954 or via email at thomas.aakjer@microchip.com.

Sincerely

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