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Letter of interest for DTU Compute project: Software-Defined Hardware (SDH) w.Martin Schoeberl

Comcores can hereby confirm our interest in the Software-Defined-Hardware project (SDH).

Comcores is an IP component provider developing components for high speed ASICs and systems. All digital hardware development is taking place using description languages such as VHDL, Verilog and System Verilog. These languages do in general not provide a larger abstraction, which in terms cause long development times for system implementations.

For future development we are very interested in the attempts to improve the design throughput and the project from prof. Martin Schoeberl on Software-Defined Hardware addresses exactly this.

If you should have any questions or require any additional information, please feel free to contact me at + 45-27113480 or via email at tgn@comcores.com.

Best Regards

Thomas Nørgaard

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