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DFF research project cooperation/support

Dear Martin,

This letter hereby states that, we (Syosil ApS), will monitor and support this project closely as it is relevant for our core business. We deliver predominantly design and verification support to our customers. Especially, functional verification of hardware designs which can be "Software defined Hardware" as this project utilizes. We are experts in the field of functional verification using methods such as constrained random verification (CRV) and formal methods. The former in context of UVM. Hence, we will provide consulting and training in connection with any questions related to functional verification, UVM and CRV for the project.

Sincerely yours

SyoSil

Jacob Sander Andersen
CTO