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Letter of Interest

Napatech is hereby showing our interest in following the Software-Defined Hardware project (SDH).

The mixture of tight time-to-market constraints and an increasing design complexity associated with the design and development of hybrid systems consisting of both digital hardware and software of today implies efficient design, verification and qualification methodologies are more important than ever.

Besides following the project, we will be able to contribute to the project by adding use-cases and challenges during the project.

Best regards,

Jesper Birch FPGA Liaison Manager