# Unit 5: Solid State Microward Device.

#### \* Pin Diode:

#### Symbol:

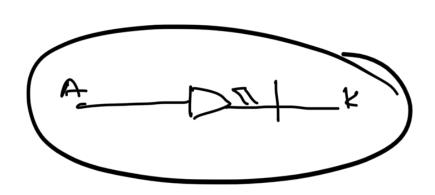


fig. Pin Diode

#### Internal Structure:

Intrinsic Layer

Anode

P

i

n

costnode

The diode consists of p-region and n-region which is separated by the intinsic semiconductor material.

p-region = hole majority.
n-region = election majority.

- intrinsic =) no free =) insulator bet w charge n and pregion.
- i-region =) high =) obstructs flow of resistance electrons to poss through it.

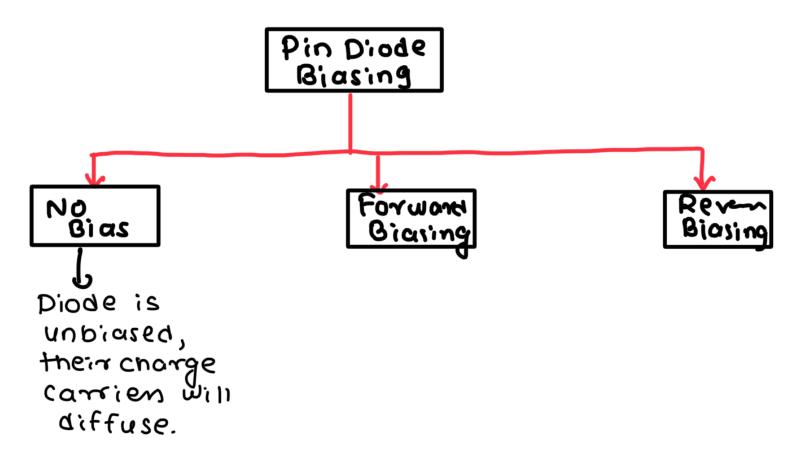
#### Characteristics:

Due to invinsic layer betw p and n region

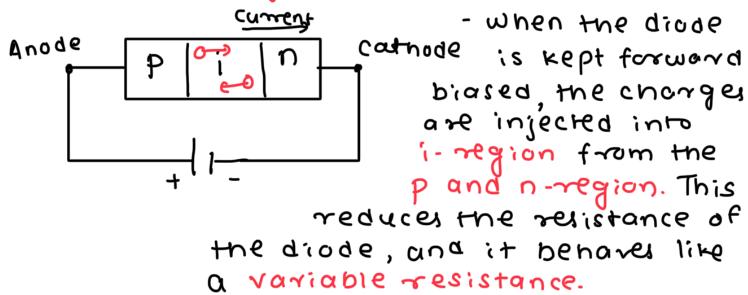
Acts of low capacitance

High reverse breakdown voitage i.e. PIV is high.

# \* Biasing:



### 2. Forward Biasing:



## 3. Reverse Biasing:

about a sendo

- As reverse bias condition voltage in charge corriens start to move from internsic layer

all charges are moved which is known as swept voltoges.

The 'd' increases so 'c' decreases, thefor Pin diode acts as variable capacitance.

#### Pin Diode:

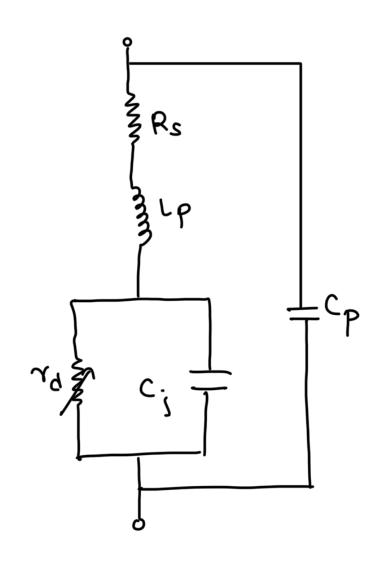
## At low freq model:

Forward => ~d behaves Biased like variable relistance.

Reverse => Cy benaves
Biared like variable
capacitance.

## At high freq. model:

RF component.



## \* Application:

#### I. Pin diode as Switch:

- forward blossed => small relitance - revere blossed => large relistance.
- 2. Pin diode of RF attenuator:
- 3. Pin diode as RF modulator.

- 4. Pin diode is used as photo detector.
- 5. High voltage rectifier.

# \* Schottky Borrier Diode:

A-D-K

metal

conta

- Used for it's low turn-on voltage
- Fast recover time
- Widely used for radio freq, RF application like mixer.

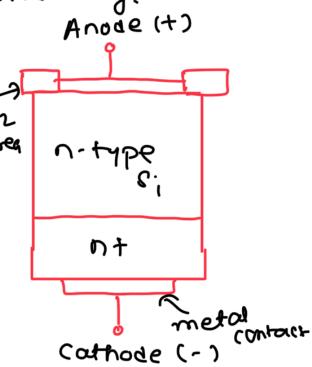
#### Construction:

- Inction is formed betwoeld and semiconducter known as schotting borrier.
- Metals: Platinum, chronium
- Semiconducte: n-type silicon
- Tungs
  ten
  Ceno
  mic
  nom.

  metau
  contact
- Anode => metal, couthode => n-type semicon.
- schottey bessie =) tost switching.

## Operation:

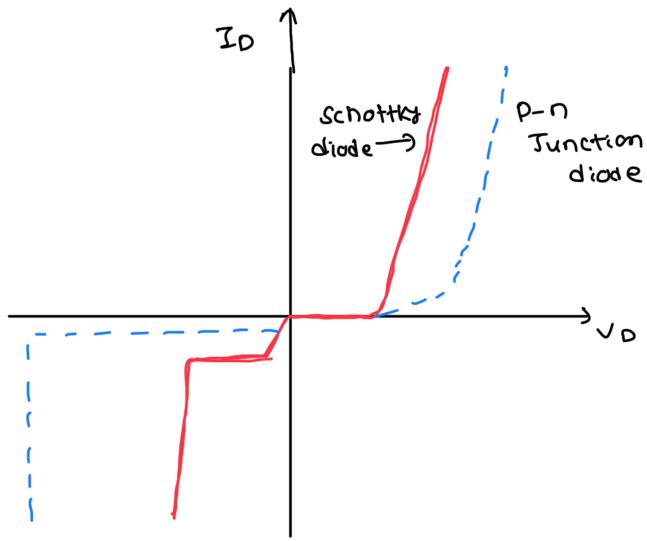
- · Electrons in diff. materials Jioz have diff. potential energy, screen
- N-type > electrons
  semiconductor in metaul
  potential entry
- Twhen both comes on contact, frow of electrons in both



directions storts.

- voltage applied to schotte, diode, such that metal is positive w.r.t. semiconductor
- voitage will oppose built in potential and makes easie for ownerst flow.

### \* V-I characteristics:

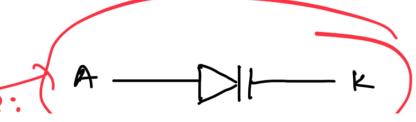


### \* Advantage

- 1. Turns on and off foster than PN Junetion
- 2. Very high freq. rango.
- 3. Low cat
  - 4. Simple

## \* Applications

- AC DC converes
- Microwave detector.

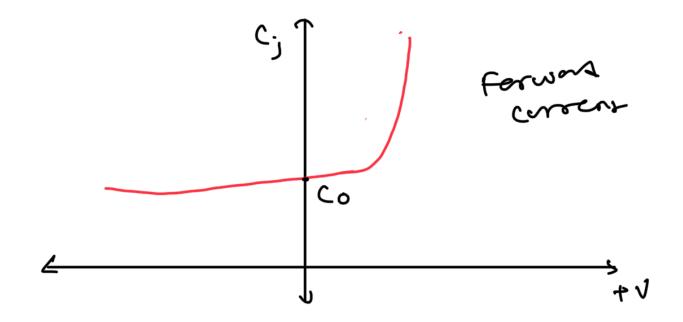


\* Varacter Diode:

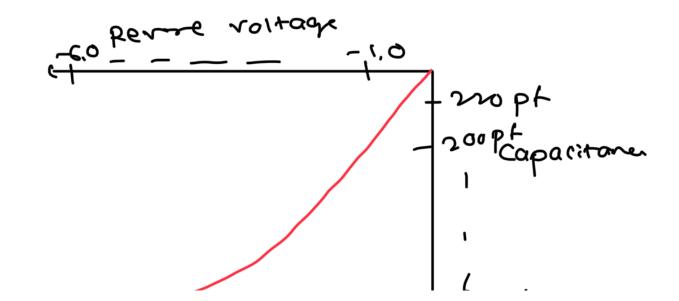
- A special diode that changes level of capacitance depending on the level of severe bios applied to diode.

- Types:

- 1. Abrupt Voracter
- 7- Hyper Amupt Vorracter.
- To construct GaAs is preferred became it has highest operating freq. Used for freq from 18GHZ up to 600 GHZ.
- The variation of capacitance with applied revere bion voltage:



The below graph represents the invience in capacitance or revoe voltore is decreased.



+ 20 Pf

## varacter voltage-cap. cume

## Roperation:

- p-n junction of semiconducting material and is always revene biased.
- Depletion zone depends on applied voltoge and this make capacitance vons.
- Twidth of depletion region increases with reverse bias & capacitonia decreases or severe bion increases

- Avalanche region is not wed on it destry: device

## \* Applications:

## 1. Freq. mutiplier:

- Freq. multiplication is phenomenon which results from non-linear characteristics.
- Capacitance of voractor diode vories w. v.to.
  reverse bios., diode acts as non-linear
  characteristics
  - The characteristic of diode is used for fry, multiplication.

## \* TED:

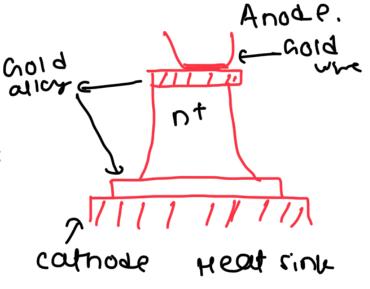
- TED are devices with no junctions as compared to microwave transisters which operate wirm junction/gats
- Fabricated from compound semiconductors such as GaAs, InP, Cd Te
- operate with hot electrons (energy > termed energy)

#### 1. Gun Diode:

- bulle semiconductor agrice.
- Bored on TED.

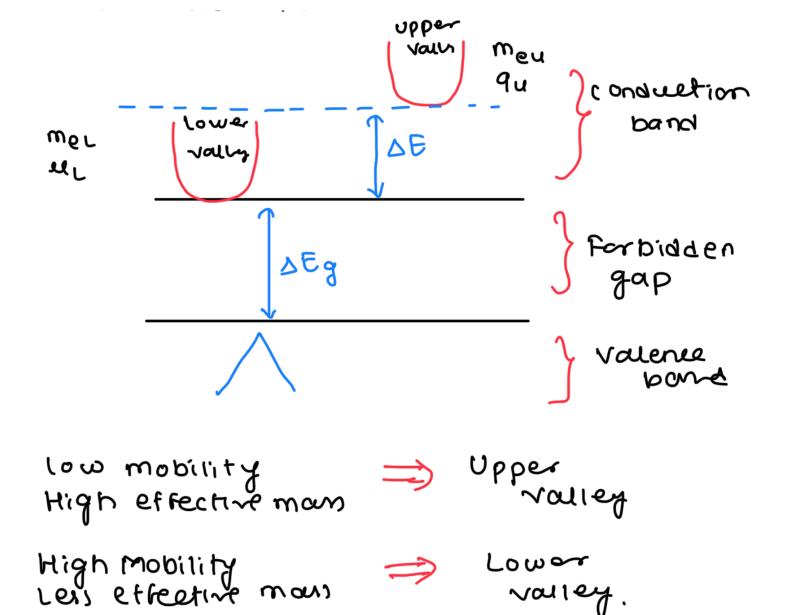
## · Construction:

- Heavily doped slice of N-doped sample of GaAs is taken.
- Two ends one soldered with gold alloy.



## · Two Valley Model Theory:

- TEE = materized which supports two valley model.
- From this model, negative relistance of gunn diode can be explained.
- Frengy bands for material which exhibits this model :-

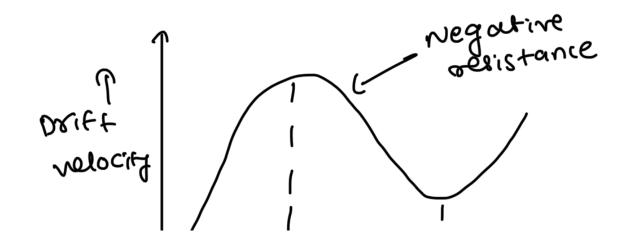


- Two valley model describer:
  - 1. The separation energy between lover and upper vailey must be larger than thermal energy of room temp.
  - 2. The separation between lower and upper valles must be less than forbidden gap. energy.
  - 3. Electrons in:

## · Operation:

- when very high electric fleed is applied

- arrois the slice of N-type heavily doped GaAs, electron flow towards the positive end of slice.
- With Increase in electric field across normal diode, velocity of electron increases no. of electrons flowing in turn current flow increases.
- GaAs having multiple energy level in it's energy.
- with increase in applied electric field, current reduces, which can be explained with the help of following steps:
  - 1. When very high electric field applied across gunn diode, valence electrons observed high energy and enter into conduction band. But energy of electrons is very high. They get transferred from conduction band to higher energy level.
  - 2. When electron from conduction band enters into higher energy level, its mobility decreases.
  - 3. Increme in velocity of decreases the electric = electron = current. field decrere



- Eventually Voltage acrus slice becomes sufficient to remove electrons from higher energy, so again current increase.