CHLOE HANKS

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College Station, TX 77845

EDUCATION

UTSA FullStack Web Developer Certification Apr 2023
- Follow Link to Chloe Hanks' Portfolio

University of Texas - San Antonio (Grade: A)

Bachelor of Science in Electrical Engineering (Mathematics minor) Apr 2017
- Utah Engineers Council Award & Scholarship

Bachelor of Art in Russian Apr 2017
- ACTFL Advanced-High/Superior Russian

Brigham Young University, Provo, UT (GPA 3.89)

Skills

- Schematic capture (Altium) and PCB layout
- Extensive test, analysis, and hands-on troubleshooting experience
- Strong LTSPICE simulation experience
- Consumer/Producer LabVIEW Design
- Proficient in datasheet component analysis
- Strong through-hole and SMD soldering
- Excellent leadership and mentoring
- Excellent documentation and presentation ability

EXPERIENCE

Southwest Institute of Technology (SwRI) – San Antonio, TX Jun 2021 – Nov 2022

Senior Research Engineer | Space Science and Instrumentation
- Troubleshooted Printed Circuit Boards (PCBs) to minimize crosstalk on low-level signals; tune delay lines to fractions of a nanosecond; mitigated anomalies observed in testing
- Troubleshooted and updated High Voltage Power Supply board to create bipolar voltages up to +/-6.5kV
- Wrote and performed test documentation from board- to system-level designs

Jacobs Technology (NASA) – Houston, TX Apr 2018 – Jun 2021

Lead Electrical Design Engineer **|** Portable Life Support System Ground Support Equipment (PLSS GSE)
- Served as technical lead and mentor 5+ electrical engineers to support a team of 50+ multidisciplinary engineers developing test rigs to measure the performance of next generation spacesuit components
- Created electrical system architectures for complex electro-mechanical test rig systems
- Wrote and perform extensive functional checkout procedures to prove system operation
- Designed and tested custom modular hardware solutions and niche applications through PCB design
- Troubleshooted PLSS electrical test systems to ensure best practices to protect Device Under Test (DUT)
- Reverse-engineered commercial products to understand how to improve upon custom designs
- Allocate electrical resources to fold into team's schedule and budget analysis

Electronics Design Engineer | Power Management
- Designed low power system to gauge capacity of Li-Ion batteries in an emergency flight system
- Developed system-wide power budget calculator based on flight data and user input to size Li-Ion batteries
- Created detailed documentation to explore nuanced design and component selection decisions

Intel Corporation – Hillsboro, OR Aug 2017 – Mar 2018

Analog Engineer | Power Delivery
- Designed and managed effective voltage regulation (VR) solutions for FPGA systems
- Recommended effective VR layout and via feedback to validation engineers to ensure workable outcomes
- Found design flaws and mitigated potential failures through investigation and analysis

National Instruments – Austin, Texas May 2016 – Aug 2016

Analog Hardware Intern | Data Acquisition and Control
- Defined and designed analog front-end power output stage with PWM drive and current sensing/control
- Developed custom schematic and PCB layouts for prototype board development
- Derived power and accuracy considerations to ensure analog front-end remained within specifications