# Machine-Level Representation

CSCI 2021: Machine Architecture and Organization

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With Slides from Bryant and O'Hallaron

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### Stack Overflow

# String Library Code

- Implementation of Unix function gets()
  - No way to specify limit on number of characters to read

```
/* Get string from stdin */
char *gets(char *dest)
{
   int c = getchar();
   char *p = dest;
   while (c != EOF && c != '\n') {
        *p++ = c;
        c = getchar();
   }
   *p = '\0';
   return dest;
}
```

- Similar problems with other library functions
  - strcpy, strcat: Copy strings of arbitrary length
  - scanf, fscanf, sscanf, when given %s conversion specification

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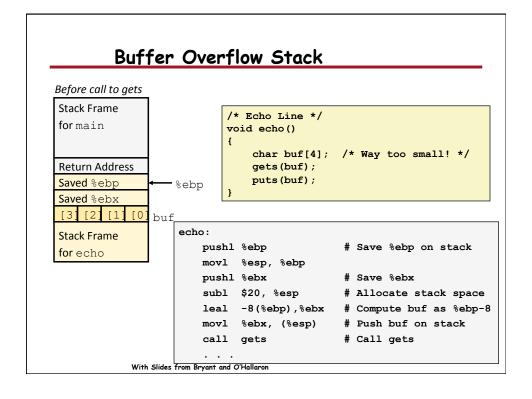
# Vulnerable Buffer Code

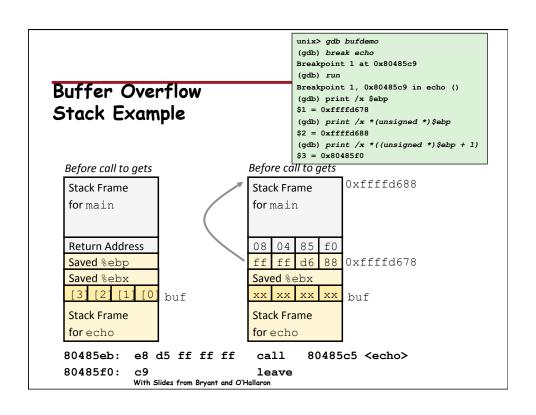
```
/* Echo Line */
void echo()
{
    char buf[4]; /* Way too small! */
    gets(buf);
    puts(buf);
}
```

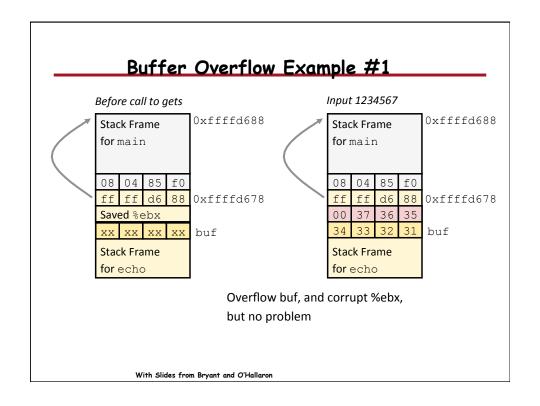
```
void call_echo() {
    echo();
}
```

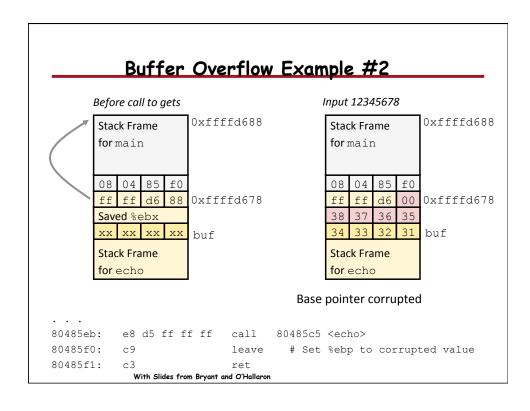
```
unix>./bufdemo
Type a string:1234567
1234567
unix>./bufdemo
Type a string:12345678
Segmentation Fault
unix>./bufdemo
Type a string:123456789ABC
Segmentation Fault
```

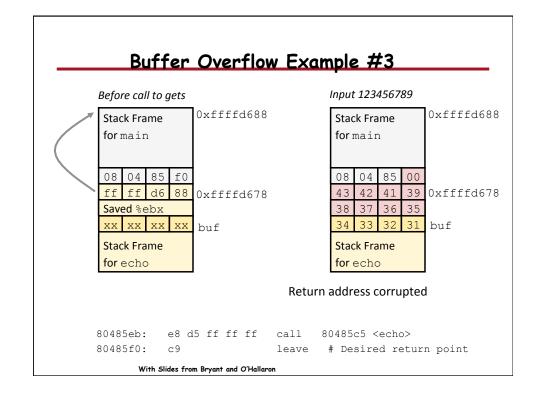
#### Buffer Overflow Disassembly echo: 80485c5: push %ebp 80485c6: 89 e5 mov %esp, %ebp push 80485c8: 53 %ebx 80485c9: 83 ec 14 sub \$0x14,%esp 80485cc: 8d 5d f8 lea 0xffffffff8(%ebp),%ebx 80485cf: 89 1c 24 mov %ebx, (%esp) call 8048575 <gets> 80485d2: e8 9e ff ff ff 80485d7: 89 1c 24 mov %ebx, (%esp) call 80483e4 <puts@plt> 80485da: e8 05 fe ff ff 80485df: 83 c4 14 \$0x14,%esp add 80485e2: 5b %ebx pop 80485e3: 5d pop %ebp 80485e4: c3 call echo: 80485eb: e8 d5 ff ff ff call 80485c5 <echo> 80485f0: c9 leave 80485f1: c3 ret With Slides from Bryant and O'Hallaron

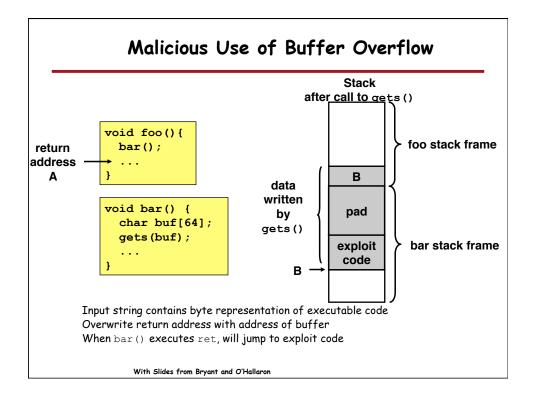












### Avoiding Overflow Vulnerability

```
/* Echo Line */
void echo()
{
    char buf[4]; /* Way too small! */
    fgets(buf, 4, stdin);
    puts(buf);
}
```

Use Library Routines that Limit String Lengths

- fgets instead of gets
- strncpy instead of strcpy
- Don't use scanf with %s conversion specification
  - Use fgets to read the string

# Yet Another Example

```
main() {
  unsigned long long II = 0xdeadbeefbeefdead;
  unsigned int i = 0x12345678;
  printf("%x %x\n", II, i);
}
```

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# Yet Another Example

Main stack frame

### System-Level Protections

- Randomized stack offsets
  - At start of program, allocate random amount of space on stack
  - Makes it difficult for hacker to predict beginning of inserted code
- · Nonexecutable code segments
  - In traditional x86, can mark region of memory as either "read-only" or "writeable"
    - · Can execute anything readable
  - X86-64 added explicit "execute" permission

```
unix> gdb bufdemo (gdb) break echo
```

(gdb) run

(gdb) print /x \$ebp \$1 = 0xffffc638

(gdb) run

(gdb) print /x \$ebp \$2 = 0xffffbb08

(gdb) run

(gdb) print /x \$ebp

\$3 = 0xffffc6a8

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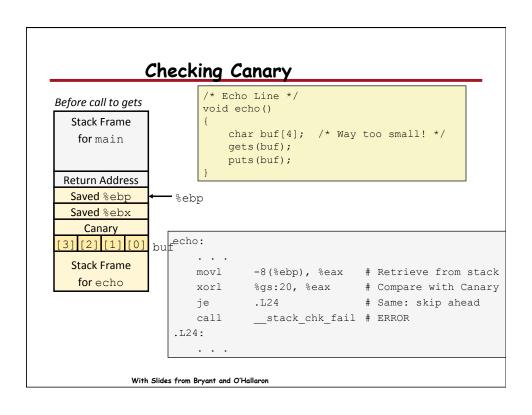
#### Stack Canaries

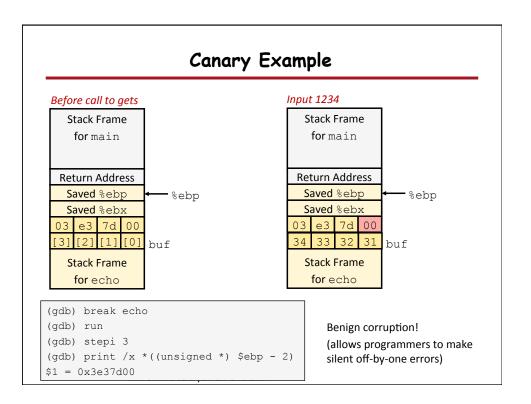
- Idea
  - · Place special value ("canary") on stack just beyond buffer
  - · Check for corruption before exiting function
- GCC Implementation
  - -fstack-protector
  - -fstack-protector-all

unix>./bufdemo-protected
Type a string:1234
1234
unix>./bufdemo-protected
Type a string:12345
\*\*\* stack smashing detected \*\*\*

```
Protected Buffer Disassembly
                                                       echo:
804864d:
                               push
           89 e5
804864e:
                               mov
                                      %esp,%ebp
8048650:
           53
                               push
                                     %ebx
8048651:
           83 ec 14
                                     $0x14,%esp
                               sub
8048654:
           65 al 14 00 00 00
                               mov
                                     %gs:0x14,%eax
           89 45 f8
804865a:
                                     %eax,0xfffffff8(%ebp)
           31 c0
804865d:
                               xor
                                     %eax,%eax
                                     0xffffffff4(%ebp),%ebx
         8d 5d f4
804865f:
                               lea
         89 1c 24
8048662:
                                     %ebx,(%esp)
                               mov
8048665:
         e8 77 ff ff ff
                              call
                                     80485e1 <gets>
804866a:
         89 1c 24
                                     %ebx,(%esp)
                              mov
804866d:
         e8 ca fd ff ff
                              call
                                     804843c <puts@plt>
         8b 45 f8
8048672:
                                     mov
8048675:
         65 33 05 14 00 00 00 xor
                                     %gs:0x14,%eax
804867c:
                                     8048683 <echo+0x36>
           74 05
                               jе
804867e:
           e8 a9 fd ff ff
                               call
                                     804842c <FAIL>
8048683:
           83 c4 14
                               add
                                     $0x14,%esp
8048686:
           5b
                               pop
                                     %ebx
8048687:
           5d
                               pop
                                     %ebp
8048688:
          с3
                               ret
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```

```
Setting Up Canary
                         /* Echo Line */
Before call to gets
                         void echo()
  Stack Frame
    for main
                             char buf[4]; /* Way too small! */
                             gets(buf);
                             puts(buf);
 Return Address
  Saved %ebp
                   −%ebp
  \textbf{Saved}~\$\texttt{ebx}
     Canary
[3] [2] [1] [0] buf
  Stack Frame
                    echo:
   for echo
                                   %gs:20, %eax
                                                      # Get canary
                                  %eax, -8(%ebp)
                                                      # Put on stack
                        movl
                        xorl
                                  %eax, %eax
                                                      # Erase canary
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```





### Worms and Viruses

- Worm: A program that
  - · Can run by itself
  - Can propagate a fully working version of itself to other computers
- · Virus: Code that
  - · Add itself to other programs
  - Cannot run independently
- Both are (usually) designed to spread among computers and to wreak havoc

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# Non-Local Jumps

### Nonlocal Jumps: setjmp/longjmp

- Powerful (but dangerous) user-level mechanism for transferring control to an arbitrary location
  - · Controlled to way to break the procedure call / return discipline
  - · Useful for error recovery and signal handling
- int setjmp(jmp\_buf j)
  - · Must be called before longjmp
  - · Identifies a return site for a subsequent longimp
  - Called once, returns one or more times
- Implementation:
  - Remember where you are by storing the current register context, stack pointer, and PC value in jmp buf
  - · Return 0

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### setjmp/longjmp (cont)

- void longjmp(jmp buf j, int i)
  - · Meaning:
    - return from the setjmp remembered by jump buffer j again ...
    - · ... this time returning i instead of 0
  - · Called after setjmp
  - · Called once, but never returns
- longjmp Implementation:
  - Restore register context (stack pointer, base pointer, PC value) from jump buffer j
  - Set %eax (the return value) to i
  - Jump to the location indicated by the PC stored in jump buf j

### setjmp/longjmp Example

```
#include <setjmp.h>
jmp_buf buf;

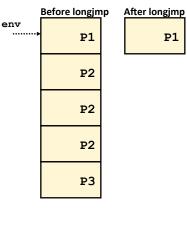
main() {
    if (setjmp(buf) != 0) {
        printf("back in main due to an error\n");
    else
        printf("first time through\n");
    p1(); /* p1 calls p2, which calls p3 */
}
...
p3() {
    <error checking code>
    if (error)
        longjmp(buf, 1)
}
```

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# Limitations of Nonlocal Jumps

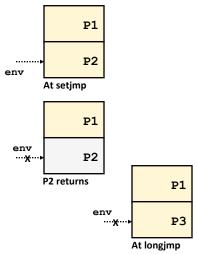
- · Works within stack discipline
  - Can only long jump to environment of function that has been called but not yet completed

```
jmp_buf env;
P1()
{
    if (setjmp(env)) {
        /* Long Jump to here */
    } else {
        P2();
    }
}
P2()
{        . . . P2();      . . . P3(); }
P3()
{
        longjmp(env, 1);
}
```



# Limitations of Long Jumps (cont.)

- Works within stack discipline
  - Can only long jump to environment of function that has been called but not yet completed



Procedures (x86-64)

%rax	%eax	%r8	%r8d
%rbx	%ebx	%r9	%r9d
%rcx	%ecx	%r10	%r10d
%rdx	%edx	%r11	%r11d
%rsi	%esi	%r12	%r12d
%rdi	%edi	%r13	%r13d
%rsp	%esp	%r14	%r14d
%rbp	%ebp	%r15	%r15d

%rax	Return value	%r8	Argument #5
%rbx	Callee saved	%r9	Argument #6
%rcx	Argument #4	%r10	Caller saved
%rdx	Argument #3	%r11	Caller Saved
%rsi	Argument #2	%r12	Callee saved
%rdi	Argument #1	%r13	Callee saved
%rsp	Stack pointer	%r14	Callee saved
%rbp	Callee saved	%r15	Callee saved

#### x86-64 Registers

- Arguments passed to functions via registers
  - If more than 6 integral parameters, then pass rest on stack
  - These registers can be used as caller-saved as well
- All references to stack frame via stack pointer
  - Eliminates need to update %ebp/%rbp
- Other Registers
  - 6 callee saved
  - 2 caller saved
  - 1 return value (also usable as caller saved)
  - 1 special (stack pointer)

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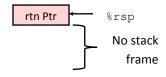
#### x86-64 Long Swap

```
void swap_1(long *xp, long *yp)
{
   long t0 = *xp;
   long t1 = *yp;
   *xp = t1;
   *yp = t0;
}
```

- Operands passed in registers
  - First (xp) in %rdi, second (yp) in %rsi
  - 64-bit pointers
- No stack operations required (except ret)
- Avoiding stack
  - Can hold all local information in registers

swap:

movq (%rdi), %rdx movq (%rsi), %rax movq %rax, (%rdi) movq %rdx, (%rsi) ret



#### x86-64 Locals in the Red Zone

```
/* Swap, using local array */
void swap_a(long *xp, long *yp)
{
    volatile long loc[2];
    loc[0] = *xp;
    loc[1] = *yp;
    *xp = loc[1];
    *yp = loc[0];
}
```

**Avoiding Stack Pointer Change** 

 Can hold all information within small window beyond stack pointer

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#### x86-64 NonLeaf without Stack Frame

```
/* Swap a[i] & a[i+1] */
void swap_ele(long a[], int i)
{
    swap(&a[i], &a[i+1]);
}
```

- No values held while swap being invoked
- No callee save registers needed
- rep instruction inserted as no-op
  - Based on recommendation from AMD

#### x86-64 Stack Frame Example

```
long sum = 0;
/* Swap a[i] & a[i+1] */
void swap_ele_su
   (long a[], int i)
{
    swap(&a[i], &a[i+1]);
    sum += (a[i]*a[i+1]);
}
```

- Keeps values of &a[i] and &a[i+1] in callee save registers
- Must set up stack frame to save these registers

```
swap ele su:
           %rbx, -16(%rsp)
   pvom
           %rbp, -8(%rsp)
   pvom
           $16, %rsp
   subq
   movslq %esi,%rax
           8(%rdi,%rax,8), %rbx
   leag
           (%rdi,%rax,8), %rbp
   leaq
   movq
           %rbx, %rsi
           %rbp, %rdi
   movq
   call
           swap
           (%rbx), %rax
   movq
   imulq
           (%rbp), %rax
   addq
           %rax, sum(%rip)
   movq
           (%rsp), %rbx
   movq
           8(%rsp), %rbp
   addq
           $16, %rsp
   ret
```

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### **Understanding x86-64 Stack Frame**

```
swap ele su:
   movq
           %rbx, -16(%rsp)
                                    # Save %rbx
           %rbp, -8(%rsp)
                                    # Save %rbp
   movq
   subq
           $16, %rsp
                                    # Allocate stack frame
                                    # Extend i
   movslq %esi,%rax
           8(%rdi,%rax,8), %rbx
                                    # &a[i+1] (callee save)
   leaq
           (%rdi,%rax,8), %rbp
                                    # &a[i]
                                               (callee save)
   leaq
                                    # 2<sup>nd</sup> argument
           %rbx, %rsi
   pvom
                                    # 1st argument
           %rbp, %rdi
   pvom
   call
           swap
   movq
           (%rbx), %rax
                                    # Get a[i+1]
   imulq
           (%rbp), %rax
                                    # Multiply by a[i]
   addq
           %rax, sum(%rip)
                                    # Add to sum
   movq
           (%rsp), %rbx
                                    # Restore %rbx
   movq
           8(%rsp), %rbp
                                    # Restore %rbp
                                    # Deallocate frame
   addq
           $16, %rsp
   ret
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```

### **Understanding x86-64 Stack Frame**

```
movq
        %rbx, -16(%rsp)
                                # Save %rbx
                                                      rtn addr
        %rbp, -8(%rsp)
                                # Save %rbp
movq
                                                       %rbp
                                                   -8
                                                       %rbx
                                                  -16
        $16, %rsp
                                # Allocate stack frame
subq
                                                      rtn addr
                                                       %rbp
                                                       %rbx
                                              %rsp •
                                # Restore %rbx
movq
        (%rsp), %rbx
        8(%rsp), %rbp
                                # Restore %rbp
movq
addq
        $16, %rsp
                                # Deallocate frame
```

### **Interesting Features of Stack Frame**

#### Allocate entire frame at once

- All stack accesses can be relative to %rsp
- · Do by decrementing stack pointer
- Can delay allocation, since safe to temporarily use red zone

#### Simple deallocation

- · Increment stack pointer
- · No base/frame pointer needed

# x86-64 Procedure Summary

#### Heavy use of registers

- Parameter passing
- More temporaries since more registers

#### Minimal use of stack

- Sometimes none
- Allocate/deallocate entire block

#### Many tricky optimizations

- What kind of stack frame to use
- Various allocation techniques